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# Specification for Approval

Customer:	
Model Name:	

Sı	Customer approval		
R&D Designed	R&D Approved	QC Approved	
Peter	Peng Jun		

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# **Revision Record**

REV NO.	REV DATE	CONTENTS	Note
Α	2021-08-13	NEW ISSUE	

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#### 1. Scope

This specification defines general provisions as well as inspection standards for TFT module supplied by AMSON electronics.

If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution.

#### 2. General Information

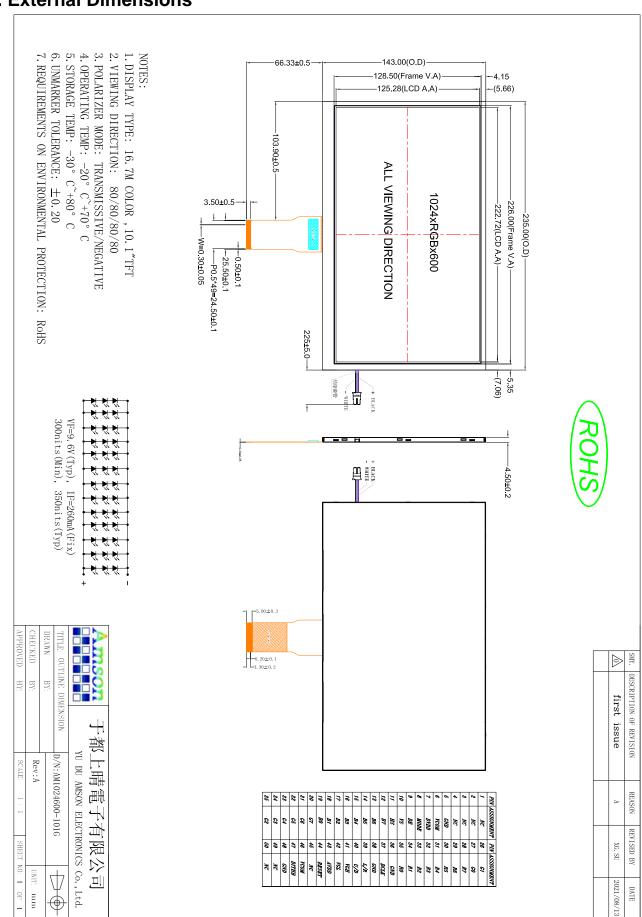
ITEM	STANDARD VALUES	UNITS
LCD type	10.1"TFT	
Dot arrangement	1024×3 (RGB)×600	dots
Color filter array	RGB vertical stripe	
Display mode	Normally BLACK	
Viewing Direction	ALL VIEWING	
Module size	$235(W) \times 143(H) \times 4.5(T)$	mm
Active area	222.72(W)×125.28(H)	mm
Dot pitch	0.2175(W)×0.2088(H)	mm
Interface	TTL	
Operating temperature	-20 ~ +70	°C
Storage temperature	-30 ~ +80	°C



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#### 3. External Dimensions





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4. Interface Description

	race Descr	•
PIN	PIN NAME	DESCRIPTION
1~2	NC	Not connect.
3~4	NC	Not connect.
5	GND	Power ground
6	VCOM	Common Voltage.
7	DVDD	Digital Power.
8	MODE	DE/SYNC mode select. Normally pull high. H: DE mode. L: SYNC mode.
9	DE	Data Enable signal.
10	VS	Vertical sync input. Negative polarity.
11	HS	Horizontal sync input. Negative polarity.
12	B7	Blue Data Input (MSB).
13~18	B6~B1	Blue Data Input.
19	В0	Blue Data Input (LSB).
20	G7	Green Data Input (MSB).
21~26	G6~G1	Green Data Input.
27	G0	Green Data Input (LSB).
28	R7	Red Data Input (MSB).
29~34	R6	Red Data Input.
35	R0	Red Data Input (LSB).
36	GND	Power ground.
37	DCLK	Clock input.
38	GND	Power ground.
39	L/R	Left/Right scan setting. Normally pull high.When L/R=L, reverse scan. When L/R=H, normal scan. (Default)
40	U/D	Up/down scan setting. Normally pull low. When U/D=H, reverse scan. When U/D=L, normal scan. (Default)
41	VGH	Positive Power for TFT Gate on.
42	VGL	Negative Power for TFT Gate off.
43	AVDD	Analog Power Supply.
44	RESET	Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10K $\Omega$ , C=1 $\mu$ F)
45	NC.	Not connect.
46	VCOM	Common Voltage.
47	DITHB	Dithering function enable control. (Normally pull high) DITHB="L", to enable internal dithering function. DITHB="H", to disable internal dithering function.
48	GND	Power ground.
40	· ·	
49	NC.	Not connect.



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5. Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit
Digital Supply Voltage	DVDD	-0.5	5	V
Analog Supply Voltage	AVDD	-05	15	V
Gate On Voltage	VGH	-0.5	40	V
Gate Off Voltage	VGL	-20	0.3	V
Operating Temperature	Тор	-20	70	°C
Storage Temperature	Тѕт	-30	80	°C
Storage Humidity	HD	10	90	%RH

#### 6. DC Characteristics

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Digital Supply Voltage	DVDD	3.0	3.3	3.6	٧	-
Analog Supply Voltage	AVDD	11.7	12.2	12.7	V	-
Gate On Voltage	VGH	19.0	22.0	25.0	V	-
Gate Off Voltage	VGL	-13.0	-10.0	-7.0	V	-
Common Voltage	VCOM	4.39	5.39	6.39	V	NOTE1
Logic Input Voltage	VIH	0.7DVDD	-	DVDD	V	-
Logic Input Voltage	VIL	GND	1	0.3DVDD	٧	1

NOTE1: VCOM 电压根据客户主板实际效果而定

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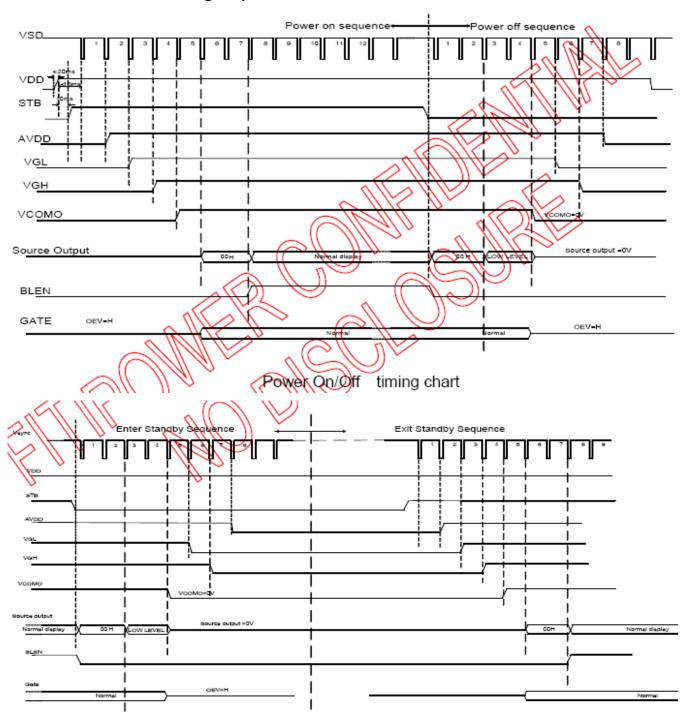
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#### 7. Timing Characteristics

### 7.1 Power ON/OFF Sequence

In order to prevent IC from power on reset fail, the rising time (TPOR) of the digital power supply VDD should be maintained within the given specifications. Refer to "AC Characteristics" for more detail on timing.

#### 7.1.1 Power on/off timing sequence



Enter and Exit Standby Mode timing chart

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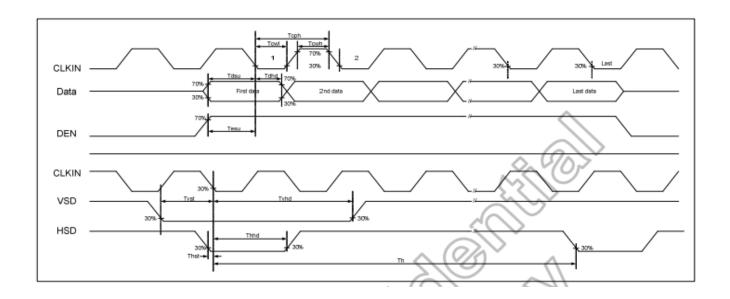
# 7.2 AC Electrical Characteristics TTL mode AC electrical characteristics

 $(TA = -20 \text{ to } 85^{\circ}C, VDD = 2.3 \text{ to } 3.6V, AVDD = 8 \text{ to } 13.5V, GND = AGND = 0V)$ 

TTL mode

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
VDD Power On Slew rate	TPOR	From 0V to 90% VDD	1	-	20	ms
RST pulse width	TRST	DCLK = 65MHz	50	-	-	us
DCLK cycle time	Tcph	-	14	-	-	ns
DCLK pulse duty	Tcwh	-	40	50	60	%
VSD setup time	Tvst	-	2/\5/\\		-	ns
VSD hold time	Tvhd	-	1/6 //	- 0	-	ns
HSD setup time	Thst	-	1/2	-	-	ns
HSD hold time	Thhd	-	5	-	-	ns
Data set-up time	Tdsu	D0[7:0], D1[7:0], D2[7:0] to DCLK	5	-	-	ns
Data hold time	Tdhd	D0[7:0], D1[7:0], D2[7:0] to DCLK	5	-	-	ns
DE setup time	Tesu		\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	-	-	ns
DE hold time	Tehd	- Jag " 11/2	15	-	-	ns
Output stable time	Tsst	Dual gate	-	-	3	us

### Input clock and data timing diagram



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### 7.3 TTL mode data input format

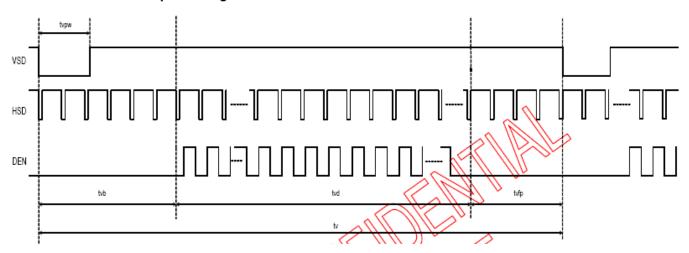
(TA = -20 to 85°C, VDD = 2.3 to 3.6V, AVDD = 8 to 13.5V, GND = AGND = 0V)

TTL mode

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
VDD Power On Slew rate	TPOR	From 0V to 90% VDD	1	-	20	ms
RST pulse width	TRST	DCLK = 65MHz	50	-	-	us
DCLK cycle time	Tcph	-	14	-	-	ns
DCLK pulse duty	Tcwh	-	40	50	60	%
VSD setup time	Tvst	- /	2/\5/\\		-	ns
VSD hold time	Tvhd	-	1/5	\ <u>-</u>	-	ns
HSD setup time	Thst	-	1/2	-	-	ns
HSD hold time	Thhd	-	5	-	-	ns
Data set-up time	Tdsu	D0[7:0], D1[7:0], D2[7:0] to DCLK	5	-	-	ns
Data hold time	Tdhd	D0[7:0], D1[7:0], Q2[7:0] to DCLK	5	-	-	ns
DE setup time	Tesu	- All and	\\\\^5_\	-	-	ns
DE hold time	Tehd	- Jag " Jag	N.	-	-	ns
Output stable time	Tsst	Dual gate \	-	-	3	us

### 7.4 Parallel RGB input timing table

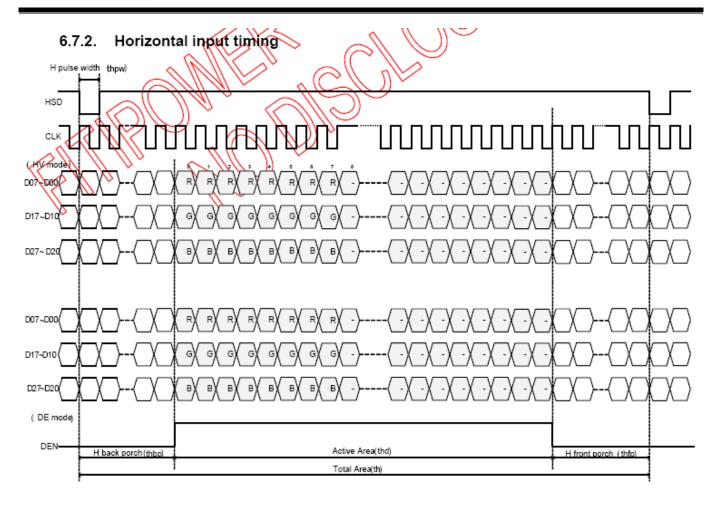
#### 6.7.1. Vertical input timing





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## DE mode

DE mode						
Parameter	Symbol		Unit			
Farameter	Syllibol	Min.	Тур.	Max.	Offic	
DCLK frequency @Frame rate=60hz	fclk	40.8	51.2	67.2	Mhz	
Horizontal display area	thd		1024		DCLK	
HSYNC period time	th	1114	1344	1400	DCLK	
HSYNC blanking	thb+thfp	90	320	376	DCLK	
Vertical display area	tvd		(600/	111	Н	
VSYNC period time	tv	610	11/63/2 11	800	Н	
VSYNC blanking	tvb+tvfp	10	85	200	Н	



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W				
MA)			>	
Symbol		Value		Unit
thd		1024		DCLK
falls \	Min.	Тур.	Max.	
2 (ICIK)	44.9	51.2	63	Mhz
th	1200	1344	1400	
0		1		
thpw		_		DOLK
		140		DCLK
thbp	160	160	160	
thfp	16	160	216	
	thd fclk th thpw	thd Min. fclk 44.9 th 1200 thpw thbp 160	thd 1024  Min. Typ. 44.9 51.2  th 1200 1344  thpw —  140  thbp 160 160	thd 1024  Min. Typ. Max.  44.9 51.2 63  th 1200 1344 1400  thpw —  140  thbp 160 160 160

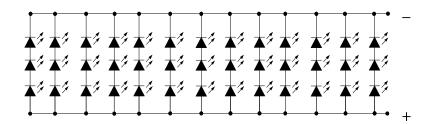
#### HV mode(2)

Vertical input timing						
Donocator	Cumphal	Value			l limit	
Parameter	Symbol	Min.	Тур.	Max.	Unit	
Vertical display area	tvd		600		Н	
VSYNC period time	tv	624	635	750	Н	
VSYNC pulse width	tvpw	1	_	20	Н	
VSYNC back porch	tvb	23	23	23	Н	
VSYNC front porch	tvfp	1	12	127	Н	

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### 8. Backlight Characteristic



Item	Symbol	MIN	TYP	MAX	UNIT	Test Condition	
Supply Voltage	Vf	9	9.6	10.5	V	If=260mA	
Supply Current	If	-	260	-	mA	If=260mA	
Luminous Intensity for LCM	-	300	350	-	cd/m <sup>2</sup>	If=260mA	
Uniformity for LCM	-	75	-	-	%	If=260mA	
Life Time	-	30000	-	-	Hr	If=260mA	



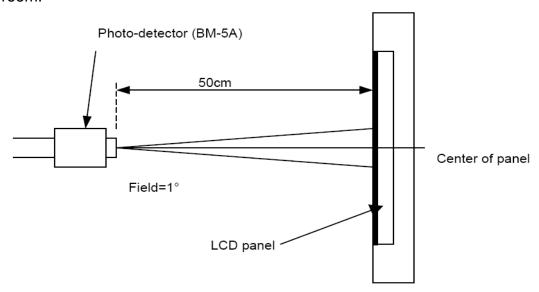
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9. Optical Characteristics

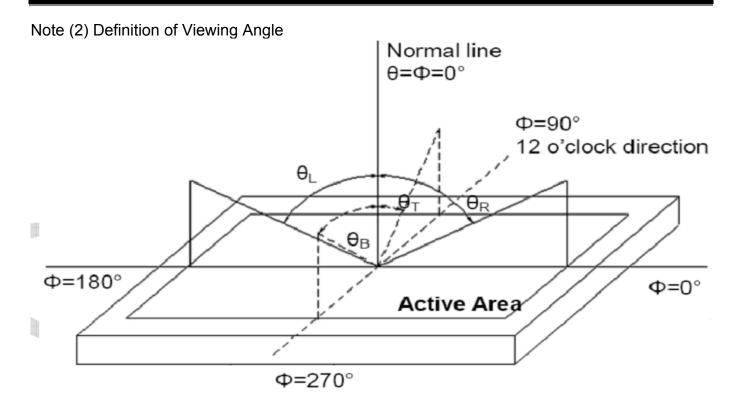
Item	Conditions		Min.	Тур.	Max.	Unit	Note	
	Horizontal	θL	80	85	-	degree		
Viewing Angle	Honzontai	θR	80	85	-		(1),(2),(6)	
(CR>10)	Vertical	θт	80	85	-			
	verticai	θв	80	85	-			
Contrast Ratio	Center		600	800	-	-	(1),(3),(6)	
Response Time	Rising + Falling		-	30	45	ms	(1),(4),(6)	
	Red x			0.601		-		
	Red y			0.328		-		
	Green x			0.288	Typ. +0.02	-	(4) (6)	
CF Color	Green y		Тур.	0.517		-		
Chromaticity (CIE1931)	Blue x		-0.02	0.150		-	(1), (6)	
(3.2.33.7)	Blue y	Blue y White x		0.144		-		
	White x			0.302		-		
	White y			0.324		-		

Note (1) Measurement Setup: The LCD module should be stabilized at given temp. 25°C for 15 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 15 minutes in a windless room.



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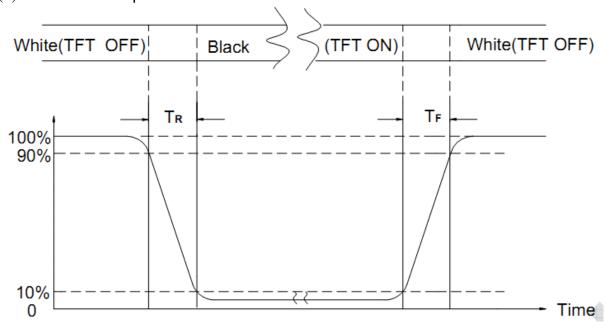


Note (3) Definition of Contrast Ratio (CR)

The contrast ratio can be calculated by the following expression Contrast Ratio (CR) = L63 / L0

L63: Luminance of gray level 63, L0: Luminance of gray level 0

Note (4) Definition of response time



Note (5) Definition of Transmittance (Module is without signal input)

Transmittance = Center Luminance of LCD / Center Luminance of Back Light x 100%

Note (6) Definition of color chromaticity (CIE1931)

Color coordinates measured at the center point of LCD



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10. Reliability Test Conditions and Methods

NO.	TEST ITEMS	TEST CONDITION			
1	High Temperature Storage	Keep in 80°C ±5°C 240 hrs Surrounding temperature, then storage at normal condition 4hrs.			
2	Low Temperature Storage	Keep in -30°C ±5°C 240 hrs Surrounding temperature, then storage at normal condition 4hrs.			
3	High Temperature / High Humidity Storage Test	Keep in 60 ℃ / 90% R.H duration for 240 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)			
4	Temperature Cycling Storage Test	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$			
(5)	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/-  1. Temperature ambiance: $15^{\circ}\text{C} \sim 35^{\circ}\text{C}$ 2. Humidity relative: $30\% \sim 60\%$ 3. Energy Storage Capacitance( Cs + Cd ): $150\text{pF}\pm10\%$ 4. Discharge Resistance(Rd): $330\Omega\pm10\%$			
		5. Discharge, mode of operation : Single Discharge (time between successive discharges a 1 sec) (Tolerance if the output voltage indication :			
6	Vibration Test (Packaged)	<ol> <li>Sine wave 10~55 Hz frequency (1 min/sweep)</li> <li>The amplitude of vibration :1.5 mm</li> <li>Each direction (X、Y、Z) duration for 2 Hrs</li> </ol>			
7	Drop Test (Packaged)	Packing Weight (Kg)  0 ~ 45.4  45.4 ~ 90.8  90.8 ~ 454  Over 454	Drop Height (cm)  122  76  61  46		
		Drop Direction: **1 corner / 3 edges / 6 sides each 1time			



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#### 11. Inspection Standard

#### 11.1. QUALITY:

THE QUALITY OF GOODS SUPPLIED TO PURCHASER SHALL COME UP TO THE FOLLOWING STANDARD.

#### 11.1.1. INSPECTIONTOOLS AND INSTRUMENTS

Vernier calipers, film scales, multimeter, magnifying eyepiece, ND5%, luminance meter and so on.

#### 11.1.2. THE METHOD OF PRESERVING GOODS

AFTER DELIVERY OF GOODS FROM AMSON TO PURCHASER. PURCHASER SHALL CONTROL THE LCM AT -10 TO 40 ,AND IT MIGHT BE DESIRABLE TO KEEP AT THE NORMAL ROOM TEMPERATURE AND HUMIDITY UNTIL INCOMING INSPECTION OR THROWING INTO PROCESS LINE.

#### 11.1.3. INCOMING INSPECTION

(A) THE METHOD OF INSPECTION

IF PURCHASER MAKE AN INCOMING INSPECTION, A SAMPLING PLAN SHALL BE APPLIED ON THE CONDITION THAT QUALITY OF ONE DELIVERY SHALL BE REGARDED AS ONE LOT.

(B) THE STANDARD OF QUALITY

ISO-2859-1 (SAME AS MIL-STD-105E), LEVEL: II

CLASS	AQL(%)
CRITICAL	0.4 %
MAJOR	0.65 %
MINOR	1.5 %

EVERY ITEM SHALL BE INSPECTED ACCORDING TO THE CLASS.

(C) MEASURE

IF AS THE RESULT OF ABOVE RECEIVING INSPECTION, A LOT OUT IS DISCOVERED. PURCHASER SHALL BE INFORM SELLER OF IT WITHIN SEVEN DAYS. BUT FIRST SHIPMENT WITHIN FOURTEEN DAYS.

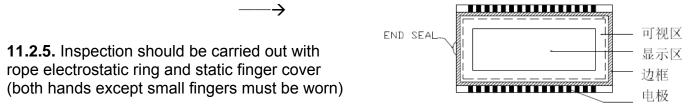
#### 11.1.4. WARRANTY POLICY

AMSON WILL PROVIDE ONE-YEAR WARRANTY FOR THE PRODUCTS ONLY IF UNDER SPECIFICATION OPERATING CONDITIONS. AMSON WILL REPLACE NEW PRODUCTS FOR THESE DEFECT PRODUCTS WHICH UNDER WARRANTY PERIOD AND BELONG TO THE RESPONSIBILITY OF AMSON.

#### 11.2. CHECKING CONDITION

- **11.2.1.**CHECKING DIRECTION SHALL BE IN THE 45 DEGREE AREA TO FACE THE SAMPLE.
- **11.2.2.**CHECKER SHALL SEE OVER 300±25 mm. WITH BARE EYES FAR FROM SAMPLE **11.2.3.**Ambient Illumination:
  - 0 ~30 Lux for functional inspection
    - 500 ~ 1200 Lux for external appearance inspection.

#### **11.2.4.** TEST AREA:



11.2.6. The inspector may make a visual inspection or a comparative examination with a film



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ruler and a magnifying eyepiece. Individual defects shall be determined according to the limited samples.

- **11.2.7.** Functional testing uses electrical testing fixtures or test fixtures required by customers.
- **11.2.8.** the ion fan should be used when testing.

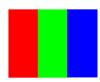
#### 11.2.9. the principle of judgment

11.3.1 If the defect outside the visual area does not affect the assembly and display, it will be judged as a good product.

#### 11.3.2 Poor definition

Pixel:

A combination of three sub-pixels (Red + Green + Blue).



#### Dot:

Any of the sub-pixels (Red or Green or Blue).





#### **Bright and dark dots:**

A point pixel (sub-pixel: R, G, B pixels) is lit or turned off during the display function test. **Highlights**:

Usually considered to be shown on a black screen.

#### Dark spots:

They are generally considered to be shown on R, G, B solid colors or white images.

#### Neighborhood:

Two or three adjacent point pixels (dot: sub-pixel) connected together (R, G or G, B or B, R or RGB).



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### 11.3. INSPECTION PLAN:

11.0. 11401 E0	TION PLAN :		
CLASS	ITEM	JUDGEMENT	CLASS
PACKING &	1. OUTSIDE AND INSIDE PACKAGE	"MODEL NO." , "LOT NO." AND "QUANTITY" SHOULD INDICATE ON THE PACKAGE.	Minor
INDICATE	2. MODEL MIXED AND QUANTITY	OTHER MODEL MIXEDREJECTED QUANTITY SHORT OR OVERREJECTED	Critical
	3. PRODUCT INDICATION	"MODEL NO." SHOULD INDICATE ON THE PRODUCT	Major
ASSEMBLY	4. DIMENSION, LCD GLASS SCRATCH AND SCRIBE DEFECT.	ACCORDING TO SPECIFICATION OR DRAWING.	Major
	5. VIEWING AREA	POLARIZER EDGE OR LCD'S SEALING LINE IS VISABLE IN THE VIEWING AREAREJECTED	Minor
	6. BLEMISH • BLACK SPOT • WHITE SPOT IN THE LCD AND LCD GLASS CRACKS	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
APPEARANCE	7. BLEMISH - BLACK SPOT WHITE SPOT AND SCRATCH ON THE POLARIZER	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
	8. BUBBLE IN POLARIZER	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
	9. LCD'S RAINBOW COLOR	STRONG DEVIATION COLOR ( OR NEWTON RING) OF LCDREJECTED.  OR ACCORDING TO LIMITED SAMPLE ( IF NEEDED, AND INSIDE VIEWING AREA )	Minor
	10. ELECTRICAL AND OPTICAL CHARACTERISTICS ( CONTRAST, VOP, CHROMATICITY ETC )	ACCORDING TO SPECIFICATION OR DRAWING . ( INSIDE VIEWING AREA )	Critical
ELECTRICAL	11.MISSING LINE	MISSING DOT: LINE : CHARACTERREJECTED	Critical
	12.SHORT CIRCUIT WRONG PATTERN DISPLAY	NO DISPLAY - WRONG PATTERN DISPLAY - CURRENT CONSUMPTION OUT OF SPECIFICATION REJECTED	Critical
	13. DOT DEFECT (FOR COLOR AND TFT)	ACCORDING TO STANDARD OF VISUAL INSPECTION	Minor



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NO.	CLASS	ITEM	JUDGEMENT				
			(A) ROUND TYPE: unit : mm.				
		DIAMETER (mm.) ACC	EPTABLE Q'TY				
			Φ ≤ 0.15	Distance≥1mm			
		DI AOK AND WHITE ODOT	0.15 < Φ ≤ 0.4	3 (Distance>15mm)			
		BLACK AND WHITE SPOT FOREIGN MATERIEL	0.4 < Φ	0			
141	MINOR	DUST IN THE CELL	NOTE: $\Phi = (\text{LENGTH+WIDTH})/2$				
1.4.1	WIINOK	BLEMISH	(B) LINEAR TYPE:	unit: mm.			
		SCRATCH	LENGTH WIDTH	ACCEPTABLE Q'TY			
		33.21.3.1	W ≦0.0	3 Distance≥1mm			
			L ≤ 4.0 0.03 < W ≤ 0.0	5 3 (Distance>15mm)			
			0.05 < W	FOLLOW ROUND TYPE			
_				unit : mm.			
			DIAMETER AG	CCEPTABLE Q'TY			
		BUBBLE IN POLARIZER	Φ ≤ 0.2	Distance≥1mm			
1.4.2	MINOR	The state of the s		3 (Distance>15mm)			
			0.5 < Ф	0			
		Dot Defect		ACC. Q'TY ≤2 (Distance≥15mm) ≤3 (Distance≥15mm)			
11.4.3	MINOR		Pixel Define : Pixel  Dot Pot Dot  Note 1: The definition of dot: The size 1/2 of whole dot is regarded Definittion:<1/2dot and visit Note 2: Bright dot: Dots appear bright in which LCD panel is display Note 3: Dark dot: Dots appear dark at which LCD panel is displaying, blue pattern.	e of a defective dot over as one defective dot. ble by 5 % ND filter N ≤ 5 at and unchanged in size by 10 ying under black pattern.			
1,4,4	MINOR	Mura	Not visible thriugh 5% ND filter in by limit sample if necessary	50% gray or judge			



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NO.	CLASS	ITEM	JUDGEMENT
11.4.5	MINOR	LCD GLASS CHIPPING	X ≥ 3mm Y > S Reject
11.4.6	MINOR	LCD GLASS CHIPPING	X or Y > S Reject
11.4.7	MAJOR	LCD GLASS GLASS CRACK	Continuous burst NG Reject
11.4.8	MAJOR	LCD GLASS SCRIBE DEFECT	ACCORDING TO DIMENSION
11.4.9	MINOR	LCD GLASS CHIPPING ( ON THE TERMINAL AREA )	Y<1/2Z $Y \ge 0.5 \text{mm}_{\text{Reject}}$ $X \ge 3 \text{mm}$
11.4.10	MINOR	LCD GLASS CHIPPING ( ON THE TERMINAL SURFACE )	$Y<1/2Z$ $Y \ge 0.5 mm$ $X \ge 3 mm$
11.4.11	MINOR	LCD GLASS CHIPPING	$X\geqslant 3mm$ $Y\geqslant T\qquad \text{Reject}$ $Z\qquad \text{If touch the electrode lines,}$ the need to retain the two-thirds electrode lines



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### 12. Handling Precautions

#### 12.1 Mounting method

The LCD panel of AMSON TFT module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

#### 12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (CI), Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

#### 12.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to power or ground, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

#### 12.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

#### 12.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.
  - Usage under the maximum operating temperature, 50%Rh or less is required.



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#### 12.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
   [It is recommended to store them as they have been contained in the inner container at the time of delivery from us.

#### 12.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

#### 13. Precaution for Use

#### 13.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

#### 13.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification.
- When a new problem is arisen this is not specified in this specification.
- When an inspection specifications change or operating condition change in customer is reported to AMSON TFT and some problem is arisen in this specification due to the change.
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

# 14. Packing Method TBD