



Specification for Approval

Customer: _____

Model Name: _____

Supplier Approval			Customer approval
R&D Designed	R&D Approved	QC Approved	
<i>Peter</i>	<i>Peng Jun</i>		

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1. Scope

This specification defines general provisions as well as inspection standards for TFT module supplied by AMSON electronics.

If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution.

2. General Information

LCM

TITEM	STANDARD VALUES	UNITS
LCD type	7.0" TFT	--
Dot arrangement	1024(RGB)×600	dots
Color filter array	RGB vertical stripe	--
Display mode	Normally White , Transmissive	-
Gray Scale Inversion Direction	6 o'clock	--
Eyes Viewing Direction	12 o'clock	--
Module size	164.9(W)×100.0(H)×6.7(T)	mm
Active area	153.6(W)×90(H)	mm
Dot pitch	0.15(W)×0.15(H)	mm
Interface	LVDS	--
Operating temperature	-20 ~ +70	°C
Storage temperature	-30 ~ +80	°C
Back Light	30White LED	--
Weight	TBD	g

RTP

ITEM	STANDARD VALUES	UNITS
RTP type	Film + Glass + FPC	--
Surface hardness	3H	--
Transmittance	≥78%	--
RTP size	164.4 (W)×98.9 (H)×1.2(T)	mm
Active area	156.2(W)×89.24 (H)	mm
Response Time	≤10ms	ms
Linearity	≤1.5%	%
Insulation resistance	>20MΩ	MΩ
Resistance	X:200Ω ~1200Ω Y:200Ω ~ 1200Ω	Ω

4. Interface Description

PIN	PIN NAME	DESCRIPTION
1	VCOM	Common voltage
2	VDD	Power for Digital Circuit
3	VDD	Power for Digital Circuit
4	NC	No connection
5	RESET	Global reset pin
6	STBYB	Standby mode, Normally pulled high STBYB= “1” , normal operation STBYB= “0” , timing controller, source Driver will turn off, all output are High-z
7	GND	Power ground
8	RXIN0-	-LVDS differential data pair
9	RXIN0+	+LVDS differential data pair
10	GND	Power ground
11	RXIN1-	-LVDS differential data pair
12	RXIN1+	+LVDS differential data pair
13	GND	Power ground
14	RXIN2-	-LVDS differential data pair
15	RXIN2+	+LVDS differential data pair
16	GND	Power ground
17	RXCLKIN-	-LVDS differential clock pair
18	RXCLKIN+	+LVDS differential clock pair
19	GND	Power ground
20	RXIN3-	-LVDS differential data pair
21	RXIN3+	+LVDS differential data pair
22	GND	Power ground
23	NC	No connection
24	NC	No connection
25	GND	Power ground
26	NC	No connection
27	DIM0	Backlight CABC controller signal output
28	SELB	6bit/8bit mode select
29	AVDD	Power for Analog Circuit
30	GND	Power ground
31	LEDK	LED backlight (Cathode).
32	LEDK	LED backlight (Cathode).
33	L/R	Horizontal inversion
34	U/D	Vertical inversion
35	VGL	Gate OFF Voltage
36	CABCEN1	CABC H/W enable

37	CABCEN0	CABC H/W enable
38	VGH	Gate ON Voltage
39	LEDA	LED backlight (Anode).
40	LEDA	LED backlight (Anode).

Note1:

CABCEN1	CABCEN0	DESCRIPTION
L	L	CABC OFF
L	H	User interface Image
H	L	Still Picture
H	H	Moving Image

RTP

PIN NO.	PIN NAME	
1	XL	TP Left
2	YD	TP Bottom
3	XR	TP Right
4	YU	TP Up

5. Electrical specification

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Input signal Voltage	VCOM	3.3	3.8	4.3	V	-
Logic Supply Voltage	VDD	2.5	3.3	3.6	V	-
Analog Supply Voltage	AVDD	8	11	13.5	V	
Low Supply Voltage	VGL	-7.1	-6.8	-6.5	V	
High Supply Voltage	VGH	19.7	20	20.3	V	
Output High Voltage	VIH	0.7XVDD	-	VDD	V	-
Output Low Voltage	VIL	0	-	0.3xVDD	V	-

6. Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit
Logic Supply Voltage	DVDD	-0.5	5	V
Analog Supply Voltage	VDD	-0.5	15	V
High Supply Voltage	VGH	-0.3	42	V
Low Supply Voltage	VGL	-20	0.3	V
Operating Temperature	T _{OP}	-20	70	°C
Storage Temperature	T _{ST}	-30	80	°C

7. Current Consumption

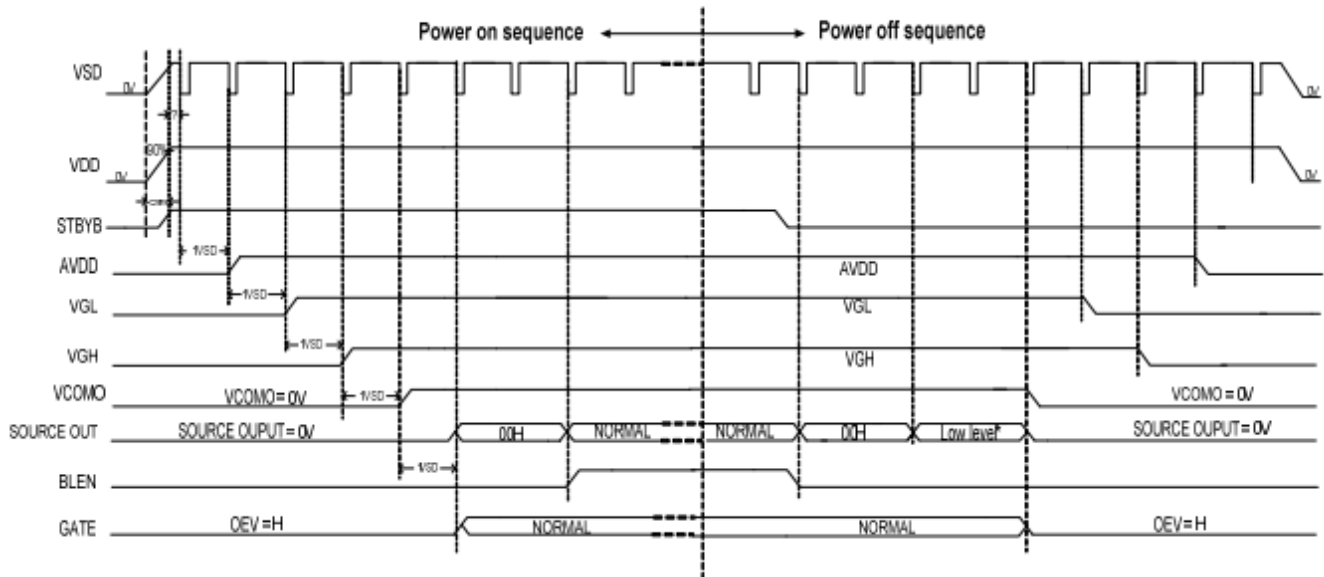
Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
High Supply for Current	IGH	-	0.2	2	mA	VGH=20V
Low Supply for Current	IGL	-	0.2	2	mA	VGL=-6.8V
Logic Supply for Current	IVDD	-	38	80	mA	VDD=3.3V
Analog Supply for Current	IAVDD	-	20	30	mA	AVDD=11V

8. Timing Characteristics

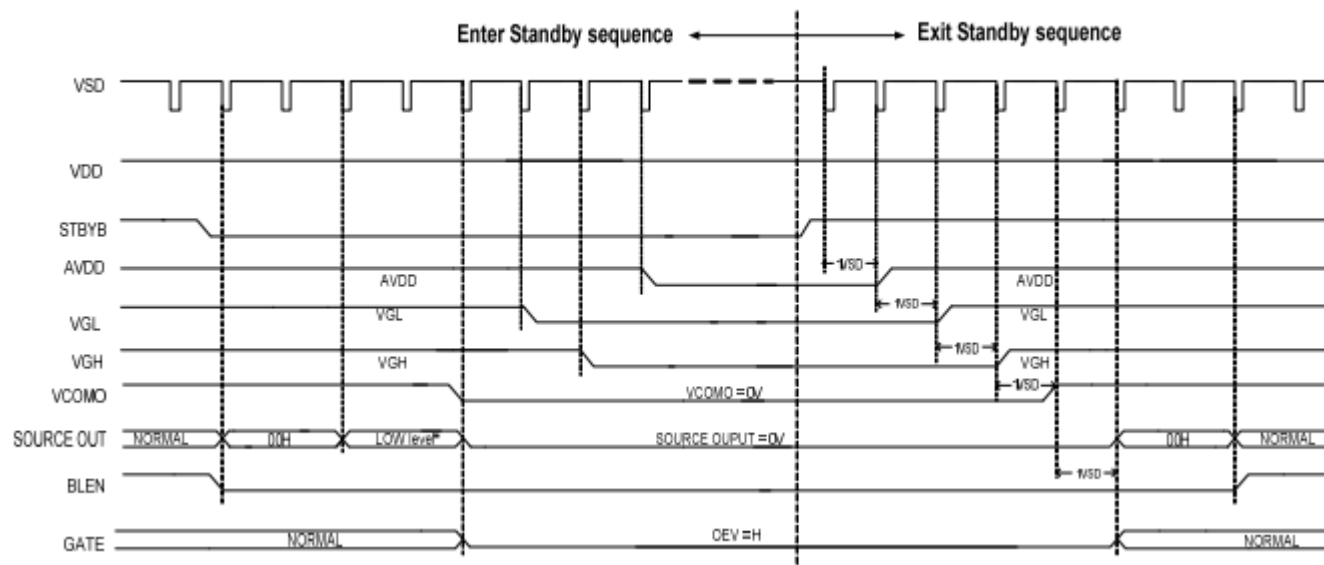
8.1 Power Sequence

In order to prevent IC from power on reset fail, the rising time (T_{POR}) of the digital power supply VDD should be maintained within the given specifications. Refer to "AC Characteristics" for more detail on timing.

Power-On/Off Timing Sequence:

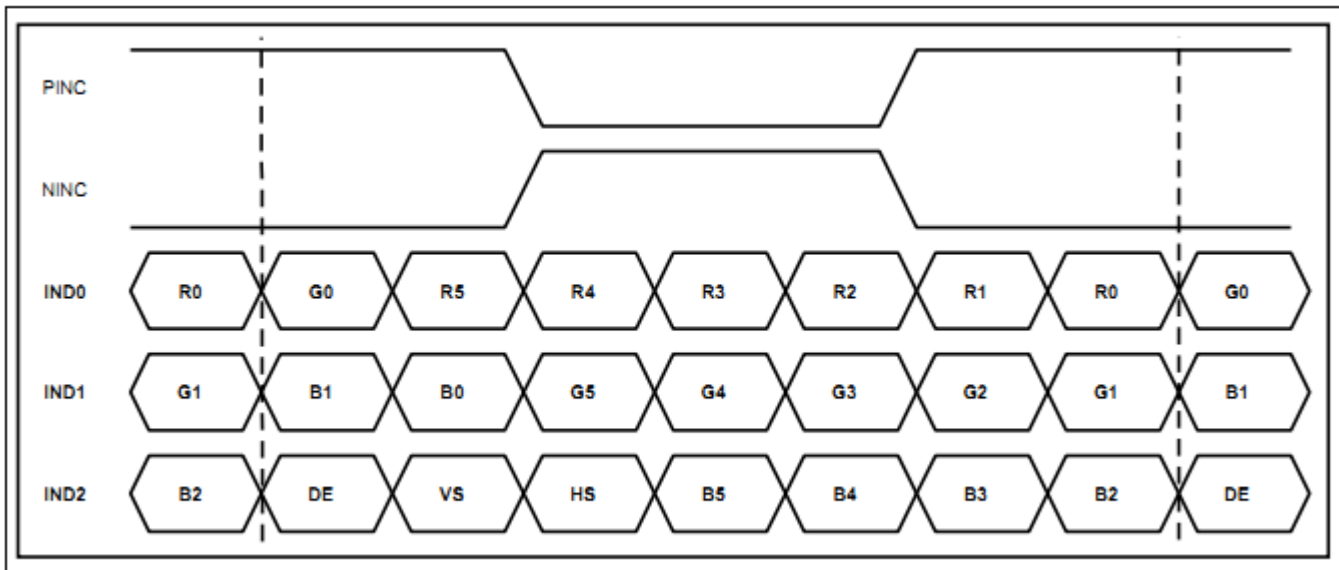


Enter and Exit Standby Mode Sequence:

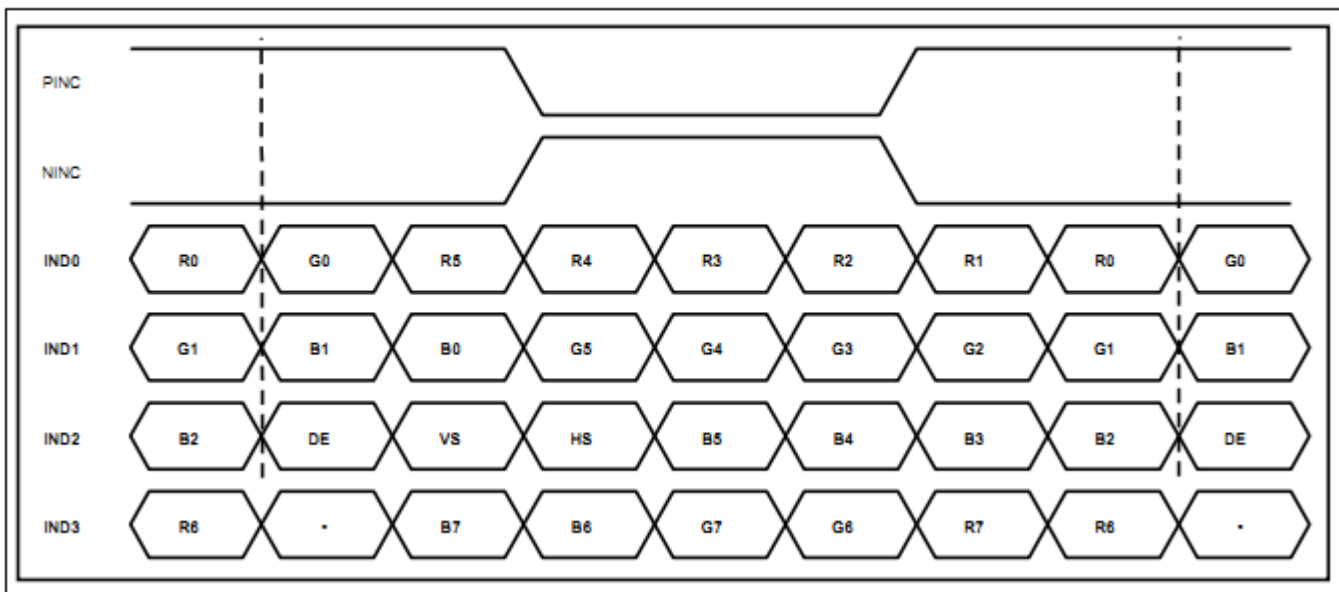


8.2 Data Input Format for LVDS

6bit LVDS input



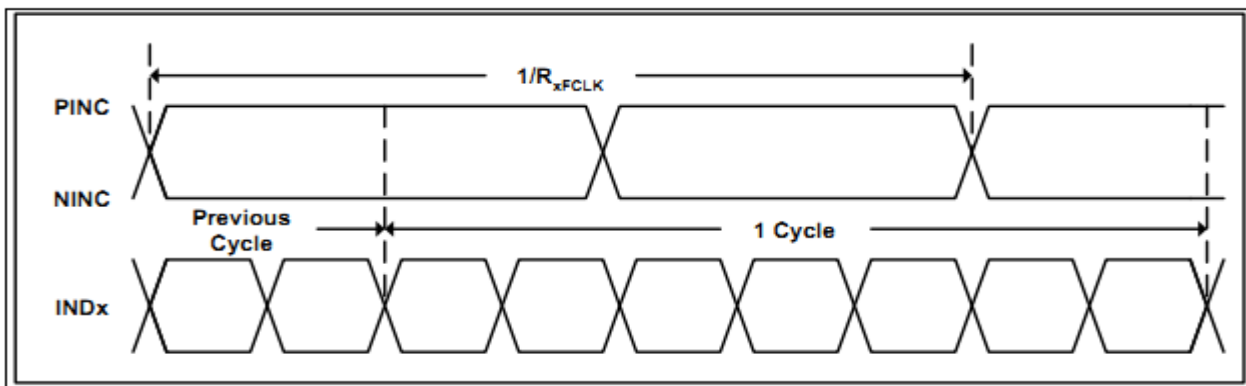
8-bit LVDS input (HSD='L')



8.3 AC Electrical Characteristics

LVDS mode

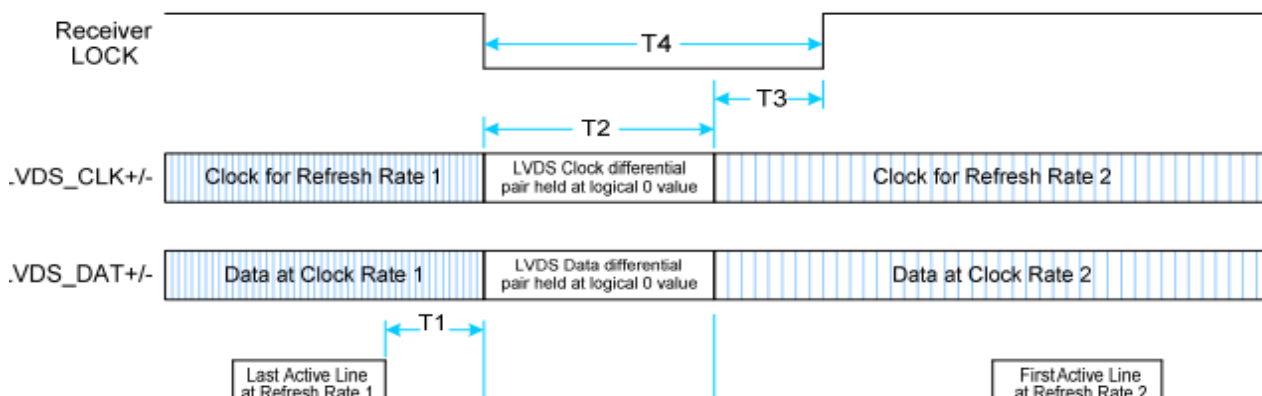
Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
Clock frequency	R_{xFCLK}	20		71	MHz	
Input data skew margin	T_{RSKM}	500			pS	$ V_{ID} = 400mV$ $R_{xVCM} = 1.2V$ $R_{xFCLK} = 71 MHz$
Clock high time	T_{LVCH}		$4/(7 * R_{xFCLK})$		ns	
Clock low time	T_{LVCL}		$3/(7 * R_{xFCLK})$		ns	
PLL wake-up time	T_{enPLL}			150	uS	



8.4 Timing

SDRRS (seamless display refresh rate switching)

When Showing the still picture, it is accept to reduce the refresh rate from 60Hz to low refresh rate (for example 40Hz). The purpose is mainly for power saving. INTEL defined a timing chart switch between different refresh rate. Following this timing chart, the switch between different refresh rates is seamless for end user.



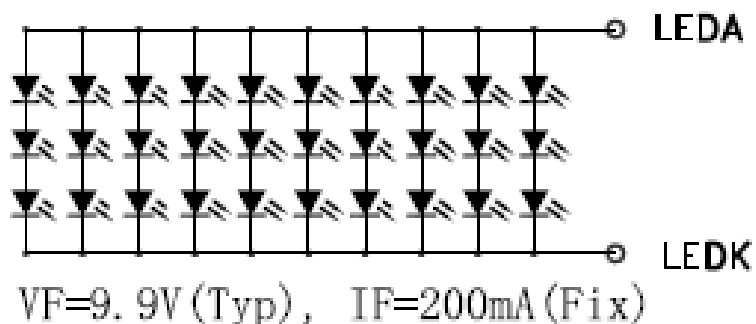
T1 - Min delay from start of vert blank to start of timing change: 2 lines (HSYNC periods)

T2 - Max delay for clock to transition to new frequency: 100us

T3 - Max receiver lock delay from stable clock: Display specific (TBD)

T4 - Max period during which panel maintains display (T2+T3): Display specific (TBD)

9. Backlight Characteristics

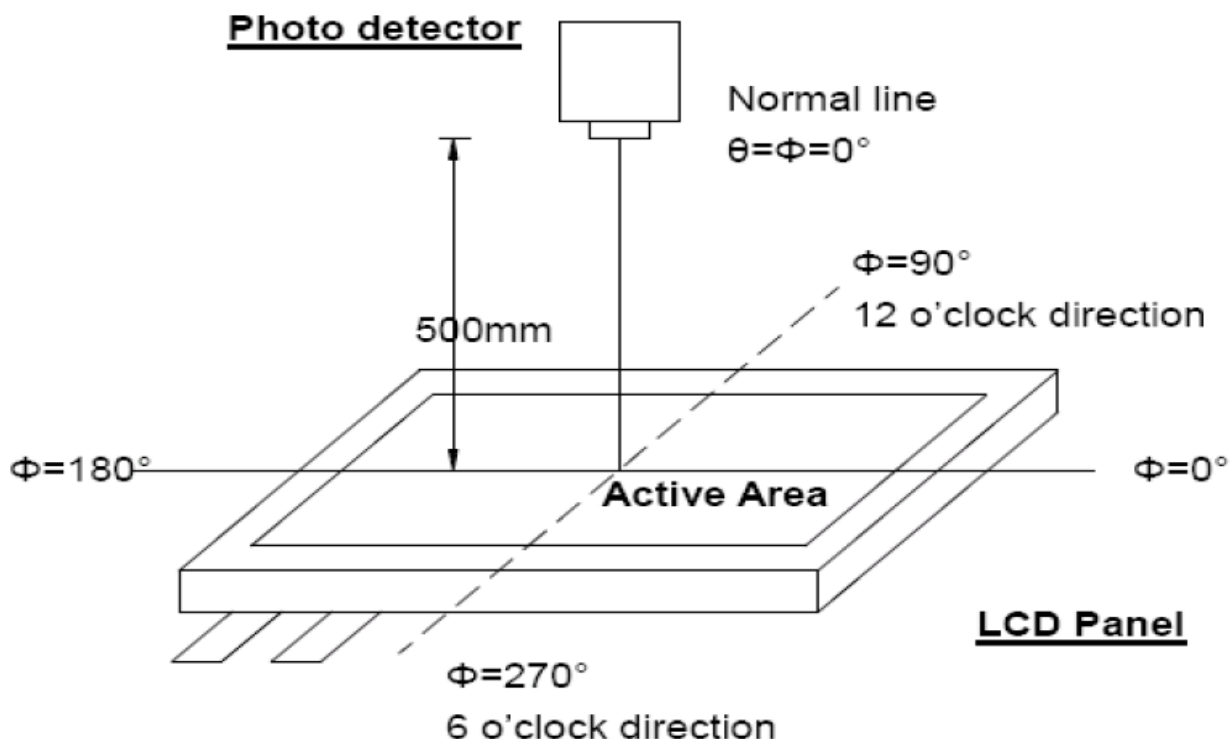


Item	Symbol	MIN	TYP	MAX	UNIT	Test Condition
Supply Voltage	V_f	8.1	9.9	10.5	V	$I_f=200mA$
Supply Current	I_f		200	-	mA	-
Luminous Intensity for LCM	-	200	300	-	cd/m^2	$I_f=200mA$
Uniformity for LCM	-	80	-	-	%	$I_f=200mA$
Life Time	-	-	50000	-	Hr	$I_f=200mA$
Backlight Color	White					

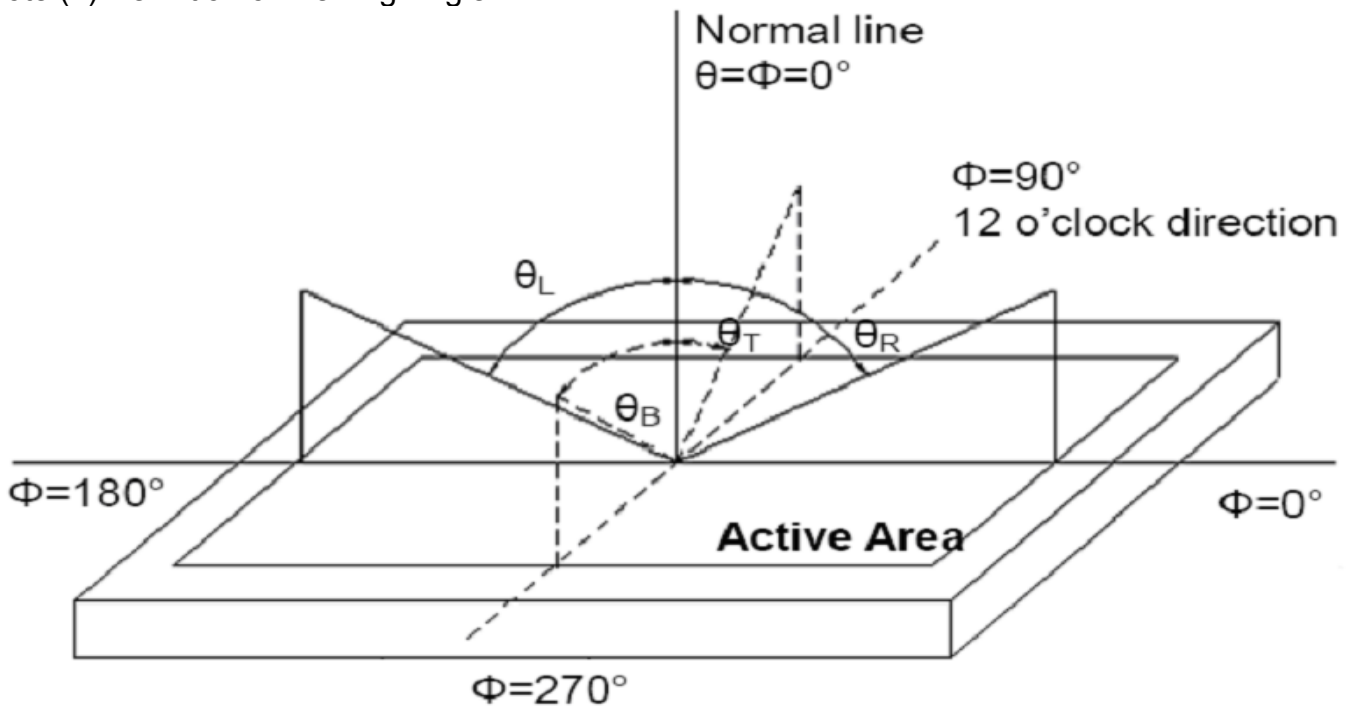
10. Optical Characteristics

Item	Conditions	Min.	Typ.	Max.	Unit	Note
Viewing Angle (CR>10)	Horizontal	θ_L	-	75	-	(1),(2),(6)
		θ_R	-	75	-	
	Vertical	θ_T	-	70	-	
		θ_B	-	75	-	
Contrast Ratio	Center	500	800	-	-	(1),(3),(6)
Response Time	Rising	-	10	20	ms	(1),(4),(6)
	Falling	-	15	30		
CF Color Chromaticity (CIE1931)	Red x	Typ. -0.05	TBD	Typ. +0.05	-	(1), (6)
	Red y		TBD		-	
	Green x		TBD		-	
	Green y		TBD		-	
	Blue x		TBD		-	
	Blue y		TBD		-	
	White x		TBD		-	
	White y		TBD		-	
Transmittance	$\theta = 0^\circ$		3.37		%	

Note (1) Measurement Setup: The LCD module should be stabilized at given temp. 25°C for 15 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 15 minutes in a windless room.



Note (2) Definition of Viewing Angle



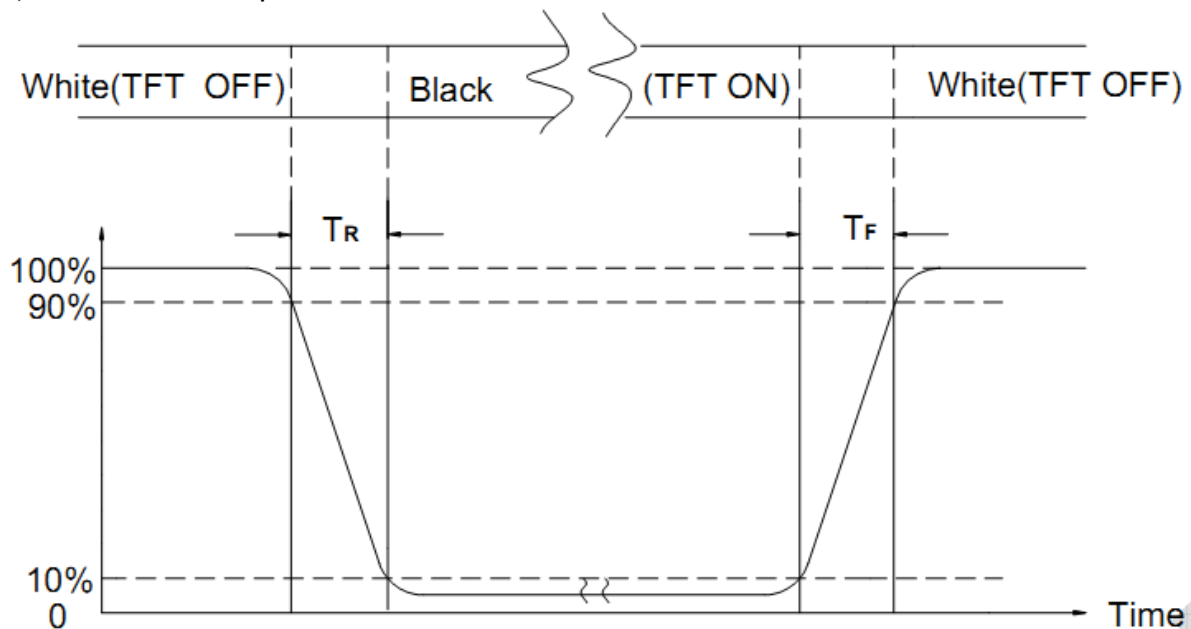
Note (3) Definition of Contrast Ratio (CR)

The contrast ratio can be calculated by the following expression

$$\text{Contrast Ratio (CR)} = L_{63} / L_0$$

L63: Luminance of gray level 63, L0: Luminance of gray level 0

Note (4) Definition of response time



Note (5) Definition of Transmittance (Module is without signal input)

$$\text{Transmittance} = \text{Center Luminance of LCD} / \text{Center Luminance of Back Light} \times 100\%$$

Note (6) Definition of color chromaticity (CIE1931)

Color coordinates measured at the center point of LCD

11. Reliability Test Conditions and Methods

NO.	TEST ITEMS	TEST CONDITION	INSPECTION AFTER TEST
<input type="checkbox"/>	High Temperature Storage	80°C±2°C×96Hours	Inspection after 2~4hours storage at room temperature, the samples should be free from defects: 1, Air bubble in the LCD. 2, Seal leak. 3, Non-display. 4, Missing segments. 5, Glass crack. 6, Current IDD is twice higher than initial value. 7, The surface shall be free from damage. 8, The electric characteristic requirements shall be satisfied.
<input type="checkbox"/>	Low Temperature Storage	-30°C±2°C×96Hours	
<input type="checkbox"/>	High Temperature Operating	70°C±2°C×96Hours	
<input type="checkbox"/>	Low Temperature Operating	-20°C±2°C×120Hours	
<input type="checkbox"/>	Temperature Cycle(Storage)	-20°C \longleftrightarrow 25°C \longleftrightarrow 70°C (30min) (5min) (30min) 1cycle Total 10cycle	
<input type="checkbox"/>	Damp Proof Test (Storage)	50°C±5°C×90%RH×96Hours	
<input type="checkbox"/>	Vibration Test	Frequency:10Hz~55Hz~10Hz Amplitude:1.5M X,Y,Z direction for total 3hours (packing condition test will be tested by a carton)	
<input type="checkbox"/>	Drooping Test	Drop to the ground from 1M height one time every side of carton. (packing condition test will be tested by a carton)	
<input type="checkbox"/>	ESD Test	Voltage:±8KV,R:330Ω,C:150PF,Air Mode,10times	

REMARK:

- 1, The Test samples should be applied to only one test item.
- 2, Sample side for each test item is 5~10pcs.
- 3, For Damp Proof Test, Pure water(Resistance > 10MΩ) should be used.
- 4, In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.
- 5, EL evaluation should be accepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.
- 6, Failure Judgment Criterion: Basic Specification Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

12. Inspection Standard

12.1. QUALITY :

THE QUALITY OF GOODS SUPPLIED TO PURCHASER SHALL COME UP TO THE FOLLOWING STANDARD.

12.1.1. THE METHOD OF PRESERVING GOODS

AFTER DELIVERY OF GOODS FROM AMSON TO PURCHASER. PURCHASER SHALL CONTROL THE LCM AT -10℃ TO 40℃ ,AND IT MIGHT BE DESIRABLE TO KEEP AT THE NORMAL ROOM TEMPERATURE AND HUMIDITY UNTIL INCOMING INSPECTION OR THROWING INTO PROCESS LINE.

12.1.2. INCOMING INSPECTION

(A) THE METHOD OF INSPECTION

IF PURCHASER MAKE AN INCOMING INSPECTION , A SAMPLING PLAN SHALL BE APPLIED ON THE CONDITION THAT QUALITY OF ONE DELIVERY SHALL BE REGARDED AS ONE LOT.

(B) THE STANDARD OF QUALITY

ISO-2859-1 (SAME AS MIL-STD-105E) , LEVEL II SINGLE PLAN.

CLASS	AQL(%)
CRITICAL	0.4 %
MAJOR	0.65 %
MINOR	1.5 %
TOTAL	1.5 %

EVERY ITEM SHALL BE INSPECTED ACCORDING TO THE CLASS.

(C) MEASURE

IF AS THE RESULT OF ABOVE RECEIVING INSPECTION , A LOT OUT IS DISCOVERED. PURCHASER SHALL BE INFORM SELLER OF IT WITHIN SEVEN DAYS. BUT FIRST SHIPMENT WITHIN FOURTEEN DAYS.

12.1.3. WARRANTY POLICY

AMSON WILL PROVIDE ONE-YEAR WARRANTY FOR THE PRODUCTS ONLY IF UNDER SPECIFICATION OPERATING CONDITIONS. AMSON WILL REPLACE NEW PRODUCTS FOR THESE DEFECT PRODUCTS WHICH UNDER WARRANTY PERIOD AND BELONG TO THE RESPONSIBILITY OF AMSON.

12.2. CHECKING CONDITION

12.2.1. CHECKING DIRECTION SHALL BE IN THE 45 DEGREE AREA TO FACE THE SAMPLE.

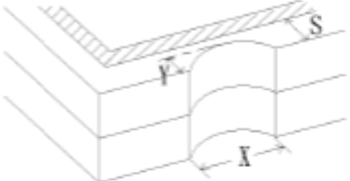
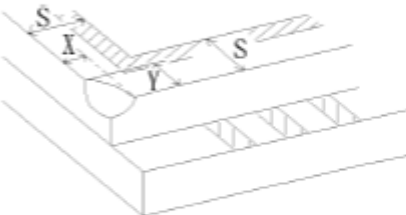
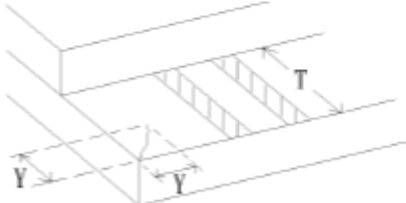
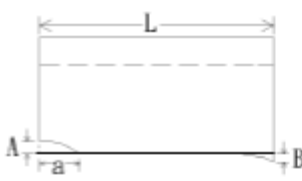
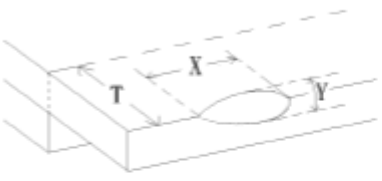
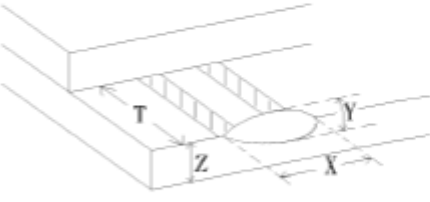
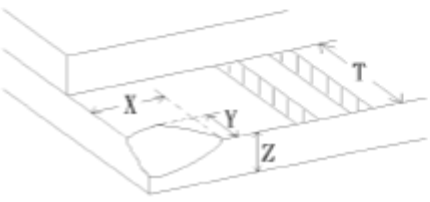
12.2.2. CHECKER SHALL SEE OVER 300±25 mm. WITH BARE EYES FAR FROM SAMPLE AND USING 2 PCS. OF 20W FLUORESCENT LAMP.

12.3. INSPECTION PLAN :

CLASS	ITEM	JUDGEMENT	CLASS
PACKING & INDICATE	1. OUTSIDE AND INSIDE PACKAGE	"MODEL NO." , "LOT NO." AND "QUANTITY" SHOULD INDICATE ON THE PACKAGE.	Minor
	2. MODEL MIXED AND QUANTITY	OTHER MODEL MIXED.....REJECTED QUANTITY SHORT OR OVER.....REJECTED	Critical
	3. PRODUCT INDICATION	"MODEL NO." SHOULD INDICATE ON THE PRODUCT	Major
ASSEMBLY	4. DIMENSION, LCD GLASS SCRATCH AND SCRIBE DEFECT.	ACCORDING TO SPECIFICATION OR DRAWING.	Major
APPEARANCE	5. VIEWING AREA	POLARIZER EDGE OR LCD'S SEALING LINE IS VISABLE IN THE VIEWING AREAREJECTED	Minor
	6. BLEMISH · BLACK SPOT · WHITE SPOT IN THE LCD AND LCD GLASS CRACKS	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
	7. BLEMISH · BLACK SPOT WHITE SPOT AND SCRATCH ON THE POLARIZER	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
	8. BUBBLE IN POLARIZER	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
	9. LCD'S RAINBOW COLOR	STRONG DEVIATION COLOR (OR NEWTON RING) OF LCD.....REJECTED. OR ACCORDING TO LIMITED SAMPLE (IF NEEDED, AND INSIDE VIEWING AREA)	Minor
ELECTRICAL	10. ELECTRICAL AND OPTICAL CHARACTERISTICS (CONTRAST· VOP · CHROMATICITY ... ETC)	ACCORDING TO SPECIFICATION OR DRAWING . (INSIDE VIEWING AREA)	Critical
	11.MISSING LINE	MISSING DOT · LINE · CHARACTERREJECTED	Critical
	12.SHORT CIRCUIT· WRONG PATTERN DISPLAY	NO DISPLAY · WRONG PATTERN DISPLAY · CURRENT CONSUMPTION OUT OF SPECIFICATION..... REJECTED	Critical
	13. DOT DEFECT (FOR COLOR AND TFT)	ACCORDING TO STANDARD OF VISUAL INSPECTION	Minor

12.4 STANDARD OF VISUAL INSPECTION:

NO.	CLASS	ITEM	JUDGEMENT																				
12.4.1	MINOR	BLACK AND WHITE SPOT FOREIGN MATERIEL DUST IN THE CELL BLEMISH SCRATCH	<div>(A) ROUND TYPE: unit : mm.<table><tr><th>DIAMETER (mm.)</th><th>ACCEPTABLE Q'TY</th></tr><tr><td>$\Phi \leq 0.1$</td><td>DISREGARD</td></tr><tr><td>$0.1 < \Phi \leq 0.25$</td><td>3 (Distance>5mm)</td></tr><tr><td>$0.25 < \Phi$</td><td>0</td></tr></table><div>NOTE: $\Phi = (\text{LENGTH} + \text{WIDTH}) / 2$</div><div>(B) LINEAR TYPE: unit : mm.<table><tr><th>LENGTH</th><th>WIDTH</th><th>ACCEPTABLE Q'TY</th></tr><tr><td>-----</td><td>$W \leq 0.03$</td><td>DISREGARD</td></tr><tr><td>$L \leq 5.0$</td><td>$0.03 < W \leq 0.07$</td><td>3 (Distance>5mm)</td></tr><tr><td>-----</td><td>$0.07 < W$</td><td>FOLLOW ROUND TYPE</td></tr></table></div></div>	DIAMETER (mm.)	ACCEPTABLE Q'TY	$\Phi \leq 0.1$	DISREGARD	$0.1 < \Phi \leq 0.25$	3 (Distance>5mm)	$0.25 < \Phi$	0	LENGTH	WIDTH	ACCEPTABLE Q'TY	-----	$W \leq 0.03$	DISREGARD	$L \leq 5.0$	$0.03 < W \leq 0.07$	3 (Distance>5mm)	-----	$0.07 < W$	FOLLOW ROUND TYPE
DIAMETER (mm.)	ACCEPTABLE Q'TY																						
$\Phi \leq 0.1$	DISREGARD																						
$0.1 < \Phi \leq 0.25$	3 (Distance>5mm)																						
$0.25 < \Phi$	0																						
LENGTH	WIDTH	ACCEPTABLE Q'TY																					
-----	$W \leq 0.03$	DISREGARD																					
$L \leq 5.0$	$0.03 < W \leq 0.07$	3 (Distance>5mm)																					
-----	$0.07 < W$	FOLLOW ROUND TYPE																					
12.4.2	MINOR	BUBBLE IN POLARIZER DENT ON POLARIZER	<div>unit : mm.<table><tr><th>DIAMETER</th><th>ACCEPTABLE Q'TY</th></tr><tr><td>$\Phi \leq 0.2$</td><td>DISREGARD</td></tr><tr><td>$0.2 < \Phi \leq 0.5$</td><td>2 (Distance>5mm)</td></tr><tr><td>$0.5 < \Phi$</td><td>0</td></tr></table></div>	DIAMETER	ACCEPTABLE Q'TY	$\Phi \leq 0.2$	DISREGARD	$0.2 < \Phi \leq 0.5$	2 (Distance>5mm)	$0.5 < \Phi$	0												
DIAMETER	ACCEPTABLE Q'TY																						
$\Phi \leq 0.2$	DISREGARD																						
$0.2 < \Phi \leq 0.5$	2 (Distance>5mm)																						
$0.5 < \Phi$	0																						
12.4.3	MINOR	Dot Defect	<table><tr><th>Items</th><th>ACC. Q'TY</th></tr><tr><td>Bright dot</td><td>$N \leq 4$</td></tr><tr><td>Dark dot</td><td>$N \leq 4$</td></tr></table> <div><div>Pixel Define :</div><div><div><div>Pixel</div><div><div>R</div><div>G</div><div>B</div></div><div><div>Dot</div><div>Dot</div><div>Dot</div></div></div></div><div><div>Note 1: The definition of dot: The size of a defective dot over 1/2 of whole dot is regarded as one defective dot.</div><div>Note 2: Bright dot: Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern.</div><div>Note 3: Dark dot: Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green ,blue pattern.</div></div></div>	Items	ACC. Q'TY	Bright dot	$N \leq 4$	Dark dot	$N \leq 4$														
Items	ACC. Q'TY																						
Bright dot	$N \leq 4$																						
Dark dot	$N \leq 4$																						

NO.	CLASS	ITEM	JUDGEMENT
12.4.4	MINOR	LCD GLASS CHIPPING	 $Y > S$ Reject
12.4.5	MINOR	LCD GLASS CHIPPING	 $X \text{ or } Y > S$ Reject
12.4.6	MAJOR	LCD GLASS GLASS CRACK	 $Y > (1/2) T$ Reject
12.4.7	MAJOR	LCD GLASS SCRIBE DEFECT	 <ol style="list-style-type: none"> $a > L/3$, $A > 1.5\text{mm}$. Reject B : ACCORDING TO DIMENSION
12.4.8	MINOR	LCD GLASS CHIPPING (ON THE TERMINAL AREA)	 $\Phi = (x+y)/2 > 2.5 \text{ mm}$ Reject
12.4.9	MINOR	LCD GLASS CHIPPING (ON THE TERMINAL SURFACE)	 $Y > (1/3) T$ Reject
12.4.10	MINOR	LCD GLASS CHIPPING	 $Y > T$ Reject

13. Handling Precautions

13.1 Mounting method

The LCD panel of AMSON TFT module consists of two thin glass plates with polarizers which easily be damaged. And since the module is so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

13.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns

Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (Cl) , Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happens by miss-handling or using some materials such as Chlorine (Cl), Sulfur (S) from customer, Responsibility is on customer.

13.3 Caution against static charge

The LCD module uses C-MOS LSI drivers, so we recommend that you:

Connect any unused input terminal to power or ground, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

13.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

13.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage than the limit causes the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the maximum operating temperature, 50%Rh or less is required.

13.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
[It is recommended to store them as they have been contained in the inner container at the time of delivery from us.]

13.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

14. Precaution for Use

14.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

14.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification.
- When a new problem is arisen this is not specified in this specification.
- When an inspection specifications change or operating condition change in customer is reported to AMSON TFT and some problem is arisen in this specification due to the change.
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

15. Packing Method

TBD