

Specification for Approval

Model Name:

Sı	upplier Approv	Customer approval	
R&D Designed	R&D Approved	QC Approved	
Peter	Peng Jun		



Revision Record

REV NO.	REV DATE	CONTENTS	Note
A	2019-05-16	NEW ISSUE	
В	2019-06-05	MODIFY CTP	
С	2019-10-31	MODIFY CTP	



Table of Contents

List	Description	Page No.
	Cover	1
	Revision Record	2
	Table of Contents	3
1	Scope	4
2	General Information	4
3	External Dimensions	5
4	Interface Description	6
5	Absolute Maximum Ratings	7
6	DC Characteristics	7
7	Timing Characteristics	8
8	Backlight Characteristics	13
9	Optical Characteristics	14
10	Reliability Test Conditions and Methods	16
11	Inspection Standard	17
12	Handling Precautions	22
13	Precaution for Use	23
14	Packing Method	23



1. Scope

This specification defines general provisions as well as inspection standards for TFT module supplied by AMSON electronics.

If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution.

2. General Information

LCM

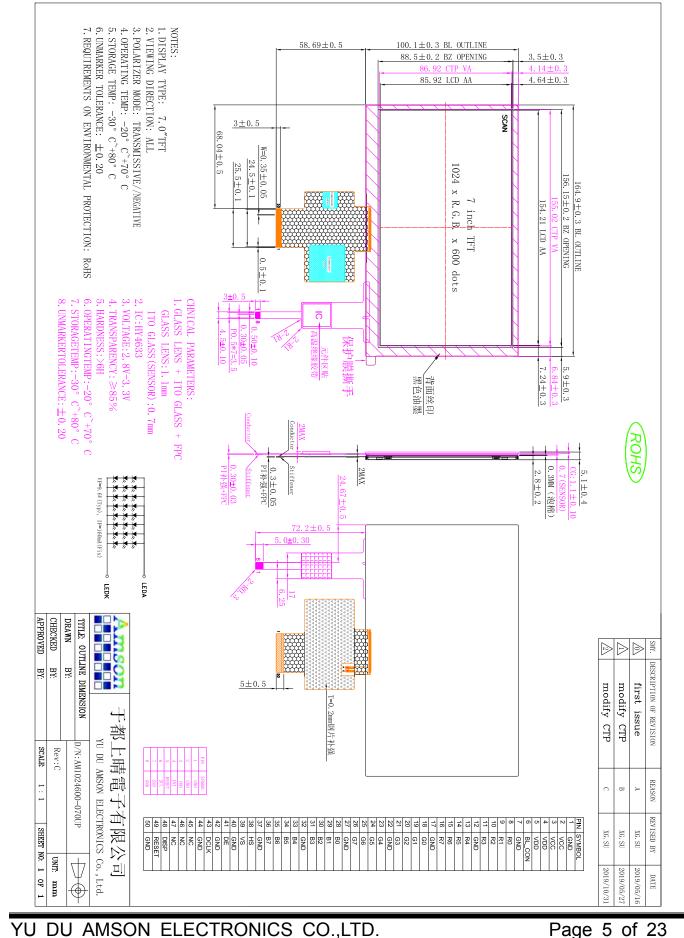
ITEM	STANDARD VALUES	UNITS
LCD type	7.0"TFT	
Dot arrangement	1024(RGB)×600	dots
Color filter array	RGB vertical stripe	
Display mode	Normally Black , Transmissive	-
Gray Scale Inversion Direction	ALL	
Eyes Viewing Direction	85/85/85	
Module size	164.90(W)×100.10 (H)×5.10 (T)	mm
Active area	154.21(W)×85.92(H)	mm
Dot pitch	0.1506(W)×0.1432(H)	mm
Interface	TTL	
Operating temperature	-20 ~ +70	°C
Storage temperature	-30 ~ +80	°C
Back Light	24White LED	

CTP

ITEM	STANDARD VALUES	UNITS
CTP type	Cover Lens + sensor + FPC	
IC	HY4633	
Surface hardness	6Н	
Transmittance	≥85%	
CTP size	164.86(W)×99.96(H)×2.3(T)	mm
Active area	155.02(W)×86.92(H)	mm
Operating temperature	-20 ~ +70	
Storage temperature	-30 ~ +80	
CTP Interface	I ² C	



3. External Dimensions





4. Interface Description

PINPIN NAMEDESCRIPTION1GNDPower ground.2. 3VDDSupply Voltage4. 5LED_VCCBL VIN Voltage6BL_CONCommon Voltage.7GNDPower ground.8~11R0~R3Red Data Input12GNDPower ground.13~16R4~R7Red Data Input.17GNDPower ground.18~21G0~G3Green Data Input.22GNDPower ground.23-26G4~G7Green Data Input.27GNDPower ground.28~31B0~B3Blue Data Input.32GNDPower ground.33~36B4~B7Blue Data Input.37GNDPower ground.38HSHorizontal sync input. Negative polarity.40GNDPower ground.41DEData Enable signal.42GNDPower ground.43DCLKClock input.44GNDPower ground.45~47NC.Not connect.48DISPDISP=L, TIMING, CONTORLLER, SOURCE DRIVER WILL TURN OFF, ALL OUTPUT ARE HIGH-Z49RESETGlobal reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KQ, C=1µF)50GNDPower ground.	TFT		
2. 3 VDD Supply Voltage 4, 5 LED_VCC BL VIN Voltage 6 BL_CON Common Voltage. 7 GND Power ground. 8~11 R0~R3 Red Data Input 12 GND Power ground. 13~16 R4~R7 Red Data Input. 17 GND Power ground. 18~21 G0~G3 Green Data Input. 22 GND Power ground. 23~26 G4~G7 Green Data Input. 27 GND Power ground. 32 GND Power ground. 33~36 B4~B7 Blue Data Input. 37 GND Power ground. 38 HS Horizontal sync input. Negative polarity. 39 VS Vertical sync input. Negative polarity. 40 GND Power ground. 41 DE Data Enable signal. 42 GND Power ground. 43 DCLK Clock input. 44<	PIN	PIN NAME	DESCRIPTION
4. 5 LED_VCC BL VIN Voltage 6 BL_CON Common Voltage. 7 GND Power ground. 8~11 R0~R3 Red Data Input 12 GND Power ground. 13~16 R4~R7 Red Data Input. 17 GND Power ground. 18~21 G0~G3 Green Data Input. 22 GND Power ground. 23~26 G4~G7 Green Data Input. 27 GND Power ground. 28~31 B0~B3 Blue Data Input. 32 GND Power ground. 33~36 B4~B7 Blue Data Input. 37 GND Power ground. 38 HS Horizontal sync input. Negative polarity. 40 GND Power ground. 41 DE Data Enable signal. 42 GND Power ground. 43 DCLK Clock input. 44 GND Power ground. 45~47	1	GND	Power ground.
6 BL_CON Common Voltage. 7 GND Power ground. 8~11 R0~R3 Red Data Input 12 GND Power ground. 13~16 R4~R7 Red Data Input. 17 GND Power ground. 18~21 G0~G3 Green Data Input. 22 GND Power ground. 23~26 G4~G7 Green Data Input. 27 GND Power ground. 28~31 B0~B3 Blue Data Input. 32 GND Power ground. 33~36 B4-B7 Blue Data Input. 37 GND Power ground. 38 HS Horizontal sync input. Negative polarity. 39 VS Vertical sync input. Negative polarity. 40 GND Power ground. 41 DE Data Enable signal. 42 GND Power ground. 43 DCLK Clock input. 44 GND Power ground. 45~47<	2, 3	VDD	Supply Voltage
7GNDPower ground. $8 - 11$ R0-R3Red Data Input12GNDPower ground.13 - 16R4-R7Red Data Input.17GNDPower ground.18 - 21G0-G3Green Data Input22GNDPower ground.23 - 26G4-G7Green Data Input.27GNDPower ground.28 - 31B0-B3Blue Data Input32GNDPower ground.33 - 36B4-B7Blue Data Input.37GNDPower ground.38HSHorizontal sync input. Negative polarity.40GNDPower ground.41DEData Enable signal.42GNDPower ground.43DCLKClock input.44GNDPower ground.45-47NC.Not connect.48DISPSTANDBY MODE. NORMALLY PULLED HIGH. DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z49RESETGlobal reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	4, 5	LED_VCC	BL VIN Voltage
8-11R0-R3Red Data Input12GNDPower ground.13-16R4-R7Red Data Input.17GNDPower ground.18-21G0-G3Green Data Input22GNDPower ground.23-26G4-G7Green Data Input.27GNDPower ground.28-31B0-B3Blue Data Input.32GNDPower ground.33-36B4-B7Blue Data Input.37GNDPower ground.38HSHorizontal sync input. Negative polarity.40GNDPower ground.41DEData Enable signal.42GNDPower ground.43DCLKClock input.44GNDPower ground.45-47NC.Not connect.48DISPSTANDBY MODE. NORMALLY PULLED HIGH. DISP=H, NORMAL OPERATION (DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF, ALL OUTPUT ARE HIGH-Z49RESETGlobal reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	6	BL_CON	Common Voltage.
12GNDPower ground.13~16R4~R7Red Data Input.17GNDPower ground.18~21G0~G3Green Data Input22GNDPower ground.23~26G4~G7Green Data Input.27GNDPower ground.28~31B0~B3Blue Data Input32GNDPower ground.33~36B4~B7Blue Data Input.37GNDPower ground.38HSHorizontal sync input. Negative polarity.39VSVertical sync input. Negative polarity.40GNDPower ground.41DEData Enable signal.42GNDPower ground.43DCLKClock input.44GNDPower ground.45~47NC.Not connect.48DISPSTANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING_CONTORLLER, SOURCE DRIVER WILL TURN OFF.ALL OUTPUT ARE HIGH-Z49RESETGlobal reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	7	GND	Power ground.
13~16R4~R7Red Data Input.17GNDPower ground.18~21G0~G3Green Data Input22GNDPower ground.23~26G4~G7Green Data Input.27GNDPower ground.28~31B0~B3Blue Data Input32GNDPower ground.33~36B4~B7Blue Data Input.37GNDPower ground.38HSHorizontal sync input. Negative polarity.39VSVertical sync input. Negative polarity.40GNDPower ground.41DEData Enable signal.42GNDPower ground.43DCLKClock input.44GNDPower ground.45~47NC.Not connect.48DISPSTANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z49RESETGlobal reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	8~11	R0~R3	Red Data Input
17GNDPower ground.18~21G0~G3Green Data Input22GNDPower ground.23~26G4~G7Green Data Input.27GNDPower ground.28~31B0~B3Blue Data Input32GNDPower ground.33~36B4~B7Blue Data Input.37GNDPower ground.38HSHorizontal sync input. Negative polarity.39VSVertical sync input. Negative polarity.40GNDPower ground.41DEData Enable signal.42GNDPower ground.43DCLKClock input.44GNDPower ground.45~47NC.Not connect.48DISPSTANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z49RESETGlobal reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	12	GND	Power ground.
18~21G0~G3Green Data Input22GNDPower ground.23~26G4~G7Green Data Input.27GNDPower ground.28~31B0~B3Blue Data Input32GNDPower ground.33~36B4~B7Blue Data Input.37GNDPower ground.38HSHorizontal sync input. Negative polarity.39VSVertical sync input. Negative polarity.40GNDPower ground.41DEData Enable signal.42GNDPower ground.43DCLKClock input.44GNDPower ground.45~47NC.Not connect.48DISPSTANDBY MODE. NORMALLY PULLED HIGH. DISP=H, NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER, SOURCE DRIVER WILL TURN OFF, ALL OUTPUT ARE HIGH-Z49RESETGlobal reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	13~16	R4~R7	Red Data Input.
22GNDPower ground.23~26G4~G7Green Data Input.27GNDPower ground.28~31B0~B3Blue Data Input32GNDPower ground.33~36B4~B7Blue Data Input.37GNDPower ground.38HSHorizontal sync input. Negative polarity.39VSVertical sync input. Negative polarity.40GNDPower ground.41DEData Enable signal.42GNDPower ground.43DCLKClock input.44GNDPower ground.45~47NC.Not connect.48DISPSTANDBY MODE. NORMALLY PULLED HIGH. DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z49RESETGlobal reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10K Ω , C=1µF)	17	GND	Power ground.
$\begin{array}{c cccc} 23{\sim}26 & G4{\sim}G7 & Green Data Input. \\ \hline 27 & GND & Power ground. \\ \hline 28{\sim}31 & B0{\sim}B3 & Blue Data Input \\ \hline 32 & GND & Power ground. \\ \hline 33{\sim}36 & B4{\sim}B7 & Blue Data Input. \\ \hline 33{\sim}36 & B4{\sim}B7 & Blue Data Input. \\ \hline 37 & GND & Power ground. \\ \hline 38 & HS & Horizontal sync input. Negative polarity. \\ \hline 39 & VS & Vertical sync input. Negative polarity. \\ \hline 40 & GND & Power ground. \\ \hline 41 & DE & Data Enable signal. \\ \hline 42 & GND & Power ground. \\ \hline 43 & DCLK & Clock input. \\ \hline 44 & GND & Power ground. \\ \hline 45{\sim}47 & NC. & Not connect. \\ \hline 48 & DISP & DISP=H, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF, ALL OUTPUT ARE HIGH-Z \\ \hline 49 & RESET & Global reset pin. Active low to enter reset state. \\ Suggest to connecting with an RC reset circuit for stability. \\ \hline Normally pull high.(R=10K\Omega, C=1\muF) \\ \hline \end{array}$	18~21	G0~G3	Green Data Input
27 GND Power ground. 28~31 B0~B3 Blue Data Input 32 GND Power ground. 33~36 B4~B7 Blue Data Input. 37 GND Power ground. 38 HS Horizontal sync input. Negative polarity. 39 VS Vertical sync input. Negative polarity. 40 GND Power ground. 41 DE Data Enable signal. 42 GND Power ground. 43 DCLK Clock input. 44 GND Power ground. 45~47 NC. Not connect. 48 DISP DISP=H,NORMAL OPERATION.(DEFAULT) DISP=H,NORMAL OPERATION.(DEFAULT) DISP=H,OUTPUT ARE HIGH-Z 49 RESET Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	22	GND	Power ground.
28~31 B0~B3 Blue Data Input 32 GND Power ground. 33~36 B4~B7 Blue Data Input. 37 GND Power ground. 38 HS Horizontal sync input. Negative polarity. 39 VS Vertical sync input. Negative polarity. 40 GND Power ground. 41 DE Data Enable signal. 42 GND Power ground. 43 DCLK Clock input. 44 GND Power ground. 45~47 NC. Not connect. 48 DISP STANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z 49 RESET Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	23~26	G4~G7	Green Data Input.
$\begin{array}{ c c c c c c c c c c c c c c c c c c c$	27	GND	Power ground.
33~36 B4~B7 Blue Data Input. 37 GND Power ground. 38 HS Horizontal sync input. Negative polarity. 39 VS Vertical sync input. Negative polarity. 40 GND Power ground. 41 DE Data Enable signal. 42 GND Power ground. 43 DCLK Clock input. 44 GND Power ground. 45~47 NC. Not connect. 48 DISP STANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z 49 RESET Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	28~31	B0~B3	Blue Data Input
37 GND Power ground. 38 HS Horizontal sync input. Negative polarity. 39 VS Vertical sync input. Negative polarity. 40 GND Power ground. 41 DE Data Enable signal. 42 GND Power ground. 43 DCLK Clock input. 44 GND Power ground. 45~47 NC. Not connect. 48 DISP STANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z 49 RESET Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	32	GND	Power ground.
38 HS Horizontal sync input. Negative polarity. 39 VS Vertical sync input. Negative polarity. 40 GND Power ground. 41 DE Data Enable signal. 42 GND Power ground. 43 DCLK Clock input. 44 GND Power ground. 45~47 NC. Not connect. 48 DISP STANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z 49 RESET Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	33~36	B4~B7	Blue Data Input.
39 VS Vertical sync input. Negative polarity. 40 GND Power ground. 41 DE Data Enable signal. 42 GND Power ground. 43 DCLK Clock input. 44 GND Power ground. 45~47 NC. Not connect. 48 DISP STANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z 49 RESET Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	37	GND	Power ground.
40 GND Power ground. 41 DE Data Enable signal. 42 GND Power ground. 43 DCLK Clock input. 44 GND Power ground. 45~47 NC. Not connect. 48 DISP STANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z 49 RESET Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	38	HS	Horizontal sync input. Negative polarity.
41 DE Data Enable signal. 42 GND Power ground. 43 DCLK Clock input. 44 GND Power ground. 45~47 NC. Not connect. 48 DISP STANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z 49 RESET Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	39	VS	Vertical sync input. Negative polarity.
42 GND Power ground. 43 DCLK Clock input. 44 GND Power ground. 45~47 NC. Not connect. 48 DISP STANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z 49 RESET Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	40	GND	Power ground.
43 DCLK Clock input. 44 GND Power ground. 45~47 NC. Not connect. 48 DISP STANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z 49 RESET Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	41	DE	Data Enable signal.
44GNDPower ground.45~47NC.Not connect.48DISPSTANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z49RESETGlobal reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	42	GND	Power ground.
45~47 NC. Not connect. 48 DISP STANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z 49 RESET Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	43	DCLK	Clock input.
48DISPSTANDBY MODE. NORMALLY PULLED HIGH. DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z49RESETGlobal reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	44	GND	Power ground.
48DISPDISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN OFF,ALL OUTPUT ARE HIGH-Z49RESETGlobal reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	45~47	NC.	Not connect.
49RESETSuggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10KΩ, C=1µF)	48	DISP	DISP=H,NORMAL OPERATION.(DEFAULT) DISP=L, TIMING ,CONTORLLER , SOURCE DRIVER WILL TURN
50 GND Power ground.	49	RESET	Suggest to connecting with an RC reset circuit for stability.
	50	GND	Power ground.



חדי

CIP							
PIN NO.		PIN NAME					
1,2,8	GND	CTP Power ground					
3	TP_VCC	CTP Digital Power 3.3V.					
4	INT	CTP interruption signal.					
5	RESET	CTP reset pin. Active low to enter reset state.					
6	SCL	CTP I ² C_clock.					
7	SDA	CTP I ² C_data					

5. Absolute Maximum Ratings

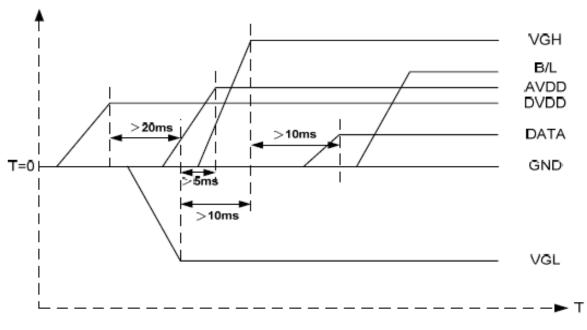
ltem	Symbol	Min.	Max.	Unit
Logic Supply Voltage	VDD	-0.5	5.0	V
Input Voltage	Vin	-0.3	VDD +0.3	V
Operating Temperature	Тор	-20	70	°C
Storage Temperature	Тѕт	-30	80	°C
Storage Humidity	HD	20	90	%RH

6. DC Characteristics

ltem	Symbol	Min.	Тур.	Max.	Unit	Remark
Logic Supply Voltage	VDD	3.0	3.3	3.6	V	-
Input High Voltage	V _{IH}	0.7VDD	-	VDD	V	-
Input Low Voltage	V _{IL}	GND	-	0.3 VDD	V	-
Output High Voltage	V _{OH}	VDD-0.4	-	VDD	V	-
Output Low Voltage	V _{OL}	GND	-	GND+0.4	V	-
I/O Leak Current	ILI	-1	-	1	uA	-
Supply Current	IDD	-	7.0	10	mA	-

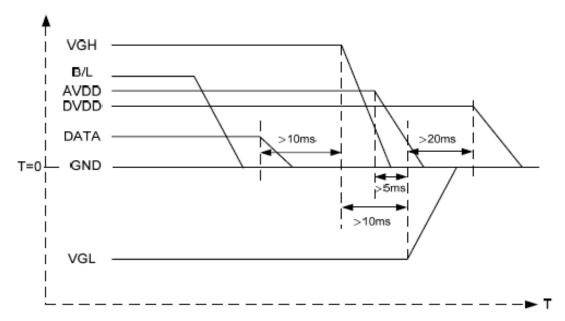


- 7. Timing Characteristics
- 7.1 Power Sequence
 - a. Power on:



 $DV_{DD} \rightarrow VGL \rightarrow VGH \rightarrow Data \rightarrow B/L$

b. Power off:



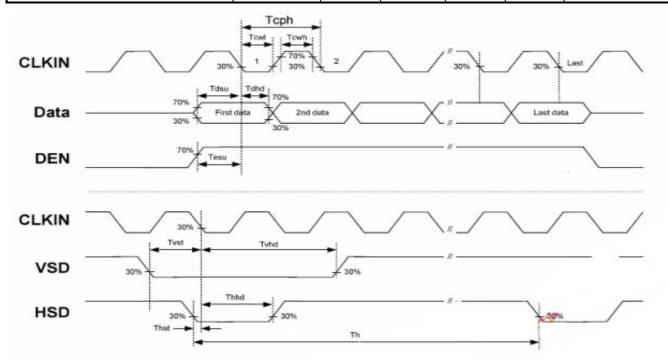
$B/L \rightarrow Data \rightarrow VGH \rightarrow VGL \rightarrow DV_{DD}$

Note: Data include R0~R7, B0~B7, GO~G7, U/D, L/R, DCLK, HS,VS,DE.



7.2 AC Electrical Characteristics

l te me	Cumula al		Values		Unit	Remark
Item	Symbol	Min.	Тур.	Max.	Unit	Remark
HS setup time	Thst	8	-	-	ns	
HS hold time	Thhd	8	-	-	ns	
VS setup time	Tvst	8	-	-	ns	
VS hold time	Tvhd	8	ŀ	-	ns	
Data setup time	Tdsu	8	-	-	ns	
Data hole time	Tdhd	8	-	-	ns	
DE setup time	Tesu	8	-	-	ns	
DE hole time	Tehd	8	-	-	ns	
DV _{DD} Power On Slew rate	TPOR	-	-	20	ms	From 0 to 90% DV _{DD}
RESET pulse width	T _{Rst}	1	-	-	ms	
DCLK cycle time	Tcoh	20	-	-	ns	
DCLK pulse duty	Towh	40	50	60	%	



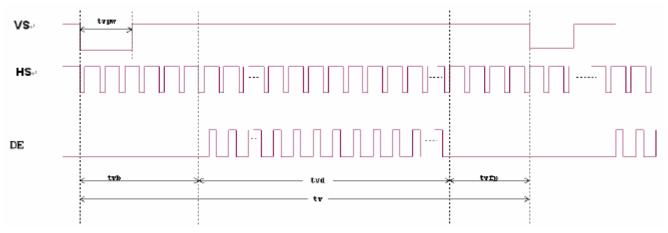


7.3 Data Input Format

Horizontal input timing diagram



Vertial input timing diagram





7.4 Timing

ltem	Symbol		Values	Unit	Remark	
Rem	Symbol	Min.	Тур.	Max.	Onic	Kennark
Horizontal Display Area	thd	-	800	-	DCLK	
DCLK Frequency	fclk	26.4	33.3	46.8	MHz	
One Horizontal Line	th	862	1056	1200	DCLK	
HS pulse width	thpw	1	-	40	DCLK	
HS Blanking	thb	46	46	46	DCLK	
HS Front Porch	thfp	16	210	354	DCLK	

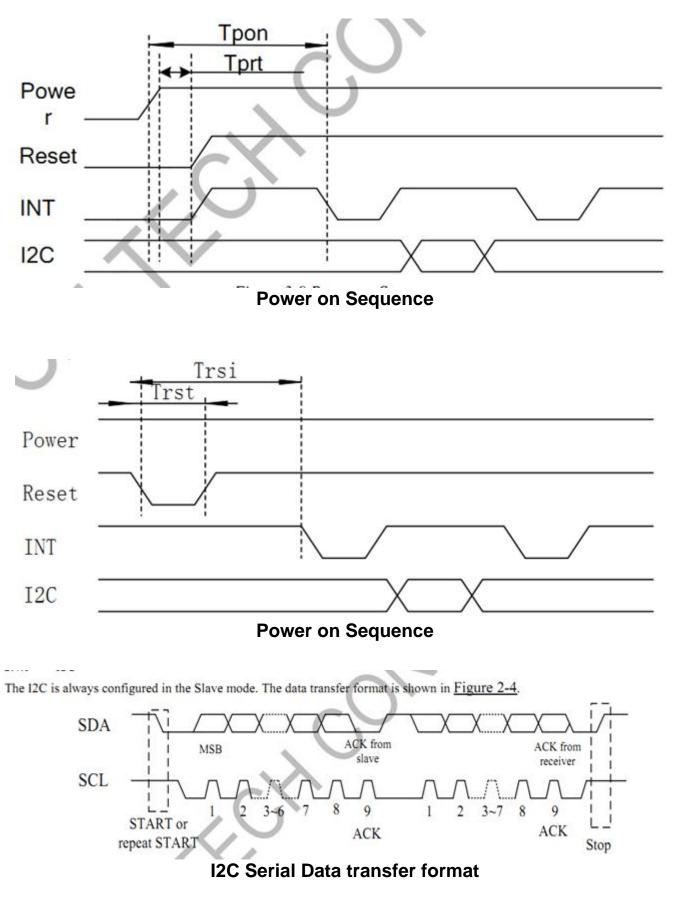
Item	Symbol		Values	Unit	Remark	
item	Symbol	Min.	Тур.	Max.	onin	Kelliark
Vertical Display Area	tvd	-	480	-	TH	
VS period time	tv	510	525	650	TH	
VS pulse width	tvpw	1	-	20	TH	
VS Blanking	tvb	23	23	23	TH	
VS Front Porch	tvfp	7	22	147	TH	

7.5 CTP Timing Characteristics 7.5.1 IIC communication timing

Parameter	Min	Max	Unit
SCL frequency	10	400	KHz
Bus free time between a STOP and START condition	4.7	1	us
Hold time (repeated) START condition	4.0	1	us
Data setup time	250	Υ	ns
Setup time for a repeated START condition	4.7	/	us
Setup Time for STOP condition	4.0	\	us

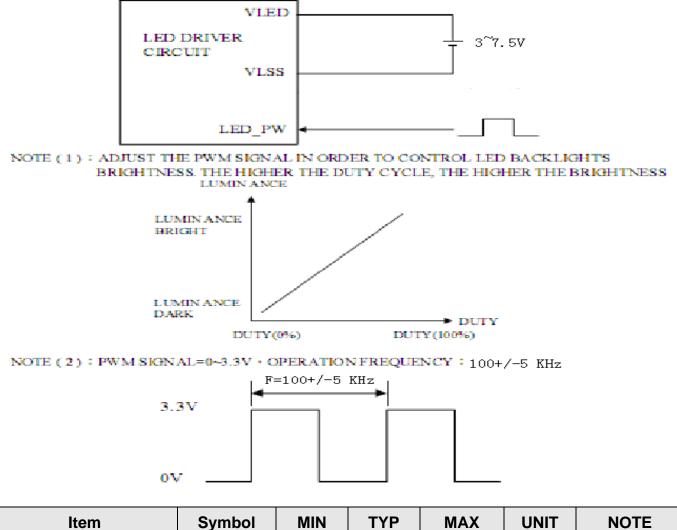


7.5.2 IIC Timing Chart





8. Backlight Characteristic



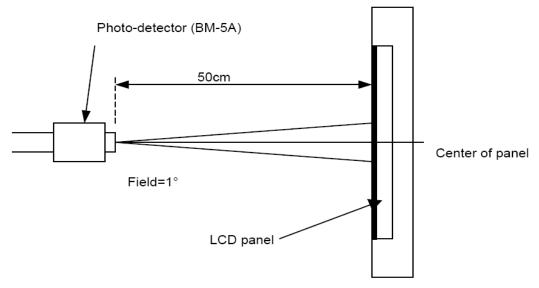
Item		Symbol	MIN	ТҮР	MAX	UNIT	NOTE
Backlight Pow	Backlight Power		3	5	7.5	V	Ta = 25°C
Backlight Pow	er	ILED_VCC	-	(0.5)	(0.7)	А	LED_VCC=5V
EN Signal Volta	VIH		1.2			V	
ge	VIL	BL_CON	GND		0.4	V	
Lifetime			30000	-	-	Hr	
Color				V	Vhite		
Average Brightness		-	300	380	-	Cd/cm2	
Luminance uniformity		-		80	-	%	



9. Optical Characteristics

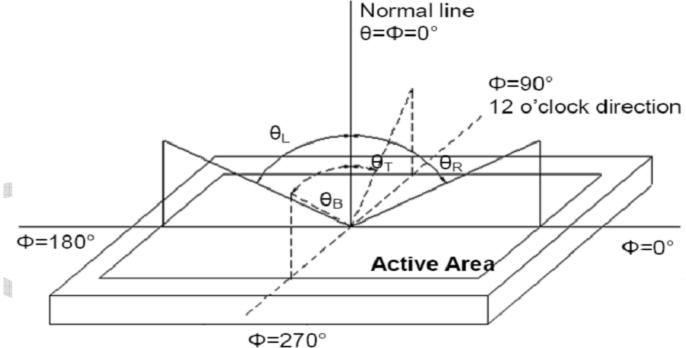
Item Conditions Min. Typ. Max. Unit Note						Note		
пет					ινιαλ.	Onit	NOLE	
	Horizontal	θι	80	85	-	dograa		
Viewing Angle	TIONZONIA	θR	80	85	-		(1) (2) (6)	
(CR>10)	Vertical	θт	80	85	-	degree	(1),(2),(6)	
	ventical	θв	80	85	-			
Contrast Ratio	Center		600	800	-	-	(1),(3),(6)	
Response Time	Rising + Falling		-	25	-	ms	(1),(4),(6)	
	Red x			TBD		-		
	Red y Green x			TBD		-		
				TBD		-		
CF Color	Green y		Тур.	TBD	Тур.	-	(1) (6)	
Chromaticity (CIE1931)	Blue x Blue y White x		-005	TBD	+0.05	-	(1), (6)	
()				TBD]	_		
				TBD]	_		
	White y			TBD		-		

Note (1) Measurement Setup: The LCD module should be stabilized at given temp. 25°C for 15 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 15 minutes in a windless room.





Note (2) Definition of Viewing Angle



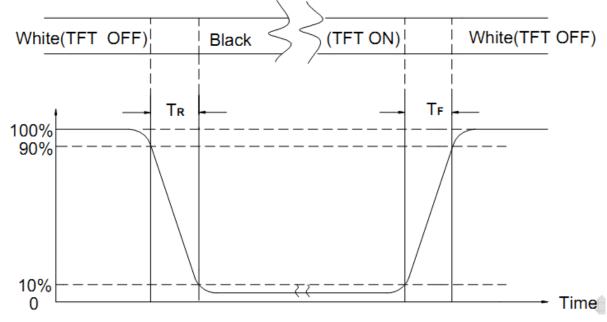
Note (3) Definition of Contrast Ratio (CR)

The contrast ratio can be calculated by the following expression C_{P}

Contrast Ratio (CR) = L63 / L0

L63: Luminance of gray level 63, L0: Luminance of gray level 0

Note (4) Definition of response time



- Note (5) Definition of Transmittance (Module is without signal input) Transmittance = Center Luminance of LCD / Center Luminance of Back Light x 100%
- Note (6) Definition of color chromaticity (CIE1931)

Color coordinates measured at the center point of LCD



10. Reliability Test Conditions and Methods

NO.	TEST ITEMS	TEST CONDITION				
1	High Temperature Storage	Keep in 80°C \pm 5°C 240 hrs Surrounding temperature, then storage at normal condition 4hrs.				
2	Low Temperature Storage	Keep in -30°C \pm 5°C 240 hrs Surrounding temperature, then storage at normal condition 4hrs.				
3	High Temperature / High Humidity Storage Test	Keep in 60 $^{\circ}$ C / 90% R.H duration for 240 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)				
4	Temperature Cycling Storage Test	$\begin{array}{rrrr} -30^{\circ}\text{C} \rightarrow +25^{\circ}\text{C} \rightarrow 80^{\circ}\text{C} \rightarrow +25^{\circ}\text{C} \\ (30 \underline{\text{mins}}) & (5 \underline{\text{mins}}) & (30 \underline{\text{mins}}) & (5 \underline{\text{mins}}) \\ 30 \text{ Cycle} \\ \end{array}$ Surrounding temperature, then storage at normal condition 4hrs.				
5	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/-Contact Discharge: Apply 250 V with 5 times discharge for each polarity +/-1. Temperature ambiance : 15°C ~35°C 2. Humidity relative : 30%~60% 3. Energy Storage Capacitance(Cs + Cd) : 150pF±10% 4. Discharge Resistance(Rd) : 330Ω±10% 5. Discharge, mode of operation : Single Discharge (time between successive discharges at least 1 sec)				
6	Vibration Test (Packaged)	 Sine wave 10~55 Hz frequency (1 min/sweep) The amplitude of vibration :1.5 mm Each direction (X, Y, Z) duration for 2 Hrs 				
7	Drop Test (Packaged)	$\begin{array}{ c c c c }\hline Packing Weight (Kg) & Drop Height (cm) \\\hline 0 \sim 45.4 & 122 \\\hline 45.4 \sim 90.8 & 76 \\\hline 90.8 \sim 454 & 61 \\\hline 0ver 454 & 46 \\\hline Drop \\\hline Direction : %1 corner / 3 edges / 6 sides each 1 time \\\hline \end{array}$				



11. Inspection Standard

11.1. QUALITY :

THE QUALITY OF GOODS SUPPLIED TO PURCHASER SHALL COME UP TO THE FOLLOWING STANDARD.

11.1.1. INSPECTIONTOOLS AND INSTRUMENTS

Vernier calipers, film scales, multimeter, magnifying eyepiece, ND5%, luminance meter and so on.

11.1.2. THE METHOD OF PRESERVING GOODS

AFTER DELIVERY OF GOODS FROM AMSON TO PURCHASER. PURCHASER SHALL CONTROL THE LCM AT -10 TO 40 ,AND IT MIGHT BE DESIRABLE TO KEEP AT THE NORMAL ROOM TEMPERATURE AND HUMIDITY UNTIL INCOMING INSPECTION OR THROWING INTO PROCESS LINE.

11.1.3. INCOMING INSPECTION

(A) THE METHOD OF INSPECTION

IF PURCHASER MAKE AN INCOMING INSPECTION , A SAMPLING PLAN SHALL BE APPLIED ON THE CONDITION THAT QUALITY OF ONE DELIVERY SHALL BE REGARDED AS ONE LOT.

(B) THE STANDARD OF QUALITY

ISO-2859-1 (SAME AS MIL-STD-105E) ,LEVEL:

, ·
AQL(%)
0.4 %
0.65 %
1.5 %

EVERY ITEM SHALL BE INSPECTED ACCORDING TO THE CLASS.

(C) MEASURE

IF AS THE RESULT OF ABOVE RECEIVING INSPECTION , A LOT OUT IS DISCOVERED. PURCHASER SHALL BE INFORM SELLER OF IT WITHIN SEVEN DAYS. BUT FIRST SHIPMENT WITHIN FOURTEEN DAYS.

11.1.4. WARRANTY POLICY

AMSON WILL PROVIDE ONE-YEAR WARRANTY FOR THE PRODUCTS ONLY IF UNDER SPECIFICATION OPERATING CONDITIONS. AMSON WILL REPLACE NEW PRODUCTS FOR THESE DEFECT PRODUCTS WHICH UNDER WARRANTY PERIOD AND BELONG TO THE RESPONSIBILITY OF AMSON.

11.2. CHECKING CONDITION

- **11.2.1.**CHECKING DIRECTION SHALL BE IN THE 45 DEGREE AREA TO FACE THE SAMPLE.
- **11.2.2.**CHECKER SHALL SEE OVER 300±25 mm. WITH BARE EYES FAR FROM SAMPLE
- **11.2.3.**Ambient Illumination:

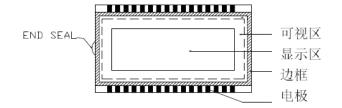
0~30 Lux for functional inspection

500 ~ 1200 Lux for external appearance inspection.

 \rightarrow

11.2.4. TEST AREA:

11.2.5. Inspection should be carried out with rope electrostatic ring and static finger cover (both hands except small fingers must be worn)





AM-1024600-070UP

Version: C

2019-10-31

11.2.6. The inspector may make a visual inspection or a comparative examination with a film ruler and a magnifying eyepiece. Individual defects shall be determined according to the limited samples.

11.2.7. Functional testing uses electrical testing fixtures or test fixtures required by customers.

11.2.8. the ion fan should be used when testing.

11.2.9. the principle of judgment

11.3.1 If the defect outside the visual area does not affect the assembly and display, it will be judged as a good product.

11.3.2 Poor definitionPixel:A combination of three sub-pixels(Red + Green + Blue).

Dot:

Any of the sub-pixels (Red or Green or Blue).

Bright and dark dots:

A point pixel (sub-pixel: R, G, B pixels) is lit or turned off during the display function test. **Highlights**:

Usually considered to be shown on a black screen.

Dark spots:

They are generally considered to be shown on R, G, B solid colors or white images. **Neighborhood**:

Two or three adjacent point pixels (dot: sub-pixel) connected together (R, G or G, B or B, R or RGB).



11.3. INSPECTION PLAN :

11.0. 1101 20	HON I LAN.		
CLASS	ITEM	JUDGEMENT	CLASS
PACKING &	1. OUTSIDE AND INSIDE PACKAGE	"MODEL NO." , "LOT NO." AND "QUANTITY" SHOULD INDICATE ON THE PACKAGE.	Minor
INDICATE	2. MODEL MIXED AND QUANTITY	OTHER MODEL MIXEDREJECTED	Critical
	3. PRODUCT INDICATION	"MODEL NO." SHOULD INDICATE ON THE PRODUCT	Major
ASSEMBLY	4. DIMENSION, LCD GLASS SCRATCH AND SCRIBE DEFECT.	ACCORDING TO SPECIFICATION OR DRAWING.	Major
	5. VIEWING AREA	POLARIZER EDGE OR LCD'S SEALING LINE IS VISABLE IN THE VIEWING AREA REJECTED	Minor
	6. BLEMISH V BLACK SPOT V WHITE SPOT IN THE LCD AND LCD GLASS CRACKS	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
APPEARANCE	7. BLEMISH • BLACK SPOT WHITE SPOT AND SCRATCH ON THE POLARIZER	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
	8. BUBBLE IN POLARIZER	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
	9. LCD'S RAINBOW COLOR	STRONG DEVIATION COLOR (OR NEWTON RING) OF LCDREJECTED. OR ACCORDING TO LIMITED SAMPLE (IF NEEDED, AND INSIDE VIEWING AREA)	Minor
	10. ELECTRICAL AND OPTICAL CHARACTERISTICS (CONTRAST, VOP, CHROMATICITY ETC)	ACCORDING TO SPECIFICATION OR DRAWING . (INSIDE VIEWING AREA)	Critical
ELECTRICAL	11.MISSING LINE	MISSING DOT LINE CHARACTER	Critical
	12.SHORT CIRCUIT- WRONG PATTERN DISPLAY	NO DISPLAY VRONG PATTERN DISPLAY CURRENT CONSUMPTION OUT OF SPECIFICATION REJECTED	Critical
	13. DOT DEFECT (FOR COLOR AND TFT)	ACCORDING TO STANDARD OF VISUAL	Minor



11.4. STANDARD OF VISUAL INSPECTION

NO.	CLASS	ITEM	JUDGEM	ENT			
			(A) ROUND TYPE: unit : mm.				
			DIAMETER (mm.) ACC	EPTABLE Q'TY			
			Φ ≦ 0.15	Distance≥1mm			
		DI ACK AND WHITE ODOT	0.15 < Φ ≦ 0.4	3 (Distance>15mm)			
		BLACK AND WHITE SPOT	0.4 < Φ	0			
		FOREIGN MATERIEL DUST IN THE CELL	NOTE: Ф=(LENGTH+WIDTH)/2				
1.4.1	MINOR	BLEMISH	(B) LINEAR TYPE:	unit : mm.			
		SCRATCH	LENGTH WIDTH	ACCEPTABLE Q'TY			
		CONTON	W ≦0.0	3 Distance≥1mm			
			L ≦ 4.0 0.03 < W ≦0.0	5 3 (Distance>15mm)			
			0.05 < W	FOLLOW ROUND TYPE			
			APE 9300	- 0 ²			
				unit : mm.			
			DIAMETER AG	CCEPTABLE Q'TY			
		BUBBLE IN POLARIZER	Φ ≤ 0.2	Distance≥1mm			
11.4.2 MINC	MINOR	DENT ON POLARIZER	0.2 < ⊕ ≦ 0.5	3 (Distance>15mm)			
			0.5 < Φ	0			
				≦2 (Distance≥15mm) ≤3 (Distance>15mm)			
11.4.3	MINOR		Pixel Define : Pixel	Dot → e of a defective dot over as one defective dot. ble by 5 % ND filter N ≤ 5 it and unchanged in size ying under black pattern. and unchanged in size in			
1,4,4	MINOR	Mura	Not visible thriugh 5% ND filter in by limit sample if necessary	150% gray or judge			



AM-1024600-070UP

Version: C

2019-10-31

NO.	CLASS	ITEM	JUDGEMENT
11.4.5	MINOR	LCD GLASS CHIPPING	X ≥ 3mm Y > S Reject
11.4.6	MINOR	LCD GLASS CHIPPING	X or Y > S Reject
11.4.7	MAJOR	LCD GLASS GLASS CRACK	T T NG Reject
11.4.8	MAJOR	LCD GLASS SCRIBE DEFECT	
11.4.9	MINOR	LCD GLASS CHIPPING (ON THE TERMINAL AREA)	$Y < 1/2Z$ $Y \ge 0.5mm_{Reject}$ $X \ge 3mm$
11.4.10	MINOR	LCD GLASS CHIPPING (ON THE TERMINAL SURFACE)	$Y < 1/2Z$ $Y \ge 0.5mm$ $Reject$ $X \ge 3mm$
11.4.11	MINOR	LCD GLASS CHIPPING	$X \ge 3mm$ $Y \ge T$ $Y \ge T$ F



12. Handling Precautions

12.1 Mounting method

The LCD panel of AMSON TFT module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (CI) , Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

12.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to power or ground, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

12.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

12.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the maximum operating temperature, 50%Rh or less is required.



12.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else. [It is recommended to store them as they have been contained in the inner container at the time of delivery from us.

12.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

13. Precaution for Use

13.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

13.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification.
- When a new problem is arisen this is not specified in this specification.
- When an inspection specifications change or operating condition change in customer is reported to AMSON TFT and some problem is arisen in this specification due to the change.
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

14. Packing Method TBD