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# Specification for Approval

| Customer:   |  |
|-------------|--|
| Model Name: |  |

| Supplier Approval |              |             | Customer approval |
|-------------------|--------------|-------------|-------------------|
| R&D Designed      | R&D Approved | QC Approved |                   |
| Peter             | Peng Jun     |             |                   |

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## **Revision Record**

| REV NO.  | REV DATE   | CONTENTS  | Note |
|----------|------------|-----------|------|
| Α        | 2021-12-14 | NEW ISSUE |      |
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### 1. Scope

This specification defines general provisions as well as inspection standards for TFT module supplied by AMSON electronics.

If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution.

#### 2. General Information

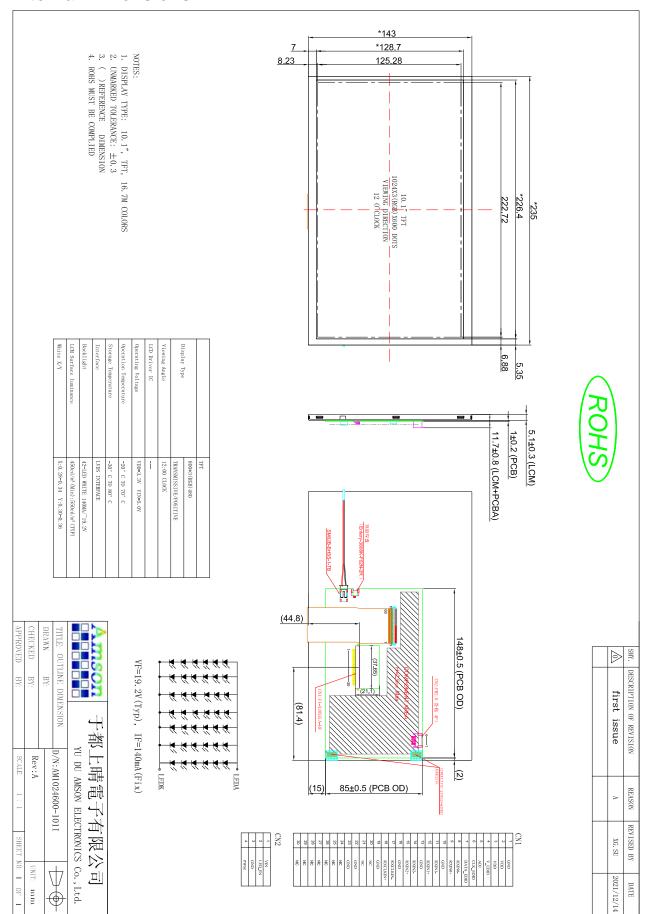
| ITEM                  | STANDARD VALUES           | UNITS |
|-----------------------|---------------------------|-------|
| LCD type              | 10.1"TFT                  |       |
| Dot arrangement       | 1024×3 (RGB)×600          | dots  |
| Color filter array    | RGB vertical stripe       |       |
| Display mode          | Normally White            |       |
| Viewing Direction     | 6 O' Clock                |       |
| Module size           | 235.0(W)×143.0(H)×11.7(T) | mm    |
| Active area           | 222.72(W)×125.28(H)       | mm    |
| Dot pitch             | 0.2175(W)×0.2088(H)       | mm    |
| Interface             | LVDS Interface            |       |
| Operating temperature | -20 ~ +70                 | °C    |
| Storage temperature   | -30 ~ +80                 | °C    |
| Surface treatment     | Anti-glare                |       |



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#### 3. External Dimensions





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4. Interface Description CN1: FI-X30SSLA-HF (equivalent JAEFI-XB30SSRI-HF16)

| Pin No. | Symbol    | Description            | Note |
|---------|-----------|------------------------|------|
| 1       | GND       | Ground                 |      |
| 2       | VDD       | 3.3V Power             |      |
| 3       | VDD       | 3.3V Power             |      |
| 4       | V_EDID    | 3.3V Power for EDID    |      |
| 5       | NC        | No connection          |      |
| 6       | CLK_EDID  | EDID Clock Input       |      |
| 7       | DATA_EDID | EDID Data Input        |      |
| 8       | RXIN0-    | LVDS Signal- channel0- |      |
| 9       | RXIN0+    | LVDS Signal+ channel0+ |      |
| 10      | GND       | Ground                 |      |
| 11      | RXIN1-    | Data Input channel1-   |      |
| 12      | RXIN1+    | Data Input channel1+   |      |
| 13      | GND       | Ground                 |      |
| 14      | RXIN2-    | Data Input channel2-   |      |
| 15      | RXIN2+    | Data Input channel2+   |      |
| 16      | GND       | Ground                 |      |
| 17      | RXCLKIN-  | Data Input CLK-        |      |
| 18      | RXCLKIN+  | Data Input CLK+        |      |
| 19      | GND       | Ground                 |      |
| 20      | NC        | No connection          |      |
| 21      | NC        | No connection          |      |
| 22      | GND       | Ground                 |      |
| 23      | GND       | Ground                 |      |
| 24      | NC        | No connection          |      |



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| 25 | NC | No connection |
|----|----|---------------|
| 26 | NC | No connection |
| 27 | NC | No connection |
| 28 | NC | No connection |
| 29 | NC | No connection |
| 30 | NC | No connection |

CN2 (LED backlight): BNSR-02VS-1(JST or equivalent)

| Pin No. | Symbol | Description                           | Note |
|---------|--------|---------------------------------------|------|
| 1       | Α      | Anode for LED backlight(+19.2V,140mA) |      |
| 2       | K      | Cathode for LED backlight             |      |

CN2 (LED Driver Board): PH2.04P (Kingfont or equivalent)

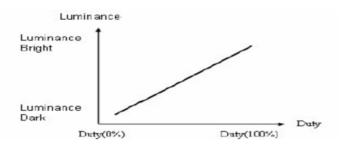
| Pin No. | Symbol | Description                      | Note |
|---------|--------|----------------------------------|------|
| 1       | VIN    | Voltage for LED circuit(+5V)     |      |
| 2       | LED_EN | LED BLU ON/OFF                   |      |
| 3       | GND    | Power ground                     |      |
| 4       | PWM    | Adjust the LED brightness by PWM |      |

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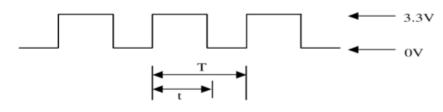
#### [Note]

(1) ADJ can adjust brightness to control Pin. Pulse duty the bigger the brighter.



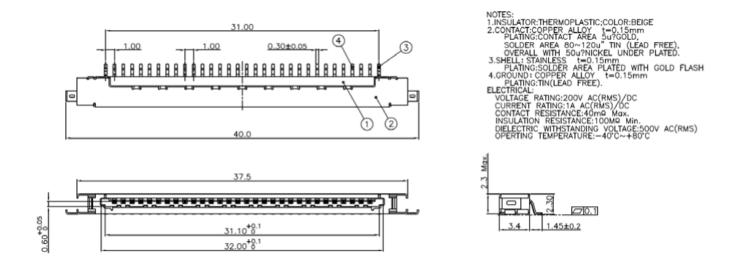
#### (2) ADJ Signal=0~3.3V , Operation Frequency :

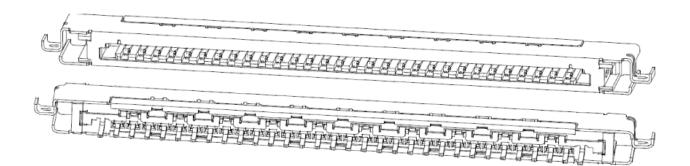
| Dimming Range     |                   |                   |
|-------------------|-------------------|-------------------|
| PWM Frequency (F) | Duty Cycle (Min.) | Duty Cycle (Max.) |
| 100Hz < F < 500Hz | 5%                | 100%              |
| 500Hz < F < 20KHz | 10%               | 100%              |



Duty Cycle = t / T \*100%

### (3) LVDS Connector: FI-X30SSLA-HF







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**5. Absolute Maximum Ratings**The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit.

| Item                     | Symbol          | Val  | ues  | UNIT  | Note  |
|--------------------------|-----------------|------|------|-------|-------|
| TCIII                    | Cyllibol        | Min. | Max. | 01411 |       |
| LED Power Supply Voltage | VLED            | -0.3 | 15.0 | V     | GND=0 |
| Logic Supply Voltage     | V <sub>DD</sub> | -0.3 | 5.0  | V     |       |
| Operating Temperature    | Тора            | -20  | 70   | °C    |       |
| Storage Temperature      | Тѕтс            | -30  | 80   | °C    |       |

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#### 6. DC Characteristics

#### **6.1 TFT LCD Modules**

|                                    |        |      | Values |      |      |                           |
|------------------------------------|--------|------|--------|------|------|---------------------------|
| Item                               | Symbol | Min. | Тур.   | Max. | UNIT | Note                      |
| Power voltage                      | VDD    | 3.0  | 3.3    | 3.6  | V    | Note 1                    |
| Current of power supply            | IDD    | -    | 0.3    | -    | А    | VDD=3.3V<br>Black pattern |
| Power voltage for<br>LED driver    | VLED   | 4.5  | 5      | 5.5  | V    |                           |
| LED driver current of power supply | ILED   | -    | 365    |      | mA   | VLED=5V<br>ADJ=100%       |

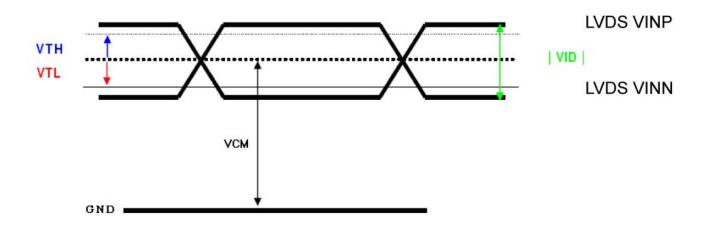
Note 1: VDD-dip condition:

When  $2.7 \le VDD < 3.0V$ ,  $td \le 10ms$ .

VDD>3.0V, VDD-dip condition should be same as VDD-turn-con condition

6.2 Switching Characteristics of LVDS Receiver

| and of the control of the control of the control |        |                   |      |                          |      |           |
|--|--------|-------------------|------|--------------------------|------|-----------|
| Item   | Symbol | Min.              | Тур. | Max.                     | Unit | Condition |
| Differential Input High<br>Threshold             | VTH    |                   |      | 100                      | mV   | VCM=1.2V  |
| Differential Input Low<br>Threshold              | VTL    | -100              |      |                          | mV   |           |
| Input current                                    | IIN    | -10               |      | +10                      | uA   |           |
| Differential input<br>Voltage                    | [VID]  | 0.2               |      | 0.6                      | V    |           |
| Common Mode<br>Voltage Offset                    | VCM    | <u>VID</u>  <br>2 | 1.25 | 2.4-   <u>VID</u>  <br>2 | V    |           |

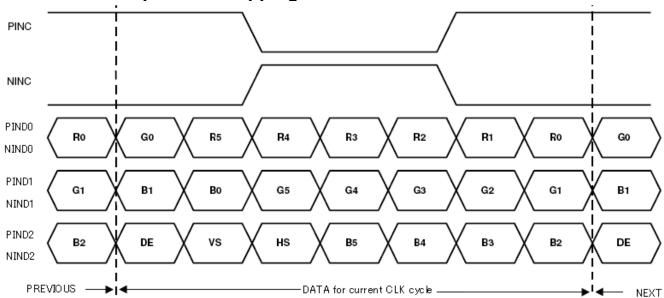


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### 7. Timing Characteristics

### 7.1 6-bit LVDS Input Data Mapping



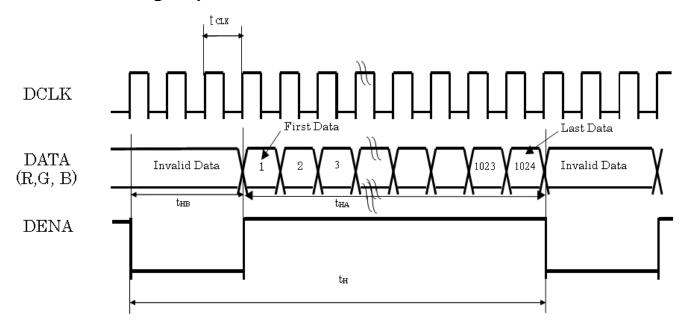
7.2 Timing Characteristics of Input signals

| .2 mining characteristics of input signals         |            |                                   |                            |                 |      |      |                |                |
|--|------------|-----------------------------------|----------------------------|-----------------|------|------|----------------|----------------|
|  | Symbol     | Min.                              | Тур.                       | Max.            | Unit |      |                |                |
| LVDS input signal sequence                         |            | Frame F                           | Rate                       | tclk            | 41   | 51.2 | 57             | MHz            |
|  |            |                                   | Horizontal total<br>Timing | tн              | 1214 | 1344 | 1364           | tCLK           |
| LCD input signal sequence (input LVDS Transmitter) | Horizontal | Horizontal<br>effective<br>Timing | t <sub>HA</sub>            | 1024            |      |      | tCLK           |                |
|  | DENA       |                                   | Horizontal<br>Blank Time   | t <sub>HB</sub> | 190  | 320  | 340            | tCLK           |
|  |            | Vertical total<br>Time            | t <sub>V</sub>             | 615             | 365  | 645  | t <sub>H</sub> |                |
|  |            | Vertical                          | Vertical effective Time    | t <sub>VA</sub> | 600  |      | t <sub>H</sub> |                |
|  |            |                                   | Vertical Blank<br>Time     | t <sub>VB</sub> | 15   | 35   | 45             | t <sub>H</sub> |

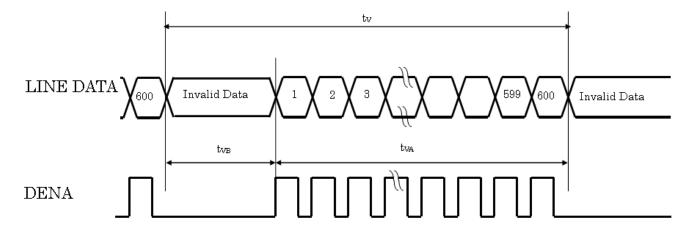
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### Horizontal timing sequence



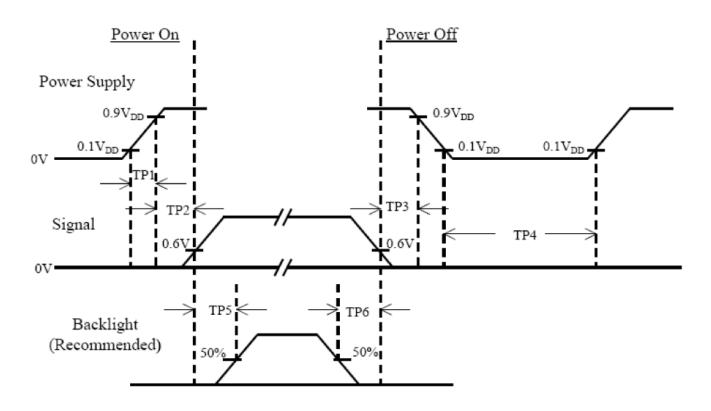
### Vertical timing sequence



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#### 7.3 Power On/off Sequence



| Item | Min. | Тур. | Max. | Unit | Remark |
|------|------|------|------|------|--------|
| TP1  | 0.5  |      | 10   | msec |        |
| TP2  | 0    |      | 50   | msec |        |
| TP3  | 0    |      | 50   | msec |        |
| TP4  | 500  |      |      | msec |        |
| TP5  | 200  |      |      | msec |        |
| TP6  | 200  |      |      | msec |        |

#### Note:

- (1) The supply voltage of the external system for the module input should be the same as the definition of VDD.
- (2) Apply the lamp voltage within the LCD operation range. When the back-light turns on before the LCD operation or the LCD turns off before the back-light turns off, the display may momentarily become white.
- (3) In case of VDD = off level, please keep the level of input signal on the low or keep a high impedance.
- (4) TP4 should be measured after the module has been fully discharged between power off and on period.
- (5) Interface signal shall not be kept at high impedance when the power is on.

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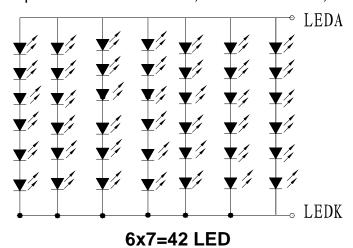
8. Backlight Characteristic

| Item                                      | Symbol   |      | Values | Unit | Note  |                                     |  |
|---|----------|------|--------|------|-------|-------------------------------------|--|
| item                                      | Syllibol | Min. | Тур.   | Max. | Offic | 11010                               |  |
| LED Driver<br>Voltage                     | VLED     | 4.5  | 5      | 5.5  | V     |                                     |  |
| Power Supply<br>Current for LED<br>Driver | ILED     |      | 365    |      | mA    | VLED=5V<br>VADJ=3.3V<br>(duty 100%) |  |
| ADJ Input Voltage                         | VADJ     |      | 3.3    | VLED | V     | Duty=100%                           |  |
| LED Voltage                               | VAK      |      | 19.2   | 21.0 | V     | IL=140mA<br>Ta=25°C                 |  |
| LED Current                               | ш        | 1    | 140    |      | mA    | Note (1)                            |  |
|   | IL -     | -    | 160    |      | mA    | Note (1)                            |  |
| LED Life Time                             |          |      | 50K    |      | Hour  | Note (2)                            |  |

Note (1): The constant current source is needed for white LED back-light driving.

When LCM is operated over 60 deg.C ambient temperature, the l∟ of the LED back-light should be adjusted to 160mA max

There are 6 Groups LED shown as below, VLEDA-LEDK=19.2V, Ta=25°C



Note (2): Condition: Ta=25°C, continuous lighting

Life time is estimated data.

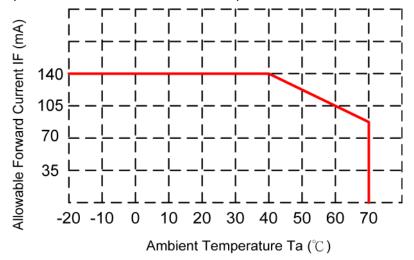
Definitions of failure:

- 1. LCM brightness becomes half of the minimum value.
- 2. LED doesn't light normally.

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When LCM is operated over 40°C ambient temperature, the ILED should be follow:





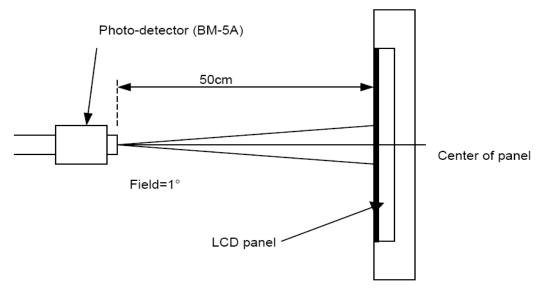
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9. Optical Characteristics

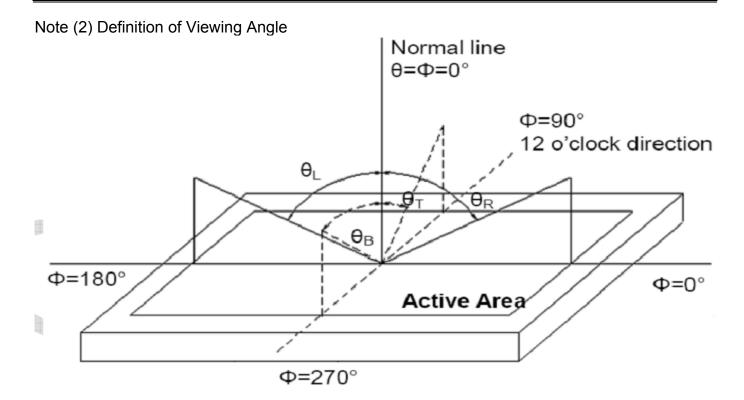
| Item                      | Conditions            |    | Min.  | Тур.               | Max.  | Unit              | Note        |  |
|---------------------------|-----------------------|----|-------|--------------------|-------|-------------------|-------------|--|
|                           | Horizontal            | θL | 60    | 70                 | -     | dograa            |             |  |
| Viewing Angle             | Honzontai             | θR | 60    | 70                 | -     |                   | (4) (2) (6) |  |
| (CR>10)                   | Vertical              | θт | 60    | 70                 | -     | degree            | (1),(2),(6) |  |
|                           | vertical              | θв | 40    | 50                 | -     |                   |             |  |
| Contrast Ratio            | Center                |    | 400   | 500                | -     | -                 | (1),(3),(6) |  |
| Luminance                 | L                     |    | 450   | 550                | -     | cd/m <sup>2</sup> | (1)         |  |
| Luminance uniformity      | YU                    |    | 70    |                    |       | %                 | (1),(5)     |  |
| Response Time             | TR                    |    | -     | 5                  | 7     | ms                | (1) (4) (6) |  |
|                           | TF                    |    | -     | 20                 | 28    | ms                | (1),(4),(6) |  |
| Red x                     |                       |    | TBD   |                    | -     |                   |             |  |
|                           | Red y Green x Green y |    |       | TBD TBD Twp. TBD T |       | -                 |             |  |
|                           |                       |    |       |                    |       | -                 |             |  |
| CF Color                  |                       |    | Тур.  |                    | Тур.  | -                 | (1), (6)    |  |
| Chromaticity<br>(CIE1931) | Blue x                |    | -0.05 | TBD                | +0.05 | ı                 |             |  |
|                           | Blue y                |    |       | TBD                |       | -                 |             |  |
|                           | White x               |    |       | TBD                |       | -                 |             |  |
|                           | White y               |    |       | TBD                |       | -                 |             |  |

Note (1) Measurement Setup: The LCD module should be stabilized at given temp. 25°C for 15 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 15 minutes in a windless room.



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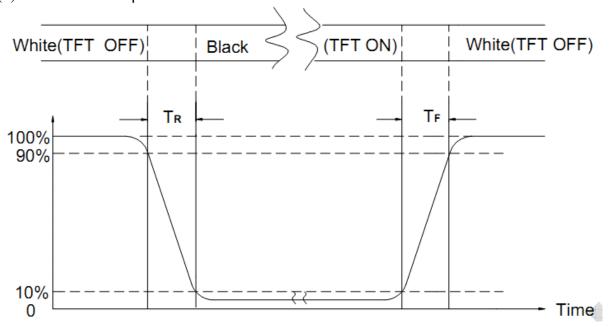


Note (3) Definition of Contrast Ratio (CR)

The contrast ratio can be calculated by the following expression Contrast Ratio (CR) = L63 / L0

L63: Luminance of gray level 63, L0: Luminance of gray level 0

Note (4) Definition of response time



Note (5) Definition of Transmittance (Module is without signal input)

Transmittance = Center Luminance of LCD / Center Luminance of Back Light x 100%

Note (6) Definition of color chromaticity (CIE1931)

Color coordinates measured at the center point of LCD



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10. Reliability Test Conditions and Methods

| IU. IXE | 10. Reliability Test Conditions and Methods         |  |   |  |  |  |  |
|---------|---|--|---|--|--|--|--|
| NO.     | TEST ITEMS  | TEST CONDITION   |   |  |  |  |  |
| 1)      | High Temperature<br>Storage                         | Keep in 80°C ±5°C 96 hrs<br>Surrounding temperature, then storage at normal condition 4hrs.  |   |  |  |  |  |
| 2       | Low Temperature<br>Storage                          | Keep in -30°C ±5°C 96 hrs<br>Surrounding temperature, then storage at normal condition 4hrs.   |   |  |  |  |  |
| 3       | High Temperature /<br>High Humidity<br>Storage Test | Keep in 60 °C / 90% R.H duration<br>Surrounding temperature, then st<br>(Excluding the polarizer)  |   |  |  |  |  |
| 4       | Temperature<br>Cycling<br>Storage Test              | $ \begin{array}{cccccccccccccccccccccccccccccccccccc$  |   |  |  |  |  |
|         |   | Air Discharge:<br>Apply 2 KV with 5 times<br>Discharge for each polarity +/-   | Contact Discharge: Apply 250 V with 5 times discharge for each polarity +/- |  |  |  |  |
| (5)     | ESD Test  | <ol> <li>Temperature ambiance : 15°C~35°C</li> <li>Humidity relative : 30%~60%</li> <li>Energy Storage Capacitance( Cs + Cd ) : 150pF±10%</li> <li>Discharge Resistance(Rd) : 330Ω±10%</li> <li>Discharge, mode of operation :<br/>Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication: ±5%)</li> </ol> |   |  |  |  |  |
| 6       | Vibration Test<br>(Packaged)                        | <ol> <li>Sine wave 10~55 Hz frequency (1 min/sweep)</li> <li>The amplitude of vibration :1.5 mm</li> <li>Each direction (X√Y√Z) duration for 2 Hrs</li> </ol>  |   |  |  |  |  |
| 7       | Drop Test<br>(Packaged)                             | Packing Weight (Kg)  0 ~ 45.4  45.4 ~ 90.8  90.8 ~ 454  Over 454  Drop   | Drop Height (cm) 122 76 61 46   |  |  |  |  |
|         |   | Direction: 1 corner / 3 edges / 6  | 3 sides each 1time  |  |  |  |  |



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#### 11. Inspection Standard

#### 11.1. The environmental condition of inspection

The environmental condition and visual inspection shall be conducted as below.

(1) Ambient temperature : 25±5 °C

(2) Humidity: 25~75 % RH

(3) Panel visual inspection on the operation condition for cosmetic shall be conducted at the distance 30~40cm or more between the LCD module and eyes of inspector.

Ambient Illumination: 800~1200 Lux for external appearance inspection

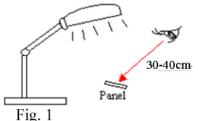
Ambient Illumination: 200~500 Lux for light on inspection

(4) The viewing angle:

a) ±15 degree to the front surface of display panel in vertical direction.

b) ±45 degree to the front surface of display panel in horizontal direction.

(5) Display panel shall be conducted at the distance 30~40cm between the LCD module and eyes of inspector (Fig. 1)



#### 11.2. Inspection Criteria

- (1) Definition of dot defect induced from the panel inside
  - a) Bright dot: Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern.
  - b) Dark dot: Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue picture.
  - c) 2 dot adjacent = 1 pair = 2 dots

Picture:









2 dot adjacent

2 dot adjacent

2 dot adjacent (vertical)

2 dot adjacent (slant)



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### (2) Display Inspection

|  | Items                                | Acceptable count   |  |  |
|--|--------------------------------------|--|--|--|
|  | Random                               | $N \leq 3$   |  |  |
| Bright dot   | 2 dots adjacent                      | $N \le 0$  |  |  |
|  | 3 dots adjacent                      | $N \leq 0$   |  |  |
| Distance   | Minimum Distance Between Bright dots | 5mm  |  |  |
|  | Random                               | $N \le 4$  |  |  |
| Dark dot   | 2 dots adjacent                      | $N \le 0$  |  |  |
|  | 3 dots adjacent                      | $N \le 0$  |  |  |
| Total bright and dark dot  |                                      | $N \le 6$  |  |  |
| Distance Minimum Distance Between dark dots Minimum Distance Between dark and bright dot.                  |                                      | 5mm  |  |  |
| Tiny bright dot  |                                      | visible through 5% ND filter $D \le 0.3 mm$ , Ignore $0.3 mm < D \le 0.5 mm$ , $N \le 4$ Distance $\ge 5 mm$ |  |  |
| Display failure (V-line/H-line/Cross line etc.)  |                                      | Not allowable  |  |  |
| Mura/ Waving/ Hot spot  Not visible through 5% ND filter in 50% gray or judge by limit sample if necessary |                                      |  |  |  |

<sup>\*</sup>Note: Defects which is on the Black Matrix (outside of Active Area) are not considered as a defect.

(3) Appearance & Display inspection

| Item                            | Standards  |
|---------------------------------|--|
|                                 | D≤0.3mm , Ignore   |
| Foreign Black/White/Bright Spot | $0.3$ mm $<$ D $\leq$ 0.5mm $, N\leq$ 4                          |
| (Display & Appearance)          | Distance ≥ 5mm   |
|                                 | It is shown in Fig. 2.   |
| Foreign Black/White/Bright Line | W≦0.07 mm , Ignore   |
| (Display & Appearance)          | $0.07 < W \le 0.1 \text{ mm}$ $L \le 5.0 \text{ mm}$ , $N \le 4$ |
|                                 | It is shown in Fig. 3.   |
|                                 | D≤0.3mm, Ignore  |
| Polarizer Dent/Air Bubble       | $0.3$ mm $<$ D $\leq$ 0.5mm $, N\leq$ 4                          |
|                                 | Distance ≥ 5 mm  |
| Polarizer Scratches             | W≤0.07 mm · Ignore   |
| Polarizer Scratches             | $0.07 < W \le 0.1 \text{ mm}$ $L \le 5.0 \text{ mm}$ , $N \le 4$ |



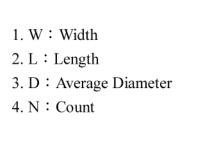
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| - 10 1                           | L  |
|----------------------------------|--|
| Panel Crack                      | Not allowable. It is shown in Fig. 4.  |
|                                  | Min. distance between the broken and dot area,   |
| Broken CF/Non-Lead Side of TFT   | $d_1 \ge 2.0$ mm, is ignored; $d_1 < 2.0$ mm, $N \le 0$ ;  |
| Broken Crytvon-Lead Side of 11 1 | It is shown in Fig. 5.   |
|                                  | d <sub>1</sub> : Minimum distance between the broken and dot area  |
|                                  | Alignment mark damage not allowed, and breakage corner >90°  |
|                                  | $1.W \le 1.5$ mm, L is ignore but the lead can't been damaged.   |
| Broken of TFT Lead Side          | 2.Sconchoidal breakage: $W \le 1.5$ mm, Depth(D) $\le 1/2$ one layer   |
|                                  | of glass thickness   |
|                                  | It is shown in Fig.6.  |
|                                  | 1)The cross mark can't been damaged.   |
| Corner Broken of TFT Lead Side   | $ 2)$ W $\leq 1.5$ mm, L $\leq 5$ mm   |
|                                  | It is shown in Fig. 7.   |
|                                  | The distance of burr from the edge of TFT /CF, $d_2 \le 0.2$ mm.   |
| Burr of TFT/CF Edge              | It is shown in Fig. 8.   |
|                                  | d <sub>2</sub> : The distance of burr from the edge of TFT/CF  |
| PCBA Components                  | External appearance is ignored   |
| COF                              | External appearance is ignored   |
| Silicone spread                  | External appearance is ignored   |
| Polarizer Protective Film        | Neglect any defect on the Polarizer Protective Film, such as protective film scratches, protective film bubbles, and particles on protective film. |

Notes: 1. All the angle of the broken must be larger than 90⊠It is shown in Fig. 9 (R>90⊠

2. If any specific defect is not included in the above defect table, this defect should be judged by INX/ODM/Brand customer discussion.



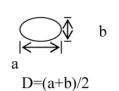
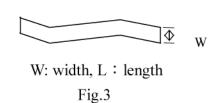
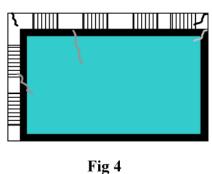
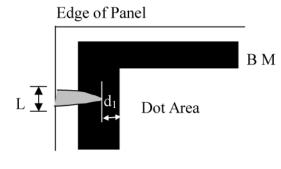


Fig. 2









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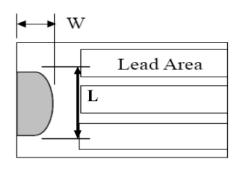


Fig 6

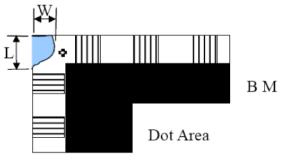


Fig 7

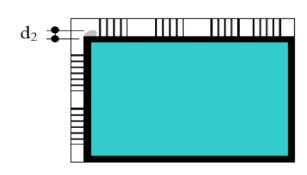


Fig 8

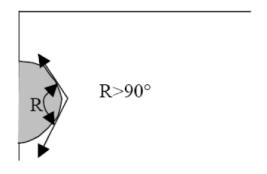


Fig 9



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### 12. Handling Precautions

### 12.1 Mounting method

The LCD panel of AMSON TFT module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

#### 12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (CI), Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

#### 12.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to power or ground, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

#### 12.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

#### 12.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.
  - Usage under the maximum operating temperature, 50%Rh or less is required.



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#### 12.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
   [It is recommended to store them as they have been contained in the inner container at the time of delivery from us.

#### 12.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

#### 13. Precaution for Use

#### 13.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

#### 13.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification.
- When a new problem is arisen this is not specified in this specification.
- When an inspection specifications change or operating condition change in customer is reported to AMSON TFT and some problem is arisen in this specification due to the change.
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

## 14. Packing Method TBD