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Specification for Approval

| Customer: | |
|-------------|--|
| Model Name: | |

| Supplier Approval | | | Customer approval |
|-------------------|--------------|-------------|-------------------|
| R&D Designed | R&D Approved | QC Approved | |
| Peter | Peng Jun | | |

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Revision Record

| REV NO. | REV DATE | CONTENTS | Note |
|---------|------------|-----------|------|
| Α | 2023-03-07 | NEW ISSUE | |
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1. Scope

This specification defines general provisions as well as inspection standards for TFT module supplied by AMSON electronics.

If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution.

2. General Information

LCM

| ITEM | STANDARD VALUES | UNITS |
|-----------------------|-----------------------|-------|
| LCD type | 10.1"TFT | |
| Dot arrangement | 1024×3 (RGB)×600 | dots |
| Color filter array | RGB vertical stripe | |
| Display mode | Normally BLACK | |
| Viewing Direction | ALL VIEWING | |
| Module size | 235(W)×143(H)×6.45(T) | mm |
| Active area | 222.72(W)×125.28(H) | mm |
| Dot pitch | 0.2175(W)×0.2088(H) | mm |
| Interface | LVDS | |
| Operating temperature | -20 ~ +70 | °C |
| Storage temperature | -30 ~ +80 | °C |

CTP

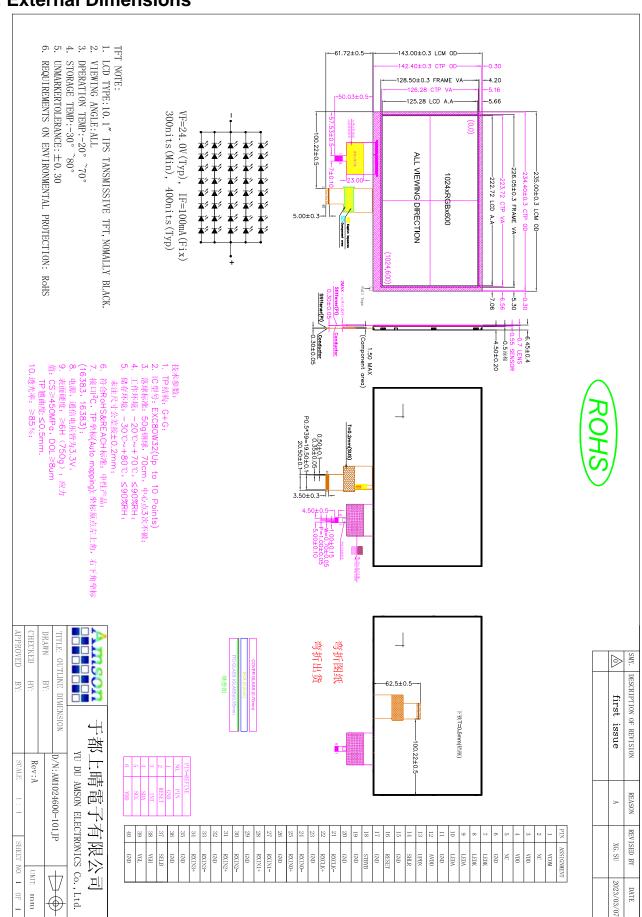
| ITEM | STANDARD VALUES | UNITS |
|--------------------|---------------------------|-------|
| CTP type | Cover Lens + sensor + FPC | |
| CTP Driver IC | EXC80W32 | |
| Transmittance | ≥85% | |
| The cover hardness | ≥6H | |
| CTP size | 235W)×143(H)×2.0(T) | mm |
| CTP Viewing area | 223.72(W)×126.28(H) | mm |
| CTP Interface | I2C | |
| channel number | 10 | |



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3. External Dimensions





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4. Interface Dimensions

| No. | Symbol | I/O | Function | | | |
|-----|--------|-----|--|--|--|--|
| 1 | VCOM | Р | Common voltage | | | |
| 2 | NC | - | Not connect | | | |
| 3 | VDD | Р | Distribution | | | |
| 4 | VDD | Р | Digital power | | | |
| 5 | NC | - | Not connect | | | |
| 6 | GND | Р | Ground | | | |
| 7 | LED K | Р | .ED backlight (Cathode). | | | |
| 8 | LED K | Р | LED backlight (Cathode). | | | |
| 9 | LED A | Р | LED backlight (Anode). | | | |
| 10 | LED A | Р | LED backlight (Anode). | | | |
| 11 | GND | Р | Ground | | | |
| 12 | AVDD | Р | Power supply for analog circuits | | | |
| 13 | UPDN | I | Gate Up or Down scan control. Normally pull low. | | | |
| 14 | SHLR | I | Source Right or Left sequence control. Normally pull high. | | | |
| 15 | GND | Р | Ground | | | |
| 16 | RESET | I | Active Low to enter Reset State. Normally pull high. | | | |
| 17 | GND | Р | Ground | | | |
| 18 | STBYB | I | Standby mode, Normally pull high. | | | |
| 19 | GND | Р | Ground | | | |
| 20 | GND | Р | Ground | | | |
| 21 | RXCLK- | I | legative LVDS differential clock inputs | | | |
| 22 | RXCLK+ | I | Positive LVDS differential clock inputs | | | |
| 23 | GND | Р | Ground | | | |
| 24 | RXIN0- | I | Negative LVDS differential data inputs | | | |
| 25 | RXIN0+ | I | Positive LVDS differential data inputs | | | |
| 26 | GND | Р | Ground | | | |
| 27 | RXIN1- | I | Negative LVDS differential data inputs | | | |
| 28 | RXIN1+ | I | Positive LVDS differential data inputs | | | |
| 29 | GND | Р | Ground | | | |
| 30 | RXIN2- | I | Negative LVDS differential data inputs | | | |
| 31 | RXIN2+ | I | Positive LVDS differential data inputs | | | |
| 32 | GND | Р | Ground | | | |
| 33 | RXIN3- | I | Negative LVDS differential data inputs | | | |
| 34 | RXIN3+ | I | Positive LVDS differential data inputs | | | |
| 35 | GND | Р | Ground | | | |
| 36 | GND | Р | Ground | | | |
| 37 | SELB | I | LVDS Bit Set:SELB="L":8 bit;SELB="H":6 bit | | | |
| 38 | VGH | Р | Positive power for TFT | | | |
| 39 | VGL | Р | Negative power for TFT | | | |
| 40 | GND | Р | Ground | | | |

I : input , O : output , P : Power



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CTP

| Pin | Symbol | Description. | | |
|-----|--------|---|--|--|
| 1 | GND | Power ground | | |
| 2 | RESET | CTP reset pin. Active low to enter reset state. | | |
| 3 | INT | CTP interruption signal. | | |
| 4 | SDA | CTP I2C_data. | | |
| 5 | SCL | CTP I2C_clock. | | |
| 6 | VDD | Power supply. | | |

5. Absolute Maximum Ratings

| 5. Absolute Maximum Ratings | | | | | |
|-----------------------------|--------|------|------|------|--|
| Item | Symbol | Min. | Max. | Unit | |
| Digital Supply Voltage | VDD | -0.5 | 5 | V | |
| Analog Supply Voltage | AVDD | -05 | 15 | V | |
| Gate On Voltage | VGH | -0.5 | 40 | V | |
| Gate Off Voltage | VGL | -20 | 0.3 | V | |
| Operating Temperature | Тор | -20 | 70 | °C | |
| Storage Temperature | Тѕт | -30 | 80 | °C | |
| Storage Humidity | HD | 10 | 90 | %RH | |

6. DC Characteristics

| Item | Symbol | Min. | Тур. | Max. | Unit | Remark |
|------------------------|--------|--------|-------|--------|------|--------|
| Digital Supply Voltage | VDD | 3.0 | 3.3 | 3.6 | V | - |
| Analog Supply Voltage | AVDD | 11.7 | 12.2 | 12.7 | V | - |
| Gate On Voltage | VGH | 19.0 | 22.0 | 25.0 | V | - |
| Gate Off Voltage | VGL | -13.0 | -10.0 | -7.0 | V | - |
| Common Voltage | VCOM | 4.39 | 5.39 | 6.39 | V | NOTE1 |
| Analog Supply Current | VDD | - | 30 | 40 | mA | |
| Analog Supply Current | AVDD | - | 20 | 40 | mA | |
| Analog Supply Current | VGH | - | 5 | 10 | mA | |
| Analog Supply Current | VGL | - | 5 | 10 | mA | |
| Analog Supply Current | VCOM | - | 1 | 3 | mA | |
| Logic Input Voltage | VIH | 0.7VDD | - | VDD | V | - |
| Logic input voitage | VIL | GND | _ | 0.3VDD | V | - |

NOTE1: VCOM voltage depends on the actual effect of the customer's main board

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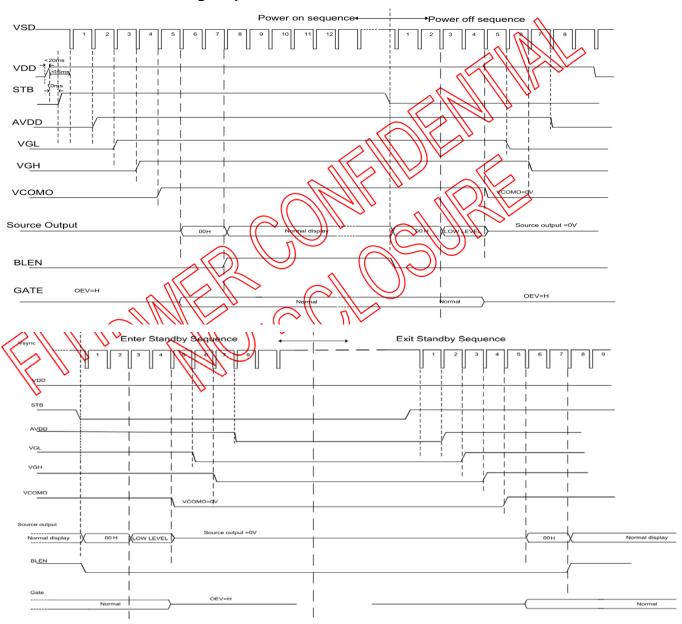
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7. Timing Characteristics

7.1 Power Sequence

In order to prevent IC from power on reset fail, the rising time (TPOR) of the digital power supply VDD should be maintained within the given specifications. Refer to "AC Characteristics" for more detail on timing.

7.1.1 Power on/off timing sequence





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7.2 AC Electrical Characteristics

| LVDS mode | | |
|-----------|--|--|
|-----------|--|--|

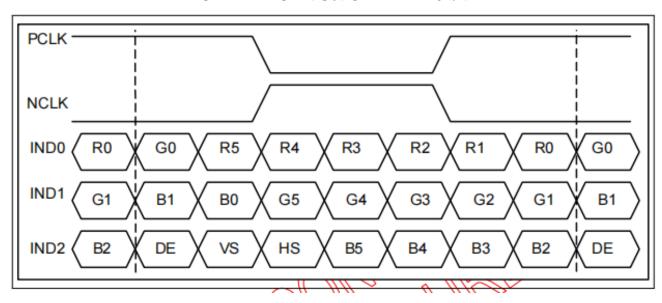
| Parameter | Symbol | Condition | Min. | Тур. | Max. | Unit |
|------------------------|----------------|--|------|---------------|------|------|
| Clock Frequency | R xFCLK | | 20 | - | 71 | MHz |
| Input data skew margin | TRSKM | WIDL=400mV RxXCM=1.2V RxFCLK=71MHz | 500 | | | ps |
| Clock High Time | TAVCH | | | 4/(7* RxFCLK) | | ns |
| Clock right little | Marian | | | 47(7 TOURDER) | | ns |
| Clock Low Time | TLVCL | | | 3/(7* RxFCLK) | | ns |
| PLL wake-up-time | TenPLL | | | | 150 | us |

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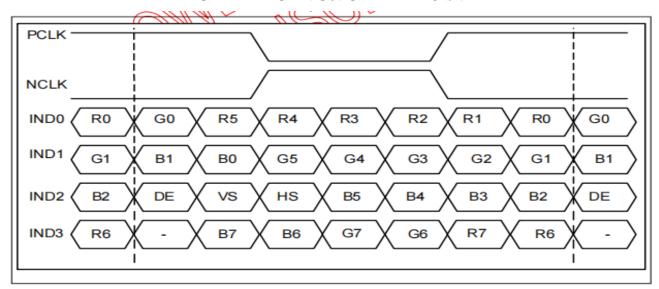
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7.3Data Input Format for LVDS

SELB LVDS Bit Set:SELB="H":6 bit



SELB LVDS Bit Set:SELB="L":8 bit



Timing Characteristic

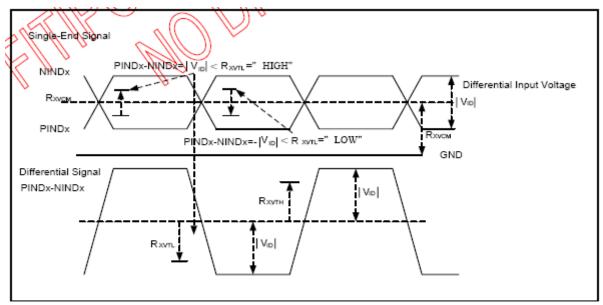
| Parameter | Cumhal | | Value | Value | |
|---------------------------------|----------|------|-------|-------|------|
| Parameter | Symbol | Min. | Тур. | Max. | Unit |
| DCLK frequency @Frame rate=60hz | fclk | 40.8 | 51.2 | 67.2 | Mhz |
| Horizontal display area | thd | | 1024 | | DCLK |
| HSYNC period time | th | 1114 | 1344 | 1400 | DCLK |
| HSYNC blanking | thb+thfp | 90 | 320 | 376 | DCLK |
| Vertical display area | tvd | | /600/ | 111 | Н |
| VSYNC period time | tv | 610 | 11835 | 800 | Н |
| VSYNC blanking | tvb+tvfp | | 85 | 200 | Н |

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7.4 LVDS DC characteristic

| Parameter | Symbol | Min. | Тур. | Max. | Unit | Condition |
|-------------------|------------------------|---------------------|-----------|---|--|---------------|
| Differential | Rx∨TH | | | +0.1V | V | RxVCM=1.2V |
| input high | | | | | | |
| threshold | | | | | | |
| voltage | | | | | | |
| Differential | Rx∨TL | -0.1 | | | V | |
| input low | | | | | | |
| threshold | | | | | _ < | |
| voltage | | | | | M n | |
| Input voltage | Rx∀IN | 0 | | 2.4 | \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\ | |
| range(single-end) | | | | | 0~11 1111 | · |
| Differential | Rx∀c M | V _{ID} /2 | | 2.4 - V _{ID} /2 | 1 N 11 m | |
| input common | | | | _ << | 11/41 | |
| mode voltage | | | | | | |
| Differential | V _{ID} | 0.2 | | J/ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ | | |
| input voltage | | | 20 | | | |
| Differential | Rx∨th | -10 | \sim | 6 /4 | | |
| input leakage | | | | o = UL | | |
| current | | 0 | IIII IIc | 2 , | | |
| LVDS Digital | Iddlvsd | - ((| 40(TBD) | 500 |)) | Fclk=65Mhz, |
| Operating | | | ~ 0 | |)) | VDD=3.3V |
| Current | | |) | | | |
| LVDS Digital | lstlvds ₍ (| | 10(TBD) | 50 | uA | Clock & all |
| Standby | 11 10 | 150 | | | | functions are |
| Current | | | | V | | stop |

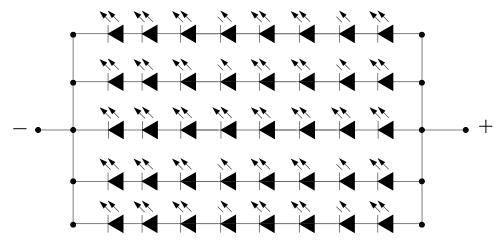


LVDS DC Characteristic

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8. Backlight Characteristic



| Item | Symbol | MIN | TYP | MAX | UNIT | TestCondition |
|----------------------------|--------|-----|-------|------|-------------------|---------------|
| Supply Voltage | Vf | 22 | 24 | 26.2 | V | If=100mA |
| Supply Current | If | - | 100 | - | mA | - |
| Luminous Intensity for LCM | - | 300 | 400 | - | cd/m ² | If=100mA |
| Uniformity for LCM | - | 75 | - | - | % | If=100mA |
| Life Time | - | | 30000 | - | Hr | If=100mA |



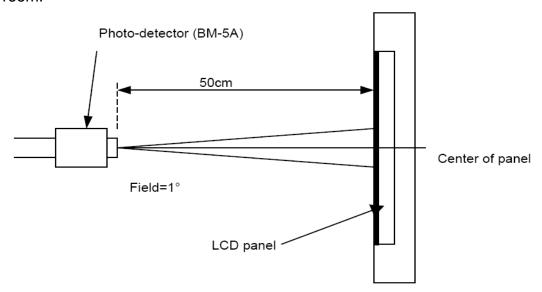
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9. Optical Characteristics

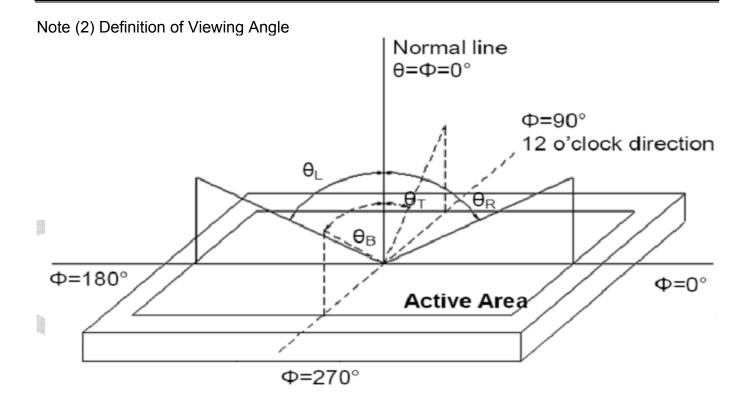
| Item | Conditions | | Min. | Тур. | Max. | Unit | Note | |
|---------------------------|--------------|------|-------|-------|---------------|--------|-------------|--|
| | Horizontal | θL | 80 | 85 | - | | | |
| Viewing Angle | Horizoniai | θR | 80 | 85 | - | | (4) (2) (6) | |
| (CR>10) | Vertical | θт | 80 | 85 | - | degree | (1),(2),(6) | |
| | vertical | θв | 80 | 85 | - | | | |
| Contrast Ratio | Center | | 600 | 800 | - | - | (1),(3),(6) | |
| Response Time | Rising + Fal | ling | - | 30 | 45 | ms | (1),(4),(6) | |
| | Red x | | | 0.60 | - | - | | |
| | Red y | | | 0.36 | | - | | |
| | Green x | | | 0.32 | | - | | |
| CF Color | Green y | | Тур. | 0.56 | Typ. +0.05 | - | (1) (6) | |
| Chromaticity (CIE1931) | Blue x | | -0.05 | 0.13 | | - | (1), (6) | |
| | Blue y | | | 0.059 | | - | | |
| | White x | | | 0.28 | | - | | |
| | White y | | | 0.32 | | _ | | |

Note (1) Measurement Setup: The LCD module should be stabilized at given temp. 25°C for 15 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 15 minutes in a windless room.



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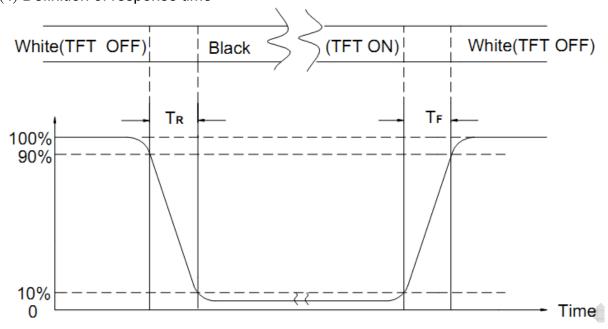


Note (3) Definition of Contrast Ratio (CR)

The contrast ratio can be calculated by the following expression Contrast Ratio (CR) = L63 / L0

L63: Luminance of gray level 63, L0: Luminance of gray level 0

Note (4) Definition of response time



Note (5) Definition of Transmittance (Module is without signal input)

Transmittance = Center Luminance of LCD / Center Luminance of Back Light x 100%

Note (6) Definition of color chromaticity (CIE1931)

Color coordinates measured at the center point of LCD



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10. Reliability Test Conditions and Methods

| NO. | TEST ITEMS | TEST CONDITION | | | | |
|-----|---|--|--|--|--|--|
| 1) | High Temperature Storage | Keep in 80°C ±5°C96hrs | | | | |
| 2 | Low Temperature Storage | Keep in -30°C±5°C96hrs | | | | |
| 3 | High Temperature / High Humidity Storage Test | Keep in 60 $^{\circ}$ / 90% R.H duration for 96hrs (Excluding the polarizer) | | | | |
| 4 | Temperature Cycling Storage Test | $-30^{\circ}\text{C} \rightarrow +25^{\circ}\text{C} \rightarrow 80^{\circ}\text{C} \rightarrow +25^{\circ}\text{C}$ (30\text{mins}) (5\text{mins}) (30\text{mins}) (5\text{mins}) 20 Cycle | | | | |
| (5) | ESD Test | Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance : 15 °C ~35 °C 2. Humidity relative : 30% ~60% 3. Energy Storage Capacitance(Cs + Cd) : 150pF±10% 4. Discharge Resistance(Rd) : 330Ω±10% 5. Discharge, mode of operation : Single Discharge (time between successive discharges at least | | | | |
| 6 | Vibration Test (Packaged) | (Tolerance if the output voltage indication :±5%) Sine wave 10~55 Hz frequency (1 min/sweep) The amplitude of vibration :1.5 mm Each direction (X、Y、Z) duration for 2 Hrs | | | | |
| 7 | Drop Test (Packaged) | Packing Weight (Kg) Drop Height (cm) 0 ~ 45.4 122 45.4 ~ 90.8 76 90.8 ~ 454 61 Over 454 46 Drop Direction: **1 corner / 3 edges / 6 sides each 1time | | | | |



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11. Inspection Standard

11.1. Quality

The quality of goods supplied to purchaser shall come up to the following standards:

11.1.1. Inspection Tools and Instruments

Vernier calipers, film scales, multimeter, magnifying eyepiece, ND5%, luminance meter and so on.

11.1.2. The Method of Preserving Goods

After delivery of goods from UNI Display to purchaser, purchaser shall keep the LCM at -10°C to30°C, and it might be desirable to keep at the normal room temperature and humidity until incoming inspection or throwing into process line.

11.1.3. Incoming Inspection

(A) The methods of Inspection

If purchaser makes an incoming inspection, a sampling plan shall be applied on The condition that quality of one delivery shall be regarded as one lot.

(B) The standard of quality:

ISO-2859-1 (same as MIL-STD-105E), Level: II

| , | |
|----------|---------|
| CLASS | AQL (%) |
| Critical | 0.4 % |
| Major | 0.65 % |
| Minor | 1.5 % |

Every item shall be inspected according to the class.

(C) Measure

If as the result of above receiving inspection, a lot out is discovered, purchaser Shall inform seller of it within seven days. But first shipment within fourteen days.

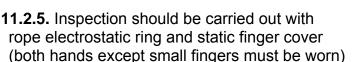
11.1.4. Warranty Policy

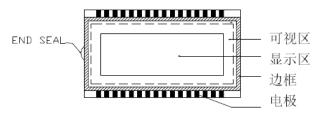
UNI Display will provide one-year warranty for the products only if under Specification operating conditions. UNI Display will replace new products for these defect products which are under warranty period and belong to the responsibility of UNI Display.

11.2. CHECKING CONDITION

- **11.2.1.**Checking direction shall be in the 45 degree area to face the sample.
- 11.2.2.Inspector shall see from over 300±25mm with bare eyes far from the sample.
- 11.2.3. Ambient Illumination:
 - 0 ~30 Lux for functional inspection
 - 500 ~ 1200 Lux for external appearance inspection.

11.2.4. TEST AREA:







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- **11.2.6.** The inspector may make a visual inspection or a comparative examination with a film ruler and a magnifying eyepiece. Individual defects shall be determined according to the limited samples.
- **11.2.7.** Functional testing uses electrical testing fixtures or test fixtures required by customers.
- **11.2.8.** The ion fan should be used when testing.

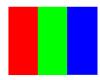
11.2.9. The principle of judgment

11.3.1 If the defect outside the visual area does Not affect the assembly and display, It will be judged as a good product.

11.3.2 Poor definition

Pixel:

A combination of three sub-pixels (Red + Green + Blue).



Dot:

Any of the sub-pixels (Red or Green or Blue).







Bright and dark dots:

A point pixel (sub-pixel: R, G, B pixels) is lit or turned off during the display function test. **Highlights**:

Usually considered to be shown on a black screen.

Dark spots:

They are generally considered to be shown on R, G, B solid colors or white images.

Neighborhood:

Two or three adjacent point pixels (dot: sub-pixel) connected together (R, G or G, B or B, R or RGB).



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11.3. INSPECTION PLAN:

| 11.0. 11401 E0 | TION PLAN : | | |
|----------------|--|---|----------|
| CLASS | ITEM | JUDGEMENT | CLASS |
| PACKING & | 1. OUTSIDE AND INSIDE PACKAGE | "MODEL NO." , "LOT NO." AND "QUANTITY" SHOULD INDICATE ON THE PACKAGE. | Minor |
| INDICATE | 2. MODEL MIXED AND QUANTITY | OTHER MODEL MIXEDREJECTED QUANTITY SHORT OR OVERREJECTED | Critical |
| | 3. PRODUCT INDICATION | "MODEL NO." SHOULD INDICATE ON THE PRODUCT | Major |
| ASSEMBLY | 4. DIMENSION, LCD GLASS SCRATCH AND SCRIBE DEFECT. | ACCORDING TO SPECIFICATION OR DRAWING. | Major |
| | 5. VIEWING AREA | POLARIZER EDGE OR LCD'S SEALING LINE IS VISABLE IN THE VIEWING AREAREJECTED | Minor |
| | 6. BLEMISH - BLACK SPOT - WHITE SPOT IN THE LCD AND LCD GLASS CRACKS | ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA) | Minor |
| APPEARANCE | 7. BLEMISH - BLACK SPOT WHITE SPOT AND SCRATCH ON THE POLARIZER | ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA) | Minor |
| | 8. BUBBLE IN POLARIZER | ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA) | Minor |
| | 9. LCD'S RAINBOW COLOR | STRONG DEVIATION COLOR (OR NEWTON RING) OF LCDREJECTED. OR ACCORDING TO LIMITED SAMPLE (IF NEEDED, AND INSIDE VIEWING AREA) | Minor |
| | 10. ELECTRICAL AND OPTICAL CHARACTERISTICS (CONTRAST, VOP, CHROMATICITY ETC) | ACCORDING TO SPECIFICATION OR DRAWING . (INSIDE VIEWING AREA) | Critical |
| ELECTRICAL | 11.MISSING LINE | MISSING DOT: LINE : CHARACTERREJECTED | Critical |
| | 12.SHORT CIRCUIT WRONG PATTERN DISPLAY | NO DISPLAY - WRONG PATTERN DISPLAY - CURRENT CONSUMPTION OUT OF SPECIFICATION REJECTED | Critical |
| | 13. DOT DEFECT (FOR COLOR AND TFT) | ACCORDING TO STANDARD OF VISUAL INSPECTION | Minor |



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11.4. STANDARD OF VISUAL INSPECTION

| NO. | CLASS | ITEM | | JUD | GEMENT | |
|-----------|---------------------------------|------------------------|--|-----------------|---|--|
| | | | (A) ROUN | ID TYPE: | unit: mm | |
| | | DIA | METER (mm.) | ACCEPTABLE Q'TY | | |
| | WHIT | BLACK AND | Ø≤ 0 .2 | | Disregard≥ 1mm | |
| | | WHITE SPOT FOREIGN | 0.2< | ⊘≤ 0.4 | 3 (Distance ≥ 15mm) | |
| 11.4.1 | | MATERIEL DUST | 0.4 | <Ø | 0 | |
| | | IN THE CELL BLEMISH | NOTE | E: ∅=(LENGTH*W | IDTH)/2 | |
| | | SCRATCH | (S) LINEA | AR TYPE: | unit: mm | |
| | | | LENGTH | WIDTH | ACCEPTABLE QTY | |
| | | | | W≤ 0.05 | Disregard≥ 1mm | |
| | | | L ≤4.0 | 0.05< W ≤ 0.07 | 3 (Distanced ≥ 15mm | |
| | | | | 0.07< W | FOLLOW ROUND TYPE | |
| | | | | | | |
| | | | | T | unit: mm. | |
| 11.4.2 | 1.4.2 MINOR BUBBLE IN POLARIZER | | DIAMETER | | ACCEPTABLE Q'TY | |
| | | DENT ON | Ø<0.2 | | Disregard≥ 1mm | |
| POLARIZER | | | 0.2<∅≤ 0.5 | | 2(Distance≥ 15mm) | |
| | | | 0.5<∅ | | 0 | |
| | | | | | | |
| | | | Items | | ACC. Q'TY | |
| | | | Bright dot | | N ≤2(Distance ≥ 15mm) | |
| | 11.4.3 MINOR Dot Defect | | Dark dot | | N ≤2(Distance ≥ 15mm) | |
| 11.4.3 | | | | Pixel | | |
| | | | Note: The definition of dot: The size of a defective dot over ofwhole dot is regarded as one defective dot. Definition:<1/2 dot and visible by 5% ND filter Bright dot: Dots appear bright and unchanged in size which LCD panel is displaying under black pattern. Dark dot: Dots appear dark and unchanged in six which LCD panel is displaying under pure Red, G Blue pattern. | | as one defective dot. visible by 5% ND filter bright and unchanged in size m ng under black pattern. dark and unchanged in size | |
| 11.4.4 | MINOR | Mura | Not visible through 5% ND filter in 50% gray or judge by limit sample if necessary | | | |



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| NO. | CLASS | ITEM | JUDGEMENT |
|---------|-------|---|--|
| 11.4.5 | MINOR | LCD GLASS CHIPPING | X ≥ 3mm Y > S Reject |
| 11.4.6 | MINOR | LCD GLASS CHIPPING | X or Y > S |
| 11.4.7 | MAJOR | LCD GLASS GLASS CRACK | Continuous burst NG Reject |
| 11.4.8 | MAJOR | LCD GLASS SCRIBE DEFECT | ACCORDING TO DIMENSION |
| 11.4.9 | MINOR | LCD GLASS CHIPPING (ON THE TERMINAL AREA) | Y<1/2Z $Y \ge 0.5 \text{mm}_{\text{Reject}}$ $X \ge 3 \text{mm}$ |
| 11.4.10 | MINOR | LCD GLASS CHIPPING (ON THE TERMINAL SURFACE) | $Y<1/2Z$ $Y \ge 0.5 mm$ $X \ge 3 mm$ |
| 11.4.11 | MINOR | LCD GLASS CHIPPING | $X\geqslant 3mm$ $Y\geqslant T\qquad \text{Reject}$ $Z\qquad \text{If touch the electrode lines,}$ the need to retain the two-thirds electrode lines |



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12. Handling Precautions

12.1 Mounting method

The LCD panel of AMSON TFT module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (CI), Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

12.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to power or ground, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

12.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

12.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.
 - Usage under the maximum operating temperature, 50%Rh or less is required.



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12.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
 [It is recommended to store them as they have been contained in the inner container at the time of delivery from us.

12.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

13. Precaution for Use

13.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

13.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification.
- When a new problem is arisen this is not specified in this specification.
- When an inspection specifications change or operating condition change in customer is reported to AMSON TFT and some problem is arisen in this specification due to the change.
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

14. Packing Method TBD