Specification for Approval

Customer:_____

Model Name:_____

Supplier Approval			Customer approval
R&D Designed	R&D Approved	QC Approved	
Peter	Peng Jun		

Revision Record

REV NO.	REV DATE	CONTENTS	Note
A	2022-09-07	NEW ISSUE	

Table of Contents

1. Scope

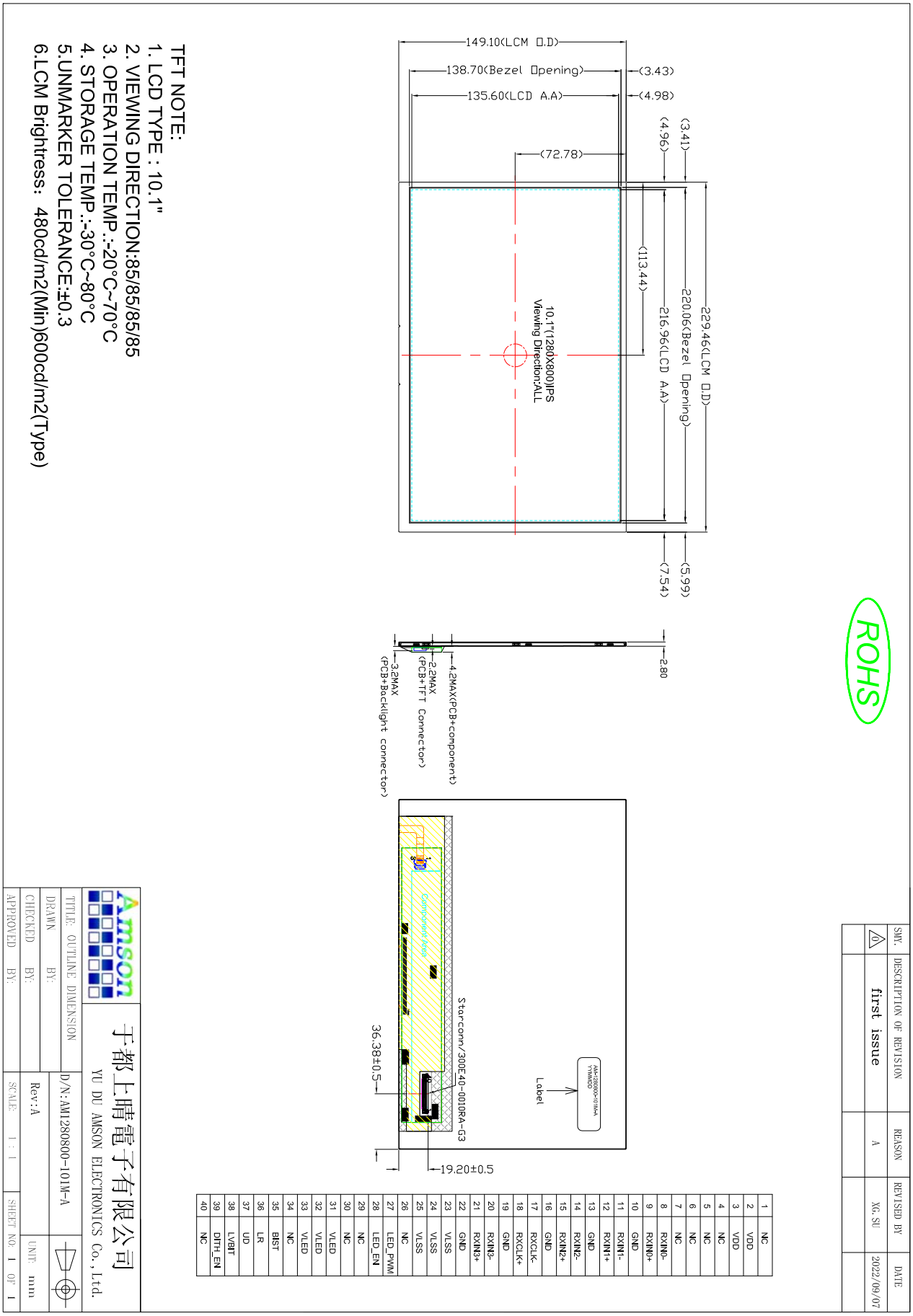
This specification defines general provisions as well as inspection standards for TFT module supplied by AMSON electronics.

If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution.

2. General Information

ITEM	STANDARD VALUES	UNITS
LCD type	10.1''TFT	--
Dot arrangement	1280×3(RGB)×800	dots
Color filter array	RGB vertical stripe	--
Display mode	Normally Black	-
Viewing Direction	85/85/85/85	--
Module size	229.46(W)×149.1(H)×2.8(T)	mm
Active area	216.96(W)×135.60(H)	mm
Dot pitch	0.1695(W)×0.1695(H)	mm
Interface	LVDS 8bit or 6bit Interface	--
Operating temperature	-20 ~ +70	°C
Storage temperature	-30 ~ +80	°C
Weight	222	g

3. External Dimensions



1	NC	11	RXIN1-	21	RXIN3+	31	VLED	40	NC
2	VDD	12	RXIN1+	22	GND	32	VLED	39	DITH_EN
3	VDD	13	GND	23	VLSS	33	VLED	38	LVBIT
4	NC	14	RXIN2-	24	VLSS	34	NC	37	UD
5	NC	15	RXIN2+	25	VLSS	35	BIST	36	LR
6	NC	16	GND	26	NC	36	LR	35	BIST
7	NC	17	RXCLK-	27	LED_PWM	37	UD	34	NC
8	RXIN0-	18	RXCLK+	28	LED_EN	38	LVBIT	33	VLED
9	RXIN0+	19	GND	29	NC	39	DITH_EN	32	VLED
10	GND	20	RXIN3-	30	NC	40	NC	31	VLED

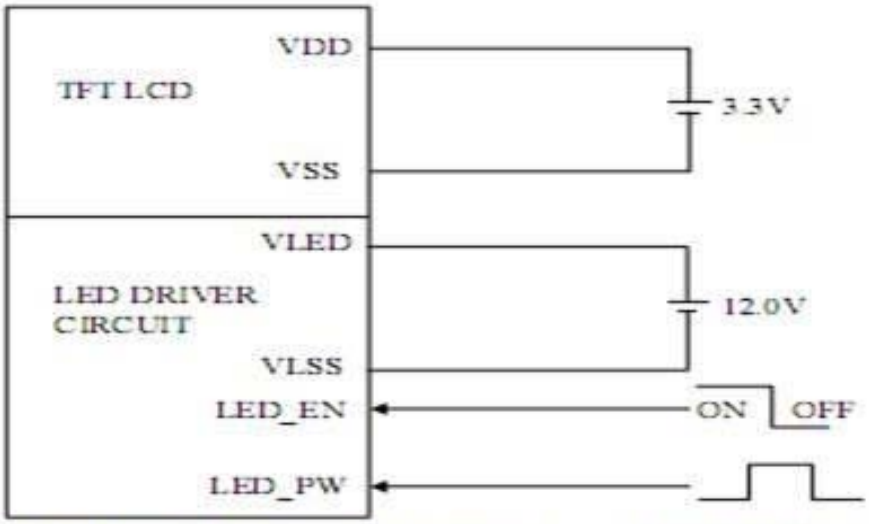
4. Interface Description

PIN	PIN NAME	DESCRIPTION	Remark
1	NC	No connection	
2-3	VDD	Power Supply	
4-7	NC	No connection	
8	RXIN0-	-LVDS Differential Data Input	R0~R5,G0
9	RXIN0+	+LVDS Differential Data Input	
10	GND	Ground	
11	RXIN1-	-LVDS Differential Data Input	G1~G5,B0 , B1
12	RXIN1+	+LVDS Differential Data Input	
13	GND	Ground	
14	RXIN2-	-LVDS Differential Data Input	B2~B5,HS, VS,DE
15	RXIN2+	+LVDS Differential Data Input	
16	GND	Ground	
17	RXCLK-	-LVDS Differential Clock Input	LVDS CLK
18	RXCLK+	+LVDS Differential Clock Input	
19	GND	Ground	
20	RXIN3-	-LVDS Differential Data Input	R6,R7,G6, G7,B6,B7
21	RXIN3+	+LVDS Differential Data Input	
22	GND	Ground	
23-25	VLSS	Ground	
26	NC	No connection	
27	LED_PWM	CABC controller signal output for backlight	
28	LED_EN	CABC Enable Input	
29	NC	No connection	
30	NC	No connection	
31-33	VLED	Power Supply for LED Backlight Driver	
34	NC	No connection	
35	BIST	H: Normal Operation/ L: BIST pattern select. (Internal pull Hi)	
36	LR	When LR="0", set right to left scan direction (Internal pull Low) When LR="1", set left to right scan direction	
37	UD	When UD="0", set top to bottom scan direction (Internal pull Low) When UD="1", set bottom to top scan direction	
38	LVBIT	input select for LVDS mode. H: 8bit / L: 6bit (Internal pull Hi)	
39	DITH_EN	Dithering function enable control. Normally pull low In LVDS 6-bit mode, IC don't care DITHER and HFRC setting. H: enable internal dithering function (Internal pull Hi) L: disable internal dithering function	
40	NC	No connection	

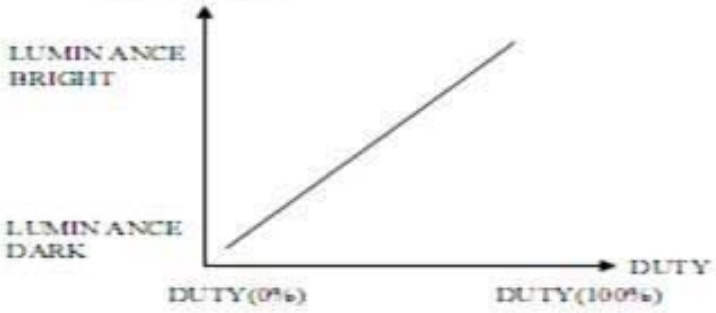
5. Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit	Remark
Digital Supply Voltage	VDD	-0.3	4.0	V	
VIN Voltage	VLED	-0.3	27	V	
Operating Temperature	**TOP**	-20	70	°C	
Storage Temperature	TST	-30	80	°C	

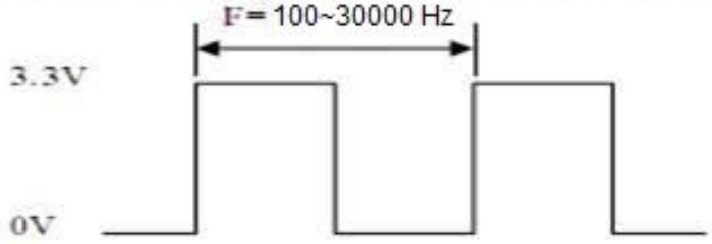
5.1 POWER SUPPLY FOR LCM



NOTE (1) : ADJUST THE PWM SIGNAL IN ORDER TO CONTROL LED BACKLIGHTS BRIGHTNESS. THE HIGHER THE DUTY CYCLE, THE HIGHER THE BRIGHTNESS



NOTE (2) : PWM SIGNAL=0~3.3V，OPERATION FREQUENCY ：100~30000 Hz

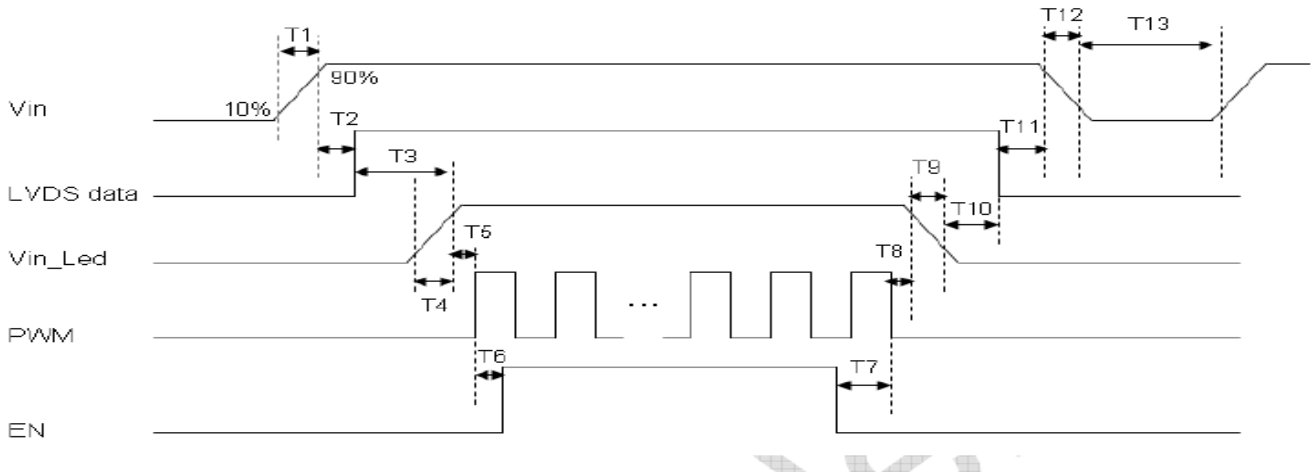


6. DC Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Digital Supply Voltage	VDD	2.75	3.3	3.6	V	-
Backlight Power Voltage	VLED	8	12	15	V	
Digital Supply Current	IDD	-	200	300	mA	VDD=3.3V
Backlight Power Current	I-VLED	-	220	350	mA	VLED=12V
Input logic high voltage	V_{IH}	0.8*VDD	-	VDD	V	-
Input logic low voltage	V_{IL}	GND	-	0.2*VDD	V	
LED_EN Control Level	V_{IH}	1.9		VLED	V	
	V_{IL}	GND		0.8	V	
LED_PWM Control Level	V_{IH}	1.9		VLED	V	
	V_{IL}	GND		0.8	V	
PWM Frequency	LED_PWM	100		30000	Hz	

7. Timing Characteristics

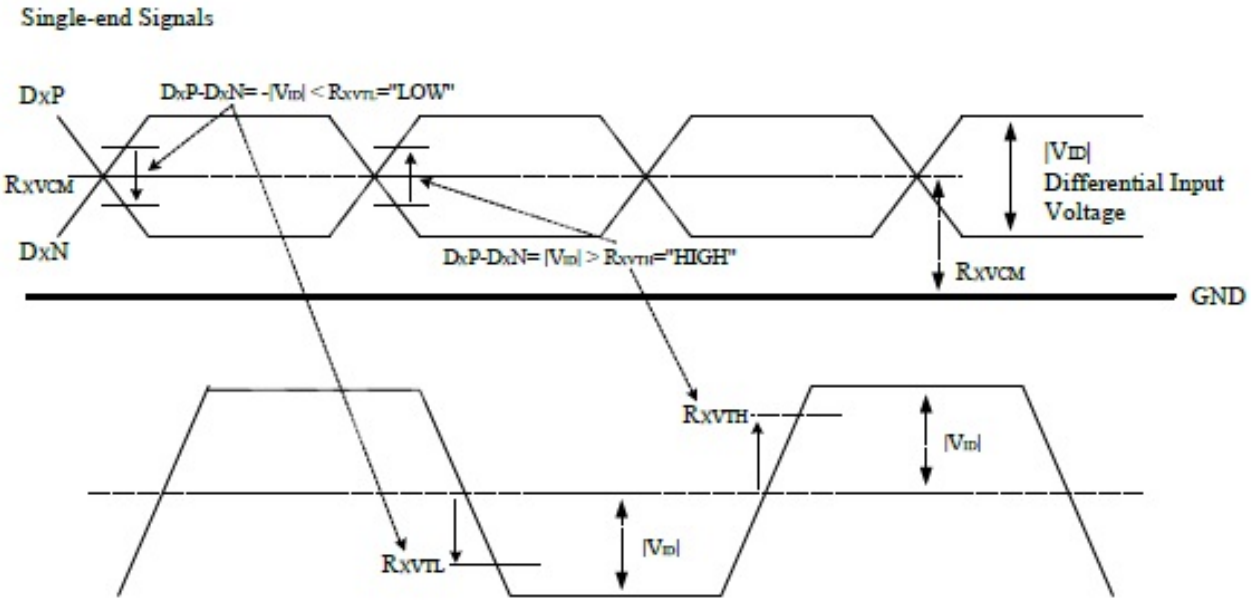
7.1 Power On and Power Off Timing



Parameter	Symbol	Min.	Typ	Max.	Unit
Vin rise time	T1	0.5	-	10	ms
Vin good to signal valid	T2	30	-	90	ms
Signal valid to backlight on	T3	200	-	-	ms
Backlight power on time	T4	0.5	-	-	ms
Backlight VDD good to system PWM on	T5	10	-	-	ms
System PWM on to backlight enable on	T6	10	-	-	ms
Backlight enable of to system PWM off	T7	0	-	-	ms
System PWM off to B/L power disable	T8	10	-	-	ms
Backlight power off time	T9	0.5	10	30	ms
Backlight off to signal disable	T10	200	-	-	ms
Signal disable to power down	T11	0	-	50	ms
VIN fall time	T12	0.5	10	30	ms
Power off	T13	500	-	-	ms

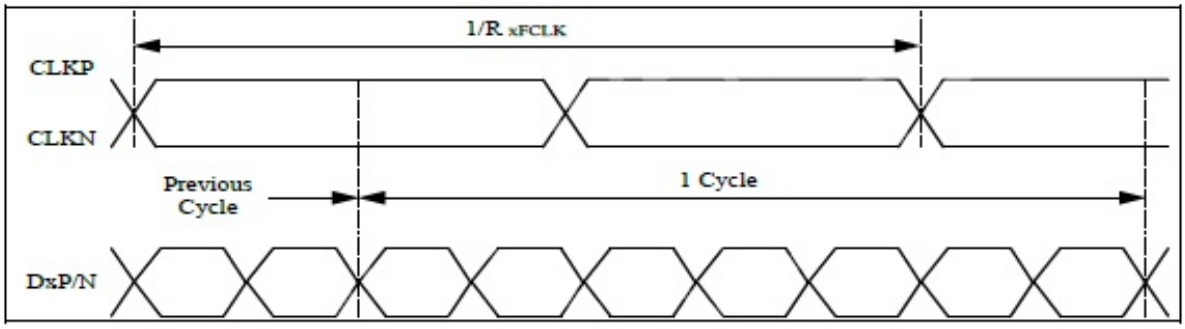
7.2 LVDS Signal Timing Characteristics

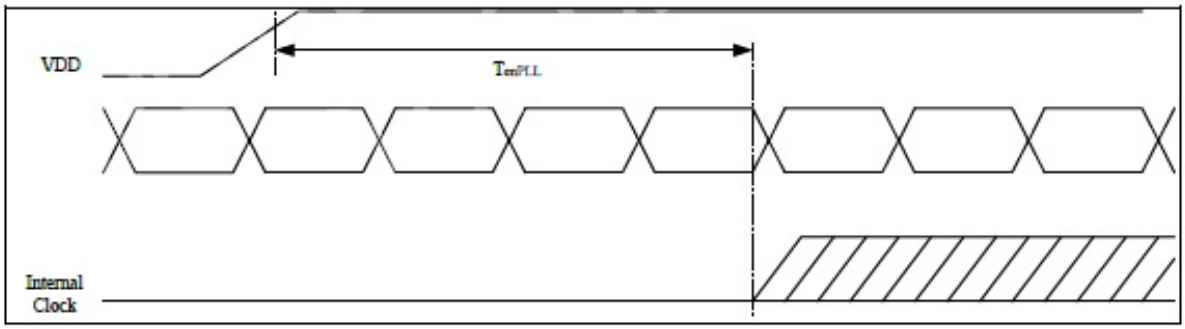
7.2.1 LVDS DC electrical characteristics

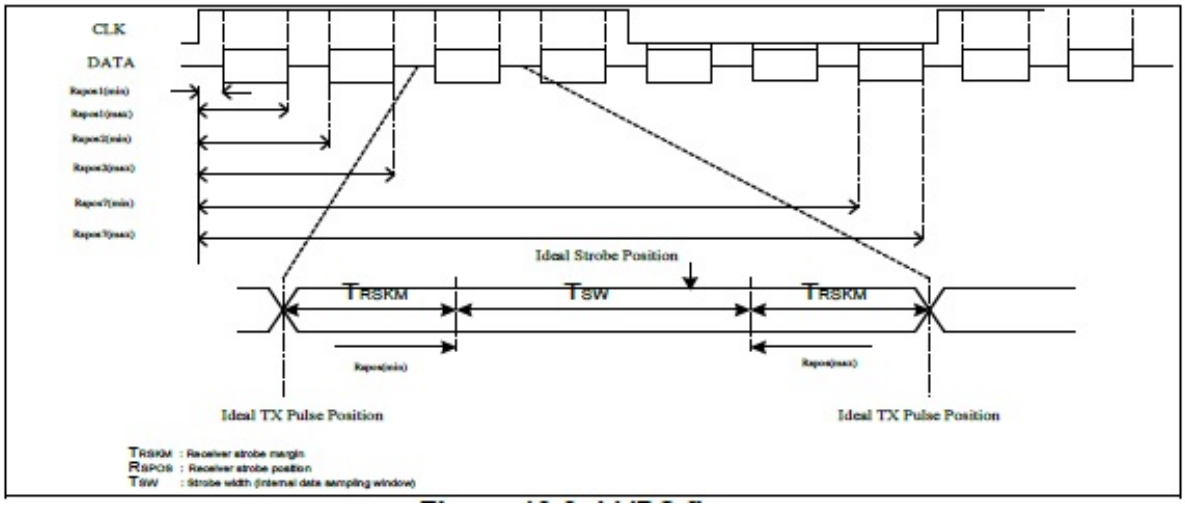


Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
Differential input high threshold voltage	RXVTH	-	-	+0.1	V	RXVCM=1.2V
Differential input low threshold voltage	RXVTL	-0.1	-	-	V	
Input voltage range (singled-end)	RXVIN	0.7	-	1.7	V	
Differential input common mode voltage	RXVCM	1	1.2	1.4	V	|VID|=0.2
Differential input impedance	ZID	80	100	125	ohm	
Differential input voltage	|VID|	0.2	-	0.6	V	
Differential input leakage current	ILCLVDS	-10	-	+10	uA	
LVDS Digital Operating Current	IVDD	-	15	20	mA	FDCLK=80MHz,VDD=3.3V, Input pattern: 55h->Aah->55h->Aah
LVDS Digital Stand-by Current	IST	-	-	250	uA	Clock & all Functions are stopped

7.2.2 LVDS mode AC electrical characteristics





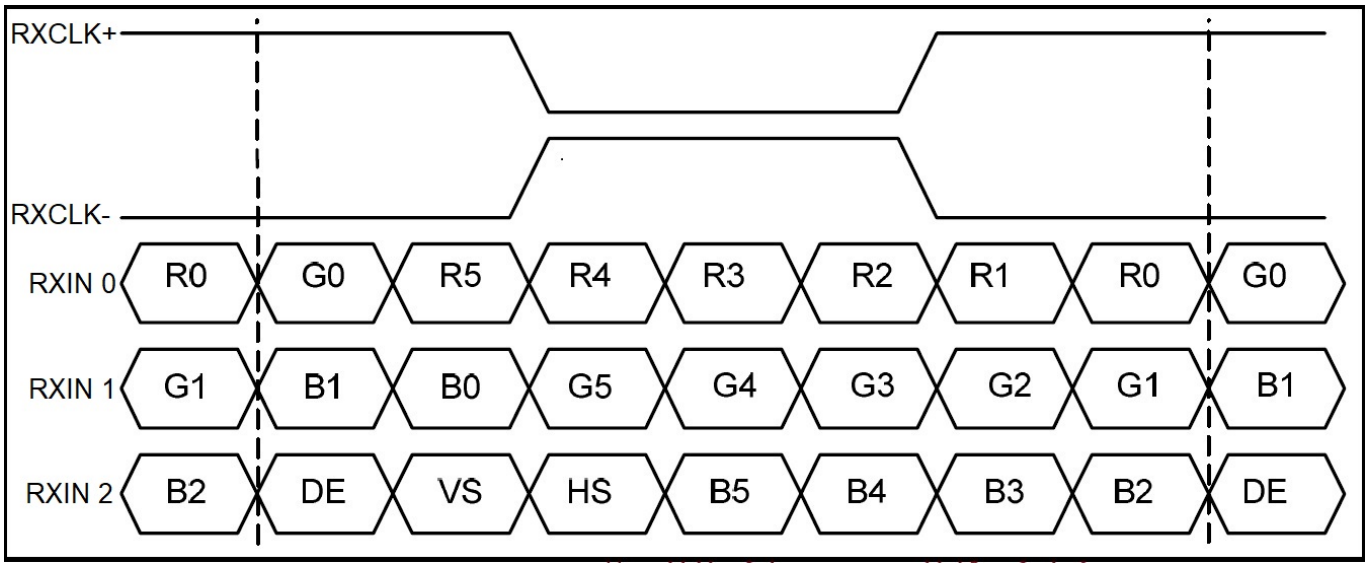


Parameter	Symbol	Spec.			Unit	Condition
		Min.	Typ.	Max.		
Clock frequency	RxFCLK	30	-	-	MHz	Refer to input timing table for each display resolution
Input data skew margin	TRSKM	500	-	-	ps	|VID| = 200mV RxVCM = 1.2V RxFCLK = 81MHz
Clock high time	TLVCH	-	4/(7* RxFCLK)	-	ns	
Clock low time	TLVCL	-	3/(7* RxFCLK)	-	ns	
PLL wake-up time	TenPLL	-	-	150	us	

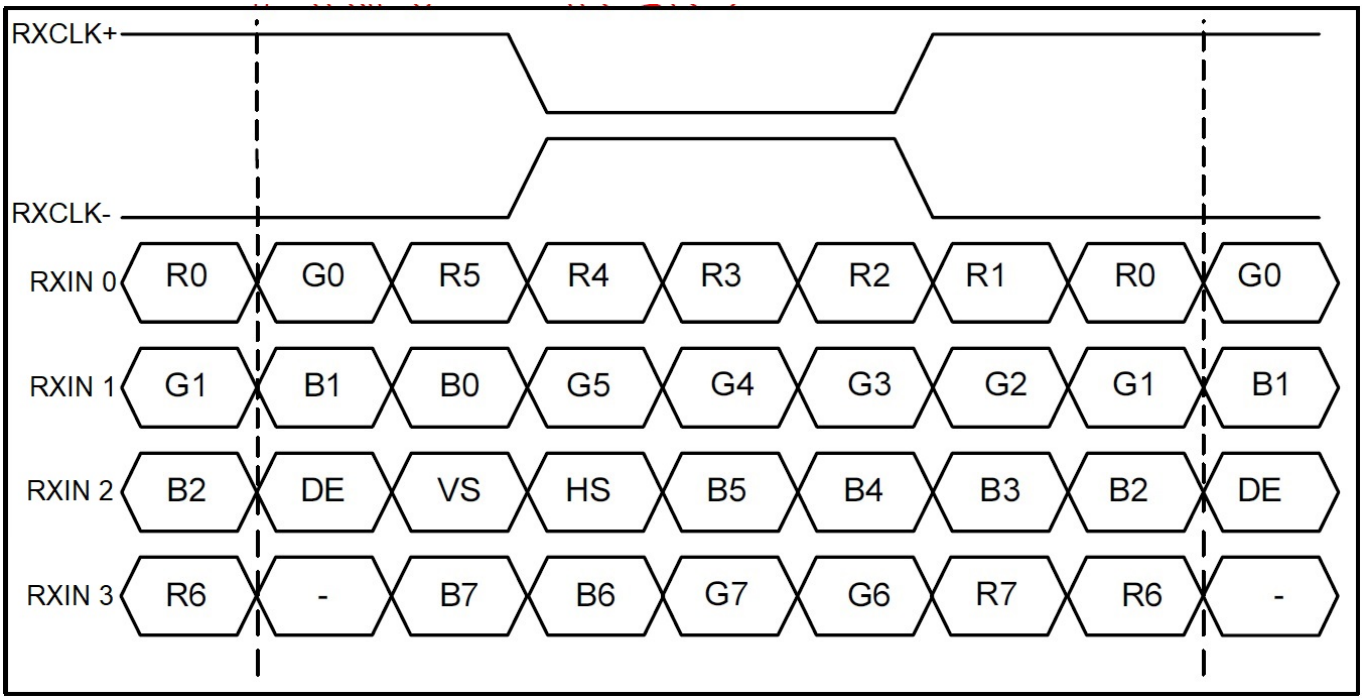
7.2.3 Interface Timings

Parameter	Symbol	Min.	Typ	Max.	Unit
DCLK frequency @Frame rate=60Hz	F_{DCLK}	66.3	72.4	78.9	MHz
HSYNC period time	T_H	1380	1440	1500	DCLK
Horizontal display area	T_{HD}		1280		DCLK
HSYNC period width	T_{HPW}	2	-	40	DCLK
HSYNC back porch (with pulse width)	T_{HBP}	88	88	88	DCLK
HSYNC front porch	T_{HFP}	12	72	132	DCLK
VSYNC period time	T_V	824	838	872	H
Vertical display area	T_{VD}		800		H
VSYNC period width	T_{VPW}	2	-	20	H
VSYNC back porch (with pulse width)	T_{VBP}	23	23	23	H
VSYNC front porch	T_{VFP}	1	15	49	H

7.2.4 LVDS Data Mapping



6-bit LVDS input (LVBIT = L)



8-bit LVDS input (LVBIT = H)

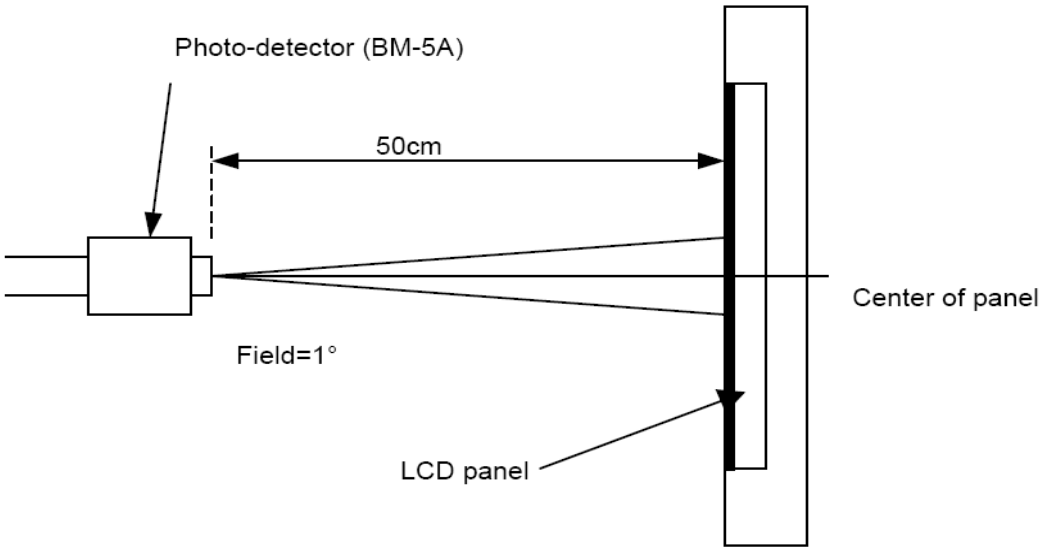
8. Backlight Characteristic

Item	Symbol	MIN	TYP	MAX	UNIT	NOTE
Lifetime		50000	-	-	Hr	
Color	White					
Luminous Intensity for LCM and TP	LED_PWM =100%	450	550	-	cd/m2	
Luminance uniformity		80	-	-	%	

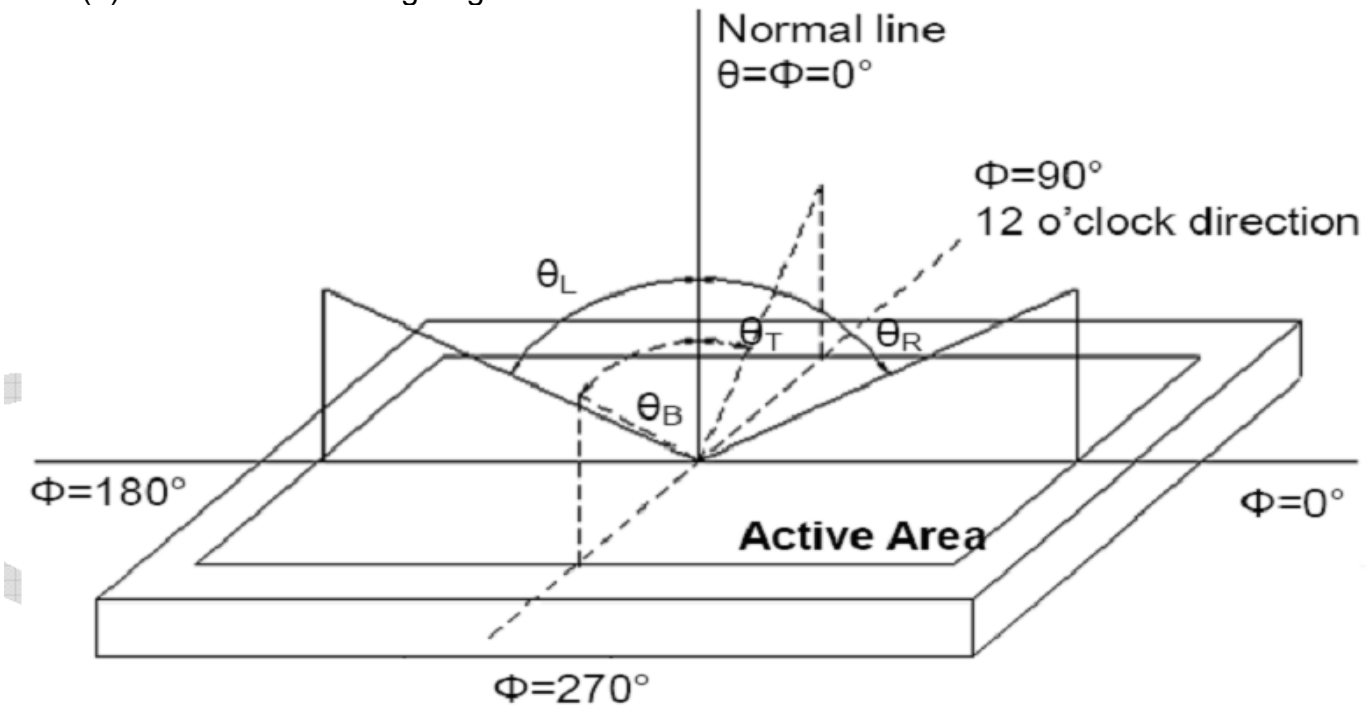
9. Optical Characteristics

Item	Conditions		Min.	Typ.	Max.	Unit	Note
Viewing Angle (CR>10)	Horizontal	θL	-	85	-	degree	(1),(2),(6)
		θR	-	85	-		
	Vertical	θT	-	85	-		
		θB	-	85	-		
Contrast Ratio	Center		800	1000	-	-	(1),(3),(6)
Response Time	TR+TF		-	25	35	ms	(1),(4),(6)
CF Color Chromaticity (CIE1931)	Red x		Typ. −0.05	0.59	Typ. +0.05	-	(1), (6)
	Red y			0.34		-	
	Green x			0.35		-	
	Green y			0.59		-	
	Blue x			0.14		-	
	Blue y			0.10		-	
	White x			0.30		-	
	White y			0.33		-	
Color Gamut	CIE 1931		-	53	-	%	

Note (1) Measurement Setup: The LCD module should be stabilized at given temp. 25°C for 15 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 15 minutes in a windless room.



Note (2) Definition of Viewing Angle



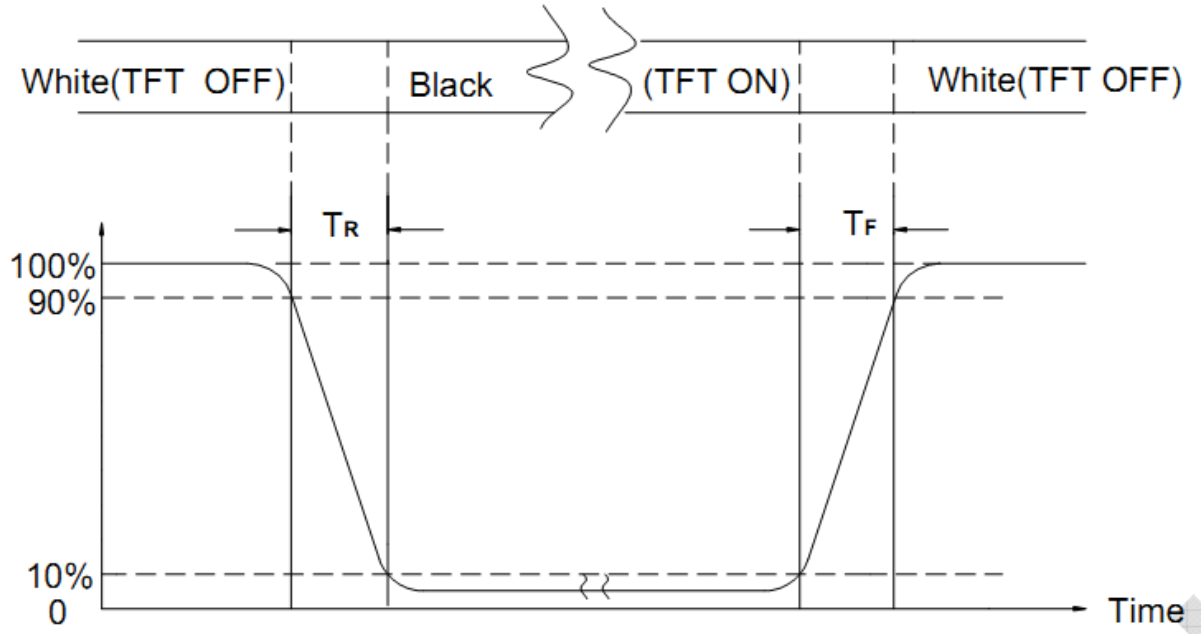
Note (3) Definition of Contrast Ratio (CR)

The contrast ratio can be calculated by the following expression

Contrast Ratio (CR) = L63 / L0

L63: Luminance of gray level 63, L0: Luminance of gray level 0

Note (4) Definition of response time



Note (5) Definition of Transmittance (Module is without signal input)

Transmittance = Center Luminance of LCD / Center Luminance of Back Light x 100%

Note (6) Definition of color chromaticity (CIE1931)

Color coordinates measured at the center point of LCD

10. Reliability Test Conditions and Methods

NO.	Test Items	Test Condition
①	High Temperature Storage	Keep in 80°C±2°C×240Hrs Surrounding temperature, then storage at normal condition 4hrs.
②	Low Temperature Storage	Keep in -30°C±2°C×240Hrs Surrounding temperature, then storage at normal condition 4hrs.
③	High Temperature Operating Test	70°C±2°C×240Hrs
④	Low Temperature Operating Test	-20°C±2°C×240Hrs
⑤	High Temperature / High Humidity Storage Test	Keep in 60°C±5°C×90%RH×240Hrs Surrounding temperature, then storage at normal condition 4hrs.
⑥	Temperature Cycling Storage Test	-30°C → +25°C → 80°C → +25°C (30mins) (5mins) (30mins) (5mins) 30 Cycle Surrounding temperature, then storage at normal condition 4hrs.
⑦	ESD Test	Air Discharge: Apply 6 KV with 5 times Discharge for each polarity +/- / Contact Discharge: Apply 4 KV with 5 times discharge for each polarity +/-
		1. Temperature ambiance : 15°C～35°C 2. Humidity relative : 30%～60% 3. Energy Storage Capacitance (Cs + Cd): 150pF±10% 4. Discharge Resistance (Rd): 330Ω±10% 5. Discharge, mode of operation: Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication : ±5%)
⑧	Vibration Test (Packaged)	1. Sine wave 10～55 Hz frequency (1 min/sweep) 2. The amplitude of vibration :1.5 mm 3. Each direction (X、Y、Z) duration for 2Hrs
⑨	Drop Test (Packaged)	Packing Weight (Kg) / Drop Height (cm) 0 ~ 45 / 122 45.4 ~ 90.8 / 76 90.8 ~ 454 / 61 Over 454 / 46 Drop Direction: ※1 corner / 3 edges / 6 sides each 1time

PS：①~ ⑦ test exclude Polaroid;

11. Inspection Standard

11.1. QUALITY :

THE QUALITY OF GOODS SUPPLIED TO PURCHASER SHALL COME UP TO THE FOLLOWING STANDARD.

11.1.1. THE METHOD OF PRESERVING GOODS

AFTER DELIVERY OF GOODS FROM AMSON TO PURCHASER. PURCHASER SHALL CONTROL THE LCM AT -10 TO 40 ,AND IT MIGHT BE DESIRABLE TO KEEP AT THE NORMAL ROOM TEMPERATURE AND HUMIDITY UNTIL INCOMING INSPECTION OR THROWING INTO PROCESS LINE.

11.1.2. INCOMING INSPECTION

(A) THE METHOD OF INSPECTION

IF PURCHASER MAKE AN INCOMING INSPECTION , A SAMPLING PLAN SHALL BE APPLIED ON THE CONDITION THAT QUALITY OF ONE DELIVERY SHALL BE REGARDED AS ONE LOT.

(B) THE STANDARD OF QUALITY

ISO-2859-1 (SAME AS MIL-STD-105E) ,LEVEL:II

CLASS	AQL(%)
CRITICAL	0.4 %
MAJOR	0.65 %
MINOR	1.5 %

EVERY ITEM SHALL BE INSPECTED ACCORDING TO THE CLASS.

(C) MEASURE

IF AS THE RESULT OF ABOVE RECEIVING INSPECTION , A LOT OUT IS DISCOVERED. PURCHASER SHALL BE INFORM SELLER OF IT WITHIN SEVEN DAYS. BUT FIRST SHIPMENT WITHIN FOURTEEN DAYS.

11.1.3. WARRANTY POLICY

AMSON WILL PROVIDE ONE-YEAR WARRANTY FOR THE PRODUCTS ONLY IF UNDER SPECIFICATION OPERATING CONDITIONS. AMSON WILL REPLACE NEW PRODUCTS FOR THESE DEFECT PRODUCTS WHICH UNDER WARRANTY PERIOD AND BELONG TO THE RESPONSIBILITY OF AMSON.

11.2. CHECKING CONDITION

11.2.1.CHECKING DIRECTION SHALL BE IN THE 45 DEGREE AREA TO FACE THE SAMPLE.

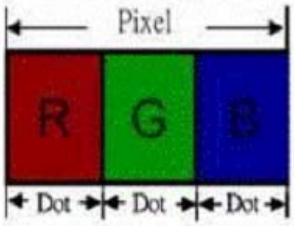
11.2.2.CHECKER SHALL SEE OVER 300±25 mm. WITH BARE EYES FAR FROM SAMPLE

Ambient Illumination:

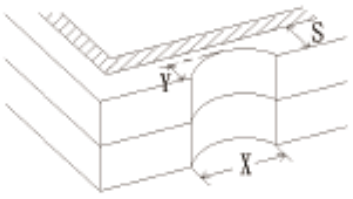
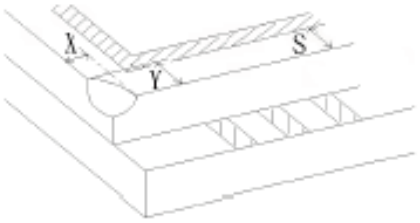
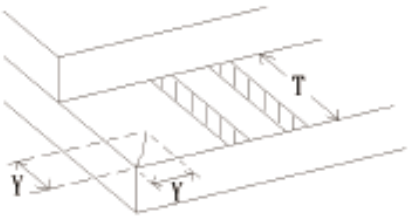
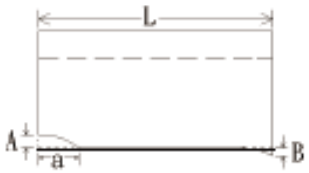
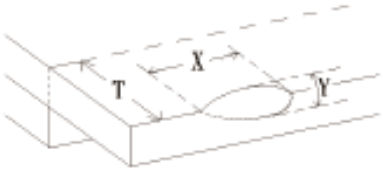
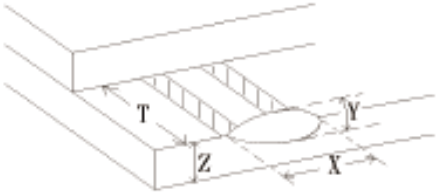
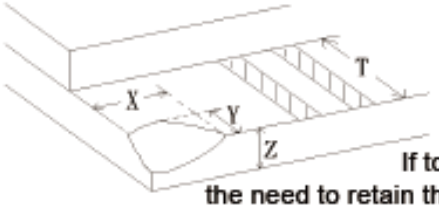
Appearance detection in 800~1000 Lux external environment

11.3. INSPECTION PLAN :

CLASS	ITEM	JUDGEMENT	CLASS
PACKING & INDICATE	1. OUTSIDE AND INSIDE PACKAGE	"MODEL NO." , "LOT NO." AND "QUANTITY" SHOULD INDICATE ON THE PACKAGE.	Minor
	2. MODEL MIXED AND QUANTITY	OTHER MODEL MIXED.....REJECTED QUANTITY SHORT OR OVER.....REJECTED	Critical
	3. PRODUCT INDICATION	"MODEL NO." SHOULD INDICATE ON THE PRODUCT	Major
ASSEMBLY	4. DIMENSION, LCD GLASS SCRATCH AND SCRIBE DEFECT.	ACCORDING TO SPECIFICATION OR DRAWING.	Major
APPEARANCE	5. VIEWING AREA	POLARIZER EDGE OR LCD'S SEALING LINE IS VISABLE IN THE VIEWING AREA .....REJECTED	Minor
	6. BLEMISH、BLACK SPOT、 WHITE SPOT IN THE LCD AND LCD GLASS CRACKS	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
	7. BLEMISH、BLACK SPOT WHITE SPOT AND SCRATCH ON THE POLARIZER	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
	8. BUBBLE IN POLARIZER	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
	9. LCD'S RAINBOW COLOR	STRONG DEVIATION COLOR (OR NEWTON RING) OF LCD.....REJECTED. OR ACCORDING TO LIMITED SAMPLE (IF NEEDED, AND INSIDE VIEWING AREA)	Minor
ELECTRICAL	10. ELECTRICAL AND OPTICAL CHARACTERISTICS (CONTRAST、VOP、 CHROMATICITY ... ETC)	ACCORDING TO SPECIFICATION OR DRAWING . (INSIDE VIEWING AREA)	Critical
	11.MISSING LINE	MISSING DOT、LINE、CHARACTER ....REJECTED	Critical
	12.SHORT CIRCUIT、 WRONG PATTERN DISPLAY	NO DISPLAY、WRONG PATTERN DISPLAY、CURRENT CONSUMPTION OUT OF SPECIFICATION..... REJECTED	Critical
	13. DOT DEFECT (FOR COLOR AND TFT)	ACCORDING TO STANDARD OF VISUAL INSPECTION	Minor

<table>
<tr><th>NO.</th><th>CLASS</th><th>ITEM</th><th>JUDGEMENT</th></tr>
<tr><td>11.4.1</td><td>MINOR</td><td>BLACK AND WHITE SPOT
FOREIGN MATERIEL
DUST IN THE CELL
BLEMISH
SCRATCH</td><td>(A) ROUND TYPE: unit : mm.
<table>
<tr><th>DIAMETER (mm.)</th><th>ACCEPTABLE Q'TY</th></tr>
<tr><td>Φ ≦ 0.15</td><td>Distance≥1mm</td></tr>
<tr><td>0.15 < Φ ≦ 0.4</td><td>3 (Distance>15mm)</td></tr>
<tr><td>0.4 < Φ</td><td>0</td></tr>
</table>
NOTE: Φ=(LENGTH+WIDTH)/2
(B) LINEAR TYPE: unit : mm.
<table>
<tr><th>LENGTH</th><th>WIDTH</th><th>ACCEPTABLE Q'TY</th></tr>
<tr><td>------</td><td>W ≦0.03</td><td>Distance≥1mm</td></tr>
<tr><td>L ≦ 4.0</td><td>0.03 < W ≦0.05</td><td>3 (Distance>15mm)</td></tr>
<tr><td>------</td><td>0.05 < W</td><td>FOLLOW ROUND TYPE</td></tr>
</table></td></tr>
<tr><td>11.4.2</td><td>MINOR</td><td>BUBBLE IN POLARIZER
DENT ON POLARIZER</td><td>unit : mm.
<table>
<tr><th>DIAMETER</th><th>ACCEPTABLE Q'TY</th></tr>
<tr><td>Φ ≦ 0.2</td><td>Distance≥1mm</td></tr>
<tr><td>0.2 < Φ ≦ 0.5</td><td>3 (Distance>15mm)</td></tr>
<tr><td>0.5 < Φ</td><td>0</td></tr>
</table></td></tr>
<tr><td>11.4.3</td><td>MINOR</td><td>Dot Defect</td><td>
<table>
<tr><th>Items</th><th>ACC. Q'TY</th></tr>
<tr><td>Bright dot</td><td>N≦2 (Distance≥15mm)</td></tr>
<tr><td>Dark dot</td><td>N≦3 (Distance≥15mm)</td></tr>
</table>
Pixel Define :
 

Note 1: The definition of dot: The size of a defective dot over 1 of whole dot is regarded as one defective dot.
Definittion:<1/2dot and visible by 5% ND filter
Note 2: Bright dot: Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern.
Note 3: Dark dot: Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green ,blue pattern.</td></tr>
<tr><td>11.4.4</td><td>MINOR</td><td>Mura</td><td>Not visible thriugh 5% ND filter in 50% gray or judge by limit sample if necessary</td></tr>
</table>

NO.	CLASS	ITEM	JUDGEMENT
11.4.5	MINOR	LCD GLASS CHIPPING	 X ≥ 3mm Y > S Reject
11.4.6	MINOR	LCD GLASS CHIPPING	 X or Y > S Reject
11.4.7	MAJOR	LCD GLASS GLASS CRACK	 Continuous burst NG Reject
11.4.8	MAJOR	LCD GLASS SCRIBE DEFECT	 ACCORDING TO DIMENSION
11.4.9	MINOR	LCD GLASS CHIPPING (ON THE TERMINAL AREA)	 Y<1/2Z Y ≥ 0.5mm Reject X ≥ 3mm
11.4.10	MINOR	LCD GLASS CHIPPING (ON THE TERMINAL SURFACE)	 Y<1/2Z Y ≥ 0.5mm Reject X ≥ 3mm
11.4.11	MINOR	LCD GLASS CHIPPING	 X ≥ 3mm Y ≥ T Reject If touch the electrode lines, the need to retain the two-thirds electrode lines

12. Handling Precautions

12.1 Mounting method

The LCD panel of AMSON TFT module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns

Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (Cl) , Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (Cl), Sulfur (S) from customer, Responsibility is on customer.

12.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to power or ground, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

12.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

12.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the maximum operating temperature, 50%Rh or less is required.

12.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
  [It is recommended to store them as they have been contained in the inner container at the time of delivery from us.

12.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

13. Precaution for Use

13.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

13.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification.
- When a new problem is arisen this is not specified in this specification.
- When an inspection specifications change or operating condition change in customer is reported to AMSON TFT and some problem is arisen in this specification due to the change.
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

14. Packing Method

TBD