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Specification for Approval

Customer:	
Model Name:	

Sı	Customer approval		
R&D Designed	R&D Approved	QC Approved	
Peter	Peng Jun		

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Revision Record

REV NO.	REV DATE	CONTENTS	Note
А	2015-11-24	NEW ISSUE	
В	2020-06-08	CHANGE ESTAPE	

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1. Scope

This specification defines general provisions as well as inspection standards for TFT module supplied by AMSON electronics.

If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution

2. General Information

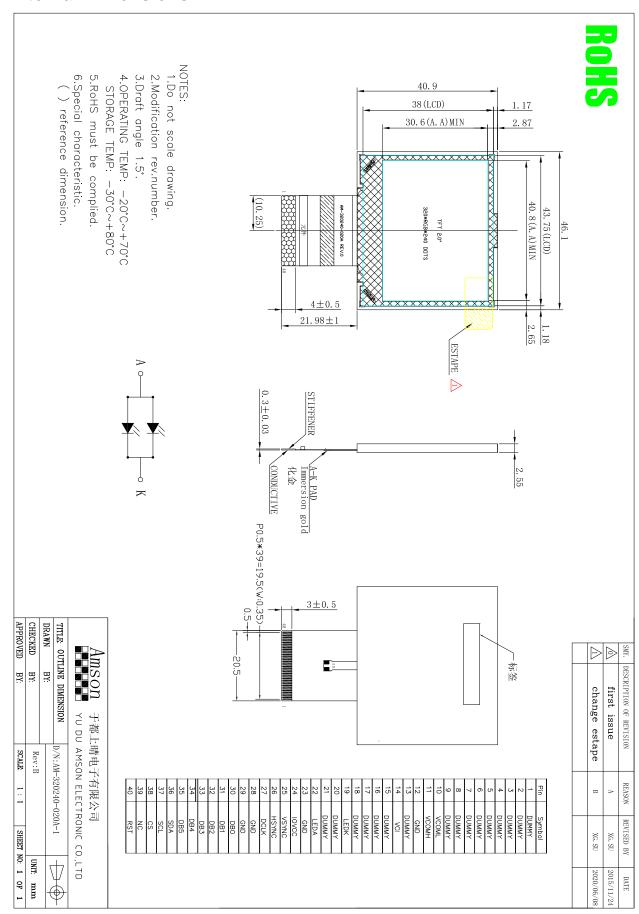
ITEM	STANDARD VALUES	UNITS
LCD type	2.0"TFT	
Dot arrangement	320(RGB)×240	dots
Color filter array	RGB vertical stripe	
Display mode	TN / Transmissive / Normally White	
Viewing Direction	12 o'clock(Gray scale inversion)	
Driver IC	ILI9342	
Module size	46.10 (W) * 40.96 (H) * 2.55(T)	mm
Active area	40.80 (W) * 30.60 (H)	mm
Dot pitch	0.0425 (W) * 0.1275 (H)	mm
Interface	3-wire SPI / RGB	
Operating temperature	-20 ~ +70	°C
Storage temperature	rature -30 ~ +80	
Back Light	2 White LED In Parallel	
Weight	TBD	g



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3. External Dimensions





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4. Interface Description

	Interface Description								
Pin	Symbol	I/O	Function						
1	DUMMY		Dummy						
2	DUMMY		Dummy						
3	DUMMY		Dummy						
4	DUMMY		Dummy						
5	DUMMY		Dummy						
6	DUMMY		Dummy						
7	DUMMY		Dummy						
8	DUMMY		Dummy						
9	DUMMY		Dummy						
10	VCOML	С	Capacitor for VCOM low						
11	VCOMH	С	Capacitor for VCOM high						
12	GND		Ground						
13	DUMMY		Dummy						
14	VCI	I	Power supply for analog circuit blocks						
15	DUMMY		Dummy						
16	DUMMY		Dummy						
17	DUMMY		Dummy						
18	DUMMY		Dummy						
19	LEDK	G	LED Power: cathode						
20	DUMMY		Dummy						
21	DUMMY		Dummy						
22	LEDA	Р	LED Power: anode						
23	GND		Ground						
24	IOVCC	I	Power supply for interface logic circuit						
25	VSYNC	I	Vertical sync input. Negative polarity						
26	HSYNC	I	Horizontal sync input. Negative polarity						
27	DCLK	I	Dot clock signal for RGB interface operation						
28	GND		Ground						
29	GND		Ground						
30	DB0	I	Data Input :						
31	DB1	I	Data Input :						
32	DB2	I	Data Input:						



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33	DB3	1	Data Input :	
34	DB4	I	Data Input:	
35	DB5	I	Data Input:	
36	SDA	I	Serial interface data line	
37	SCL	С	Serial interface clock line	
38	CS	С	Chip select input pin	
39	NC		No connection	
40	REST	I	System reset pin	

5. Absolute Maximum Ratings

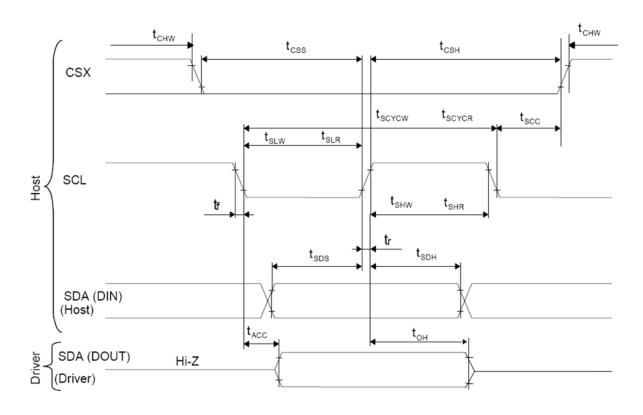
Item	Symbol	Min.	Max.	Unit
Logic Supply Voltage	IOVCC	-0.3	4.6	V
Analog Supply Voltage	VCI	-0.3	4.6	V
Input Voltage	Vin	-0.3	IOVCC+0.3	V
Operating Temperature	Тор	-20	70	°C
Storage Temperature	Тѕт	-30	80	°C
Storage Humidity	HD	20	90	%RH

6. DC Characteristics

	1					
Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Logic Supply Voltage	IOVCC	1.65	2.8	3.3	٧	-
Analog Supply Voltage	VCI	2.5	2.8	3.3		
Input High Voltage	V _{IH}	0.7IOVCC		IOVCC	V	Digital input pins
Input Low Voltage	V _{IL}	GND		0.3IOVCC	V	Digital input pins
Output High Voltage	V _{OH}	0.8IOVCC		IOVCC	V	Digital output pins
Output Low Voltage	V _{OL}	GND		0.2IOVCC	V	Digital output pins
I/O Leak Current	ILI	-0.1		0.1	uA	

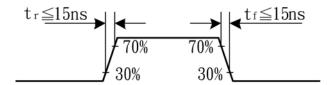
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7. Timing Characteristics7.1 Serial Interface Timing Characteristics (3-wire SPI)



Signal	Symbol Symbol	Parameter	min	max	Unit	Description
	tscycw	Serial Clock Cycle (Write)	100	-	ns	
	tshw	SCL "H" Pulse Width (Write)	35	_	ns	
SCL	tslw	SCL "L" Pulse Width (Write)	35	_	ns	
SCL	tscycr	Serial Clock Cycle (Read)	150	-	ns	
	tshr	SCL "H" Pulse Width (Read)	60	_	ns	
	tslr	SCL "L" Pulse Width (Read)	60	-	ns	
SDA	tsds	Data setup time (Write)	30	_	ns	
(Input)	tsdh	Data hold time (Write)	30	-	ns	
SDA	tacc	Access time (Read)	10	_	ns	
(Output)	toh	Output disable time (Read)	15	50	ns	
	tscc	SCL-CSX	20	_	ns	
CCV	tchw	CSX "H" Pulse Width	40	-	ns	
CSX	tcss	CCV CCL Time(maile)	30	-	ns	
	tcsh	CSX-SCL Time(write)	30	-	ns	

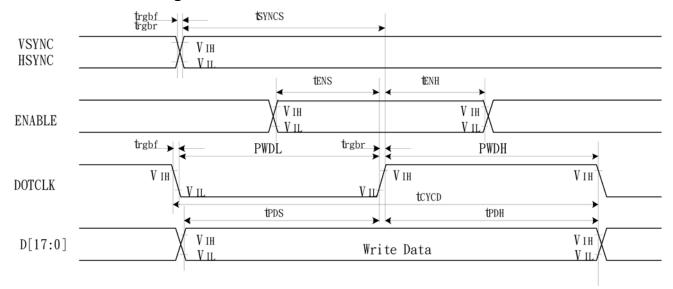
Note: Ta = 25 °C, IOVCC=1.65V to 2.8V, VCI=2.6V to 3.3V, AGND=GND=0V



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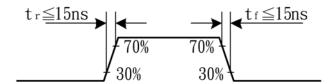
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7.2 RGB Interface Timing Characteristics



Signal	Symbol	Parameter	min	max	Unit	Description
VSYNC /	tsyncs	VSYNC/HSYNC setup time	15	-	ns	
HSYNC	tsynch	VSYNC/HSYNC hold time	15	-	ns	
DE	ten s	DE setup time	15	-	ns	
DE	tenh	DE hold time	15	-	ns	
D[17:0]	tpo s	Data setup time	15	-	ns	18/16-bit bus RGB
ענוו:0ן	Фри	Data hold time	15	-	ns	interface mode
	PWDH	DOTCLK high-level period	33	-	ns	Titter race mode
	PWDL	DOTCLK low-level period	33	-	ns	
DOTCLK	tcycd	DOTCLK cycle time(18 bit)	100	-	ns	
	trgbr trgbi	DOTCLK, HSYNC, VSYNC rise/fall time	-	15	ns	
VSYNC /	tsyncs	VSYNC/HSYNC setup time	15	-	ns	
HSYNC	tsynch	VSYNC/HSYNC hold time	15	-	ns	
DE.	ten s	DE setup time	15	-	ns	
DE	tenh	DE hold time	15	-	ns	
D[17.0]	tpo s	Data setup time	15	-	ns	6-bit bus RGB
D[17:0]	†pdH	Data hold time	15	_	ns	interface mode
	PWDH	DOTCLK high-level pulse period	25	-	ns	
DOTCL V	PWDL	DOTCLK low-level pulse period	25	-	ns	
DOTCLK	t cycd	DOTCLK cycle time	50	-	ns	
	trgbr trgbi	DOTCLK, HSYNC, VSYNC rise/fall time	-	15	ns	

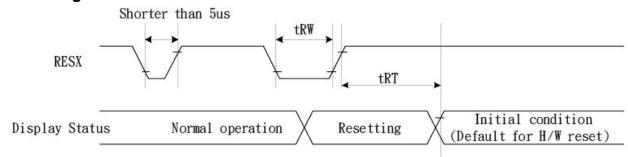
Note: Ta = -30 to 70 °C, IOVCC=1.65V to 2.8V, VCI=2.6V to 3.3V, AGND=GND=0V



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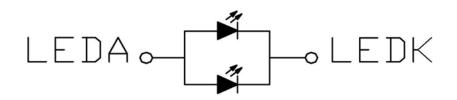
7.3 Reset Timing Characteristics



Signal	Symbo1	Parameter	Parameter Min		Unit
RESX	tRW	Reset pulse duration	eset pulse duration 10		uS
tRT	+DT	Poset sensel		5 (note 1, 5)	mS
	tri	Reset cancel		120 (note 1, 6, 7)	mS

8. Backlight Characteristic

LED Circuit:



Item	Symbol	MIN	TYP	MAX	UNIT	Test Condition
Supply Voltage	Vf	3.0	3.3	3.5	V	If=40mA
Supply Current	lf	-	40	50	mA	
Luminous Intensity for LCM		180	230		cd/m ²	If=40mA
Uniformity for LCM		80			%	If=40mA
Backlight Color	White					

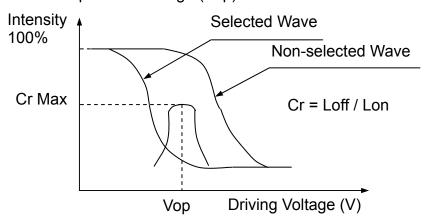
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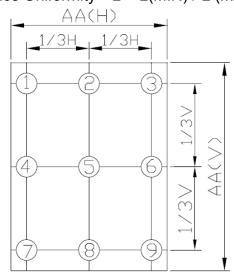
9. Optical Characteristics

ITEN	1	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
Brightn	ess	BL	θ =φ= 0°	180	230		cd/m²	Note2
Contrast	Ratio	CR	θ =φ= 0°		600			Note3
Doononee	Time	Tr	Ω = α = 0°		6	12	m.	Note4
Response	rime	Tf	θ =φ= 0°		15	30	ms	
	Upper	0	θ ————————————————————————————————————	10	(45)			
Viewing	Down	Ð		30	(60)			Note F
Angle	Right			45	(60)			Note 5
	Left	Ψ		45	(60)			
Color Filter	White	Х	θ =φ= 0°	(0.26)	(0.31)	(0.36)		Note 6
Chromaticity		у		(0.29)	(0.34)	(0.39)		Note 6

Note1: Definition of Operation Voltage (Vop)



Note2: Definition of Luminance Uniformity: L = L(MIN) / L (MAX) × 100%

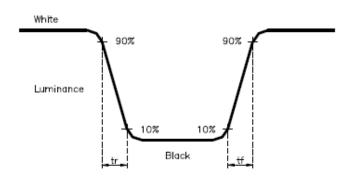


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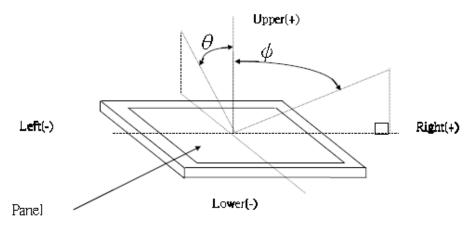
Note 3. Definition of Contrast Ratio:

CR = White Luminance (ON) / Black Luminance (OFF)

Note 4. Definition of response time: The response time is defined as the time interval between the 10% and 90% amplitudes.



Note 5. Definition of view angle($\theta \cdot \psi$):



Note 6. Light source: Clight.



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10. Reliability Test Conditions and Methods

NO.	ITEM	CONDITION	CRITERION
1	High Temperature Operating	60°C 240 hrs	No Defect OfOperational Function In
2	Low Temperature Operating		Room Temperature Are Allowable.
3	High Temperature Non-Operating	70°C 240 hrs	 Leakage current should
4	Low Temperature Non-Operating	-20°C 240 hrs	be below double of initial value.
5	High Temperature/ Humidity Non-Operating	60°C ,90%RH 240 hrs	
6	Temperature Shock Non-Operating	-20°C 70°C (30min) (5min) (30min) 100 CYCLES	
7	Electrostatic Discharge Test Non-Operating	HBM:±2kV	

- Note 1: Test after 24 hours in room temperature.
- Note 2: The sampling above is individually for each reliability testing condition.
- Note 3: The color fading of polarizing filter should not care.
- Note 4: All of the reliability testing chamber above, is using D.I. water. (Min value: 1.0 M Ω -cm)
- Note 5: In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.

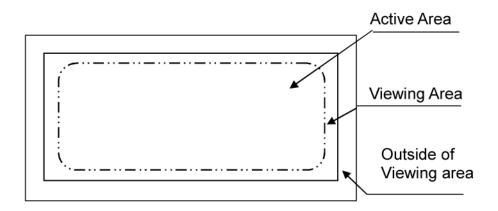
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11. Inspection Standard

11.1 Definition Of Inspection Area

V.A: Viewing Area A.A: Active Area



11.2 Items And Criteria

11.2.1 Visual inspection criterion in cosmetic

	11.2.1 Visual inspection chierion in cosmetic						
	Glass defect						
No	Item	Criteria	Remark				
1	Dimension (Minor)	By engineering diagram	Y Z (
2	Crack (Major)	Extensive crack G RejectH					

	LCD appearance defect							
No	Item		Criteria	Remark				
	Round type	Defect Spe	ec.	Permissible Q'ty	1:I =(L+W)/2, L: Length,			
		I * 0.2m	m	Disregard	W: Width			
1	(Minor)	0.2mm <ij 0<="" td=""><td>).3mm</td><td>2</td><td>2: Disregard if out of A.A.</td></ij>).3mm	2	2: Disregard if out of A.A.			
		[>0.3mr	m	0	₩ L			



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	LCD appearance defect						
	Line type	Defect Spec.	Permissible Q'ty	1: L: Length, W: Width 2: Disregard if out of A.A			
2	(Minor)	W* 0.5mm, L* 2.0mm	2	L Disregard in out of A.A			
		W* 0.5mm, L>2.0mm	0	- V			
	Polarizer bubble	Defect Spec.	Permissible Q'ty	1:I =(L+W)/2, L: Length,			
3	(Minor)	Out of A.A	Disregard	W: Width.			
		Within A.A	0				

	FPC					
No	Item	Criter	ia	Remark		
1	Copper peeling (Minor)	Copper peeling G RejectH				
2	No release film or Peeling (Minor)	No release film or pe G RejectH				
	Finger Spots, Impurities defect	Defect Spec. IJ 0.35mm	Permissible Q'ty	No bridge Disregard if the		
	(Minor)	I >0.35mm	0	dirty removed		

	Silicon				
No	o Item Criteria Remark				
	l .	ITO exposed G RejectH			

	Bezel					
No	Defect	Criteria	Remark			
1	Oxidized spot (Minor)	Oxidized spot, rust G Reje	ectH			
2	Outline deformation	By engineering diagram				
3	(Minor) Greasiness (Minor)	Greasiness G Rej	ectH			
4	Spots, round Type (Minor)	HJ By engineering diagram G Disreg	H=Total height (thickness)			



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		Beze	el	
5	Plating	Bubble, peeling	G Reject H	
5	(Mir	nor)		

11.	1.2.2 Visual inspection criterion in electrical display						
No	Defect	Criteria			Remark		
1	No display (Major)	Not allowed					
2	Missing line (Major)	Not allowed					
3	Darker or lighter line (Major)	Not allowed					
No	Defect		Criteria			Remark	
4	Weak line (Minor)	By limit sample					
	Bright / Dark point (Minor)		A Area	B Area	Total	1:1sub-pixel: 1R or 1G or 1B 2:Point	
		Bright point	0	1	1	defect areaK 1/2 sub	
5		Dark dot point	1	1	2	pixel.	
		Bright +Dark point	1	2	3	2.Point distanceK 5mm 3.Refere to Note 1	
	Round type	Spec.		Permissible Qty		1.I =(L+W)/2, L:	
	(Minor)	IJ 0.2 r	nm	Disre	gard	Length, W: Width	
6		0.2mm<[J	0.3mm	2	2	2. Disregard if out of	
		I >0.3m	ım	0		A.A. W	
7	Line type (Minor)	Spec.		Permiss	ible Qty	L: Length, W: Width Disregard if out of	
		WJ 0.5mm, LJ	2.0mm	2	2	A.A.	



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		WJ 0.5mm, L>2.0mm	0	
8	Mura (Minor)	By 5% ND filter invisible		

N	ote	1
I٧	ULE	

1/4	1/2	1/4	
В	В	В	1/4
В	A	В	1/2
В	В	В	1/4

Please Follow the section separate to judgment the Bright or Dark Dots

11.2.3 Others

- 1. Issues that are not defined in this document shall be discussed and agreed with both parties. (Customer and supplier)
- 2. Unless otherwise agreed upon in writing, the criteria shall be applied to both parties. (Customer and supplier)



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12. Handling Precautions

12.1 Mounting method

The LCD panel of AMSON TFT module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (CI), Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

12.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to power or ground, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

12.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

12.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.
 - Usage under the maximum operating temperature, 50%Rh or less is required.



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12.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else. [It is recommended to store them as they have been contained in the inner container at the time of delivery from us

12.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

13. Precaution for Use

13.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

13.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification
- When a new problem is arisen which is not specified in this specifications
- When an inspection specifications change or operating condition change in customer is reported to AMSON TFT, and some problem is arisen in this specification due to the change
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

14. Packing Method

TBD.