Specification for Approval

Customer:_____

Model Name:_____

Supplier Approval			Customer approval
R&D Designed	R&D Approved	QC Approved	
Peter	*Peng Jun*		

Revision Record

REV NO.	REV DATE	CONTENTS	Note
A	2012-07-10	NEW ISSUE	
B	2013-04-08	Add Touch Panel Characteristics	
C	2015-10-15	Add Label of Drawing	

Table of Contents

1. Scope

This specification defines general provisions as well as inspection standards for TFT module supplied by AMSON electronics.

If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution.

2. General Information

ITEM	STANDARD VALUES	UNITS
LCD type	3.5"TFT	--
Dot arrangement	320(RGB)×240	dots
Color filter array	RGB vertical stripe	--
Display mode	TN / Transmissive / Normally White	--
Viewing Direction	6 o'clock(Gray scale inversion)	--
Driver IC	HX8238-D	--
Module size	76.9(W)×63.9(H)×4.36(T)	mm
Active area	70.08(W)×52.56(H)	mm
Dot pitch	0.219(W)×0.219(H)	mm
Interface	24 bits RGB with Serial Interface / CCIR656	--
Operating temperature	-20 ~ +70	°C
Storage temperature	-30 ~ +80	°C
Back Light	6 White LED	--
Weight	TBD	g

3. External Dimensions

1. 3.5" Transmissive, Normally-White TFT-LCD Module
2. Resolution: 320(RGB) × 240
3. Drive IC: HX8238
4. Interface: Parallel RGB-24bit+CCIR656
5. Backlight: 6LED in serial @15mA
6. 4-wire analog resistive touch panel

BL Circuit Diagram:

LEDA — LEDK

DETAIL 'A'(S=4:1)
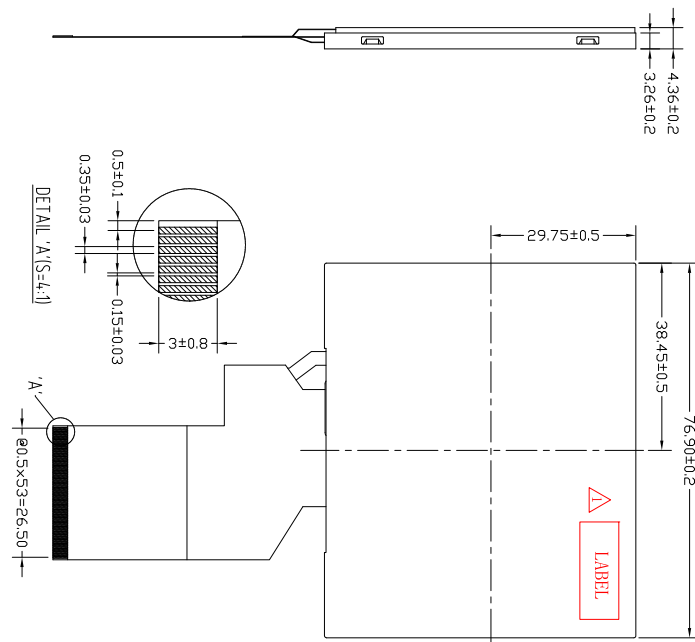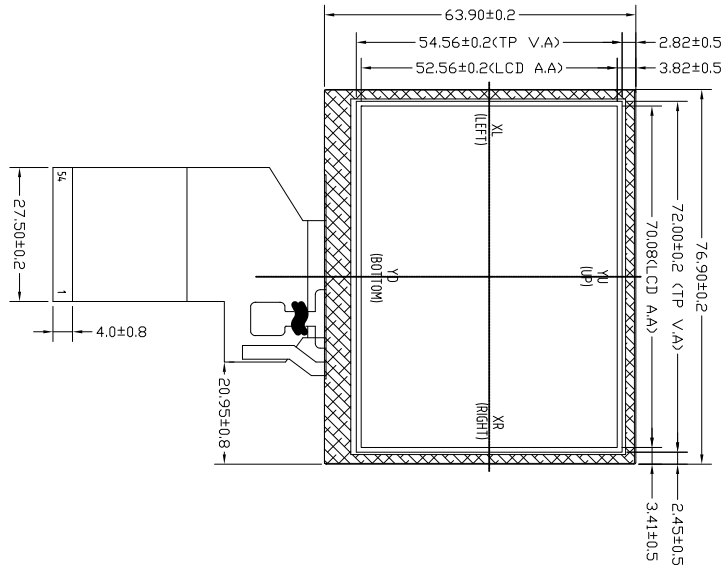
PIN NO.	PIN NAME	PIN NO.	PIN NAME	PIN NO.	PIN NAME
1	LEDK	28	DR0		
2	LEDK	29	DR1		
3	LEDA	30	DR2		
4	LEDA	31	DR3		
5	YU	32	DR4		
6	XR	33	DR5		
7	NC	34	DR6		
8	/RESET	35	DR7		
9	SPENA	36	HSYNC		
10	SPCLK	37	VSYNC		
11	SPDAT	38	DOTCLK		
12	DB0	39	NC		
13	DB1	40	NC		
14	DB2	41	VCC		
15	DB3	42	VCC		
16	DB4	43	YD		
17	DB5	44	XL		
18	DB6	45	NC		
19	DB7	46	NC		
20	DG0	47	NC		
21	DG1	48	SEL2		
22	DG2	49	SEL1		
23	DG3	50	SEL0		
24	DG4	51	NC		
25	DG5	52	DEN		
26	DG6	53	GND		
27	DG7	54	GND		

SYM.	DESCRIPTION OF REVISION	REASON	REVISED BY	DATE
△	first issue	XG. SU	XG. SU	2012/07/10
△	Add Label of Drawing	XG. SU	2015/10/15

APPROVED	BY:	
CHECKED	BY:	
DRAWN	BY:	
TITLE: OUTLINE DIMENSION		
Rev:B		
SCALE: 1 : 1	SHEET NO: 1 OF 1	UNIT: mm
D/N:AM320240-035AT		

于都上晴电子有限公司
YU DU AMSON ELECTRONICS Co., Ltd.

LABEL

4. Interface Description

PIN NO.	PIN NAME	DESCRIPTION
1	LEDK	LED backlight cathode
2	LEDK	LED backlight cathode
3	LEDA	LED backlight anode
4	LEDA	LED backlight anode
5	YU	TOUCH PIN(YU)
6	XR	TOUCH PIN(XR)
7	NC	No Connection
8	/RESET	System Reset
9	SPENA	SPI ENABLE
10	SPCLK	SERIAL CLOCK SIGNAL
11	SPDAT	SERIAL DATA SIGNAL
12	DB0	RGB Data Bus
13	DB1	
14	DB2	
15	DB3	
16	DB4	
17	DB5	
18	DB6	
19	DB7	
20	DG0	
21	DG1	
22	DG2	
23	DG3	
24	DG4	
25	DG5	
26	DG6	
27	DG7	
28	DR0	
29	DR1	
30	DR2	
31	DR3	
32	DR4	
33	DR5	
34	DR6	
35	DR7	
36	HSYNC	Line synchronizing signal for RGB interface operation
37	VSYNC	Frame synchronizing signal for RGB interface operation
38	DOTCLK	Dot Clock
39	NC	No Connection

40	NC	No Connection
41	VCC	Power supply
42	VCC	Power supply
43	YD	TOUCH PIN(YD)
44	XL	TOUCH PIN(XL)
45	NC	No Connection
46	NC	No Connection
47	NC	No Connection
48	SEL2	Input Interface Mode
49	SEL1	Input Interface Mode
50	SEL0	Input Interface Mode
51	NC	No Connection.
52	DEN	Display enable pin from controller
53	GND	Power ground
54	GND	Power ground

Note:

SEL2	SEL1	SEL0	Format	Operating Frequency
0	0	0	Parallel-RGB data format (only support stripe type color filter)	6.5MHz
0	0	1	Serial-RGB data format	19.5MHz
0	1	0	CCIR 656 data format (640RGB)	24.54MHz
0	1	1	CCIR 656 data format (720RGB)	27MHz
1	0	0	YUV mode A data format (Cr-Y-Cb-Y)	24.54MHz
1	0	1	YUV mode A data format (Cr-Y-Cb-Y)	27MHz
1	1	0	YUV mode B data format (Cb-Y-Cr-Y)	27MHz
1	1	1	YUV mode B data format (Cb-Y-Cr-Y)	24.54MHz

Input format	DOTCLK Freq (MHz)	Display Data	Active Area (DOTCLK)
YUV mode	24.54	640	1280
	27	720	1440

5. Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit
Supply Voltage	VCC	-0.3	4.0	V
Input Voltage	Vin	GND-0.3	4.0	V
Operating Temperature	T_{OP}	-20	70	°C
Storage Temperature	T_{ST}	-30	80	°C
Storage Humidity	HD	--	90	%RH

6. DC Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Supply Voltage	VCC	2.5	--	3.6	V	--
Input High Voltage	V_{IH}	0.8 VCC	--	VCC	V	Digital input pins
Input Low Voltage	V_{IL}	0	--	0.2 VCC	V	Digital input pins
Output High Voltage	V_{OH}	0.9 VCC	--	VCC	V	Digital output pins
Output Low Voltage	V_{OL}	0	--	0.1 VCC	V	Digital output pins
Logic Input Current	I_{IL}/I_{IH}	-1	--	1	uA	--

7. Timing Characteristics
7.1 Pixel Timing Characteristics
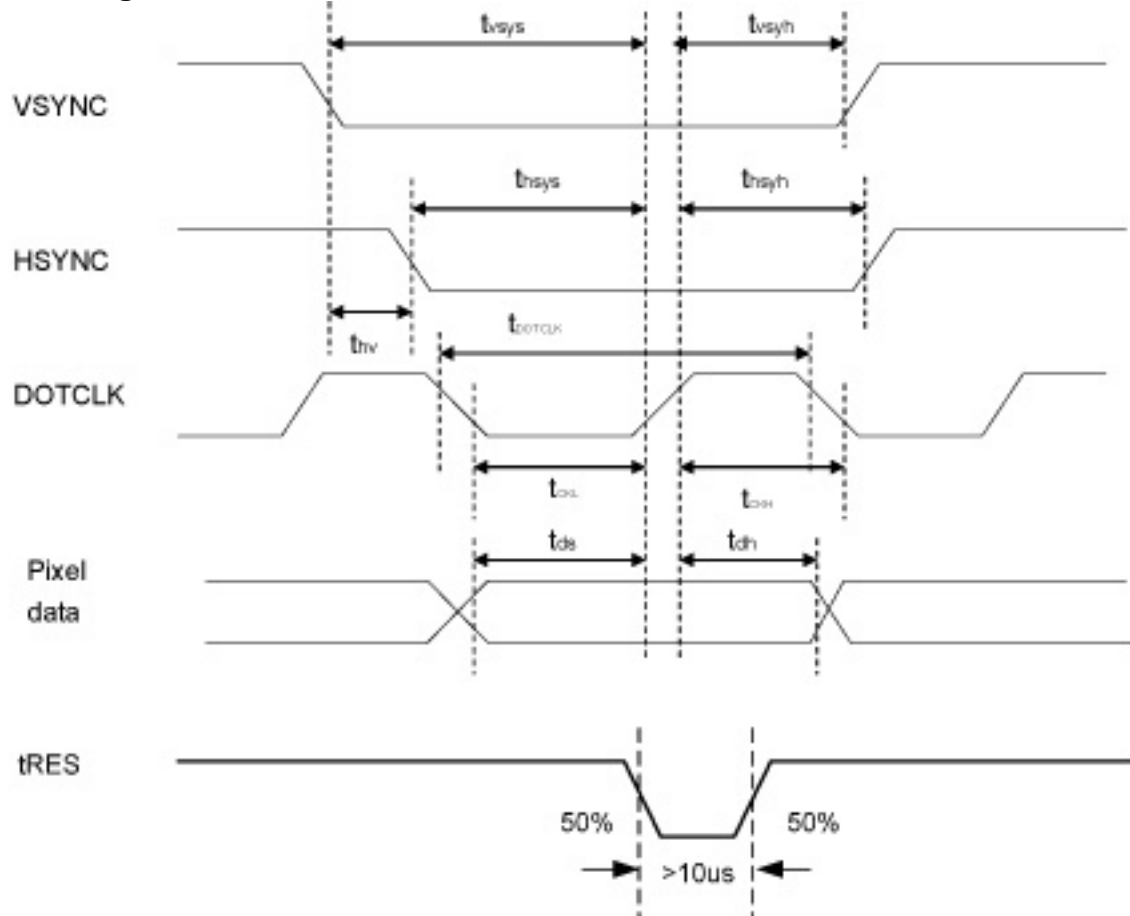
Figure 7. 1 Pixel Timing

Characteristics	Symbol	Min.		Typ.		Max.		Unit
		24 bit	8 bit	24 bit	8 bit	24 bit	8 bit	
DOTCLK Frequency	fDOTCLK	-	-	6.5	19.5	10	30	MHz
DOTCLK Period	tDOTCLK	100	33.3	154	51.3	-	-	ns
Vertical Sync Setup Time	tvsys	20	10	-	-	-	-	ns
Vertical Sync Hold Time	tvsyh	20	10	-	-	-	-	ns
Horizontal Sync Setup Time	thsys	20	10	-	-	-	-	ns
Horizontal Sync Hold Time	thsyh	20	10	-	-	-	-	ns
Phase difference of Sync Signal Falling Edge	thv	1		-		240		tDOTCLK
DOTCLK Low Period	tCKL	50	15	-	-	-	-	ns
DOTCLK High Period	tCKH	50	15	-	-	-	-	ns
Data Setup Time	tds	12	10	-	-	-	-	ns
Data hold Time	tdh	12	10	-	-	-	-	ns
Reset pulse width	tRES	10		-		-		μs

Note: External clock source must be provided to DOTCLK pin of HX8238-D. The driver will not operate if absent of the clocking signal.

Table 7. 1 Pixel Timing

7.2 RGB Interface Timing Characteristics
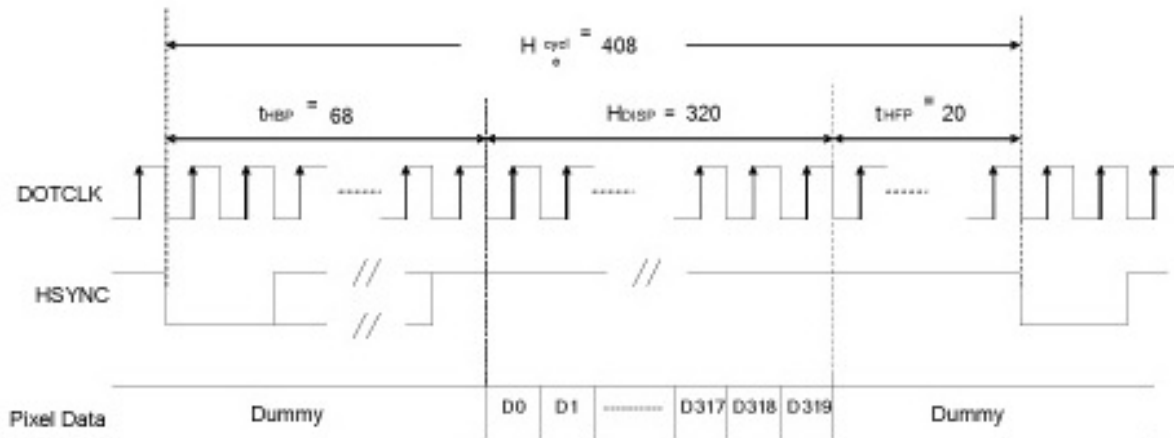
(a) Horizontal Data Transaction Timing
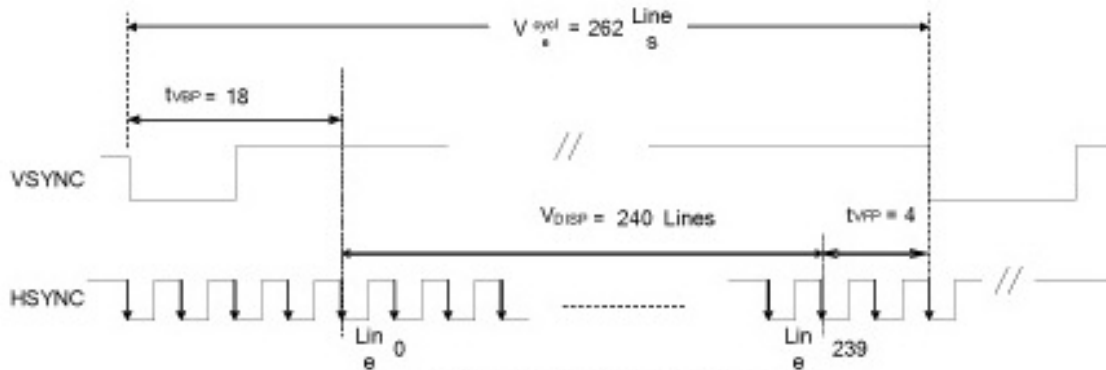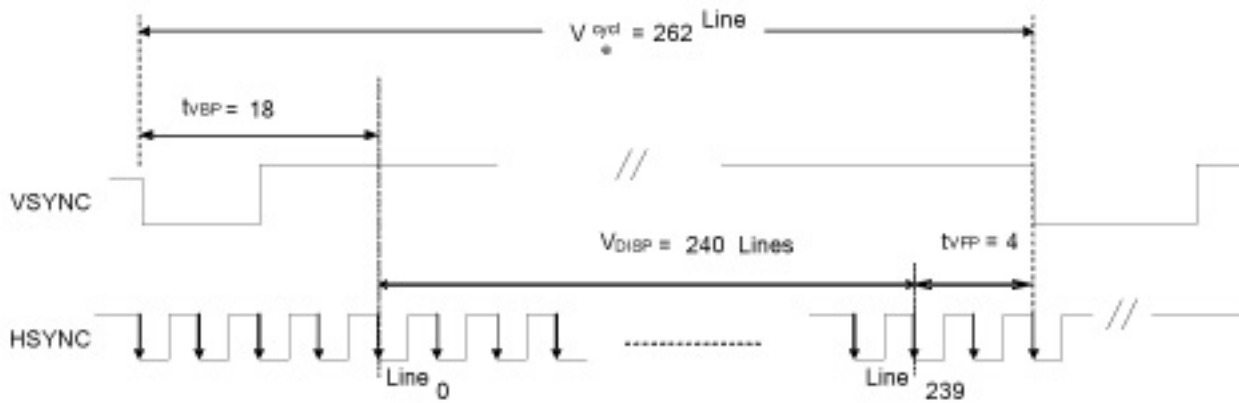

(b) Vertical Data Transaction Timing

Figure 7. 2 Data Transaction Timing in Parallel RGB (24 bit) Interface (SYNC Mode)

Characteristics		Symbol	Min.		Typ.		Max.		Unit
			24 bit	8 bit	24 bit	8 bit	24 bit	8 bit	
DOTCLK Frequency		fDOTCLK	-	-	6.5	19.5	10	30	MHz
DOTCLK Period		tDOTCLK	100	33.3	154	51.3	-	-	ns
Horizontal Frequency (Line)		fH	-		14.9		22.35		KHz
Vertical Frequency (Refresh)		fV	-		60		90		Hz
Horizontal Back Porch		tHBP	-	-	68	204	-	-	tDOTCLK
Horizontal Front Porch		tHFP	-	-	20	60	-	-	tDOTCLK
Horizontal Data Start Point		tHBP	-	-	68	204	-	-	tDOTCLK
Horizontal Blanking Period		tHBP + tHFP	-	-	88	264	-	-	tDOTCLK
Horizontal Display Area		HDISP	-	-	320	960	-	-	tDOTCLK
Horizontal Cycle		Hcycle	-	-	408	1224	450	1350	tDOTCLK
Vertical Back Porch		tVBP	-		18		-		Lines
Vertical Front Porch		tVFP	-		4		-		Lines
Vertical Data Start Point		tVBP	-		18		-		Lines
Vertical Blanking Period		tVBP + tVFP	-		22		-		Lines
Vertical Display Area	NTSC	VDISP	-		240		-		Lines
	PAL				280(PALM=0)				
	PAL				288(PALM=1)				
Vertical Cycle	NTSC	Vcycle	-		262		350		Lines
	PAL				313				

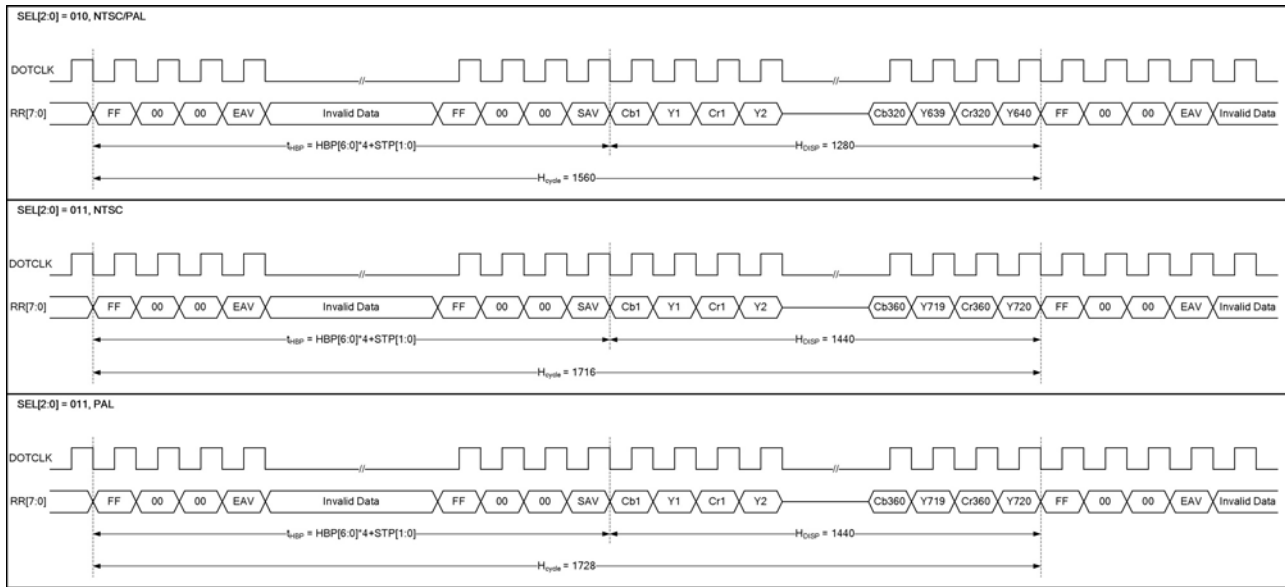**Table 7. 2 Data Transaction Timing in Normal Operating Mode**
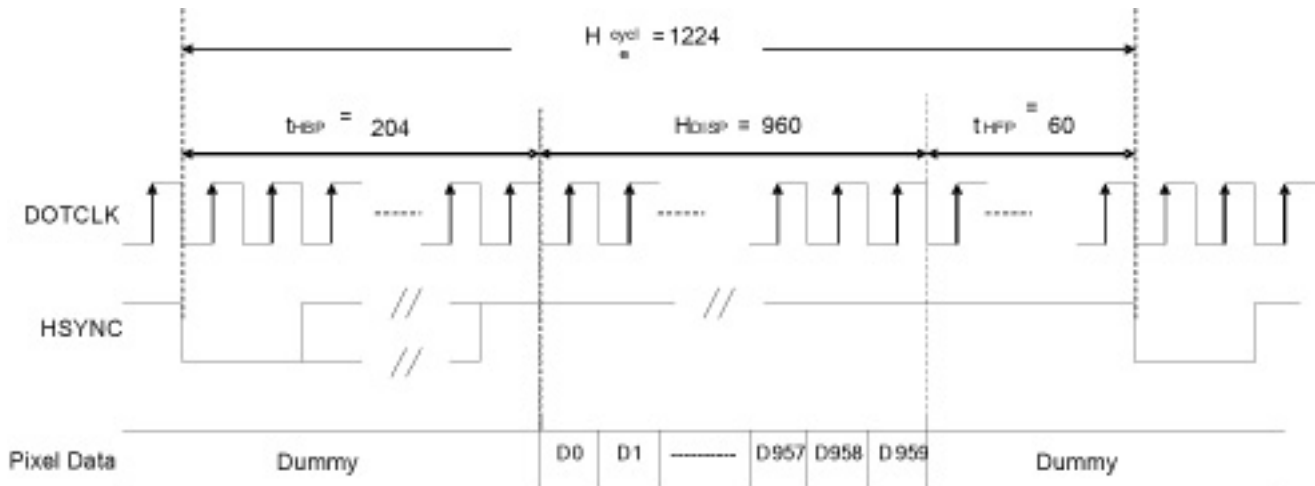
(1) Horizontal Data Transaction Timing

(2) Vertical Data Transaction Timing

Figure 7. 3 Data Transaction Timing in Serial RGB (8 bit) Interface (SYNC Mode)

7.3 CCIR656 Timing Characteristics
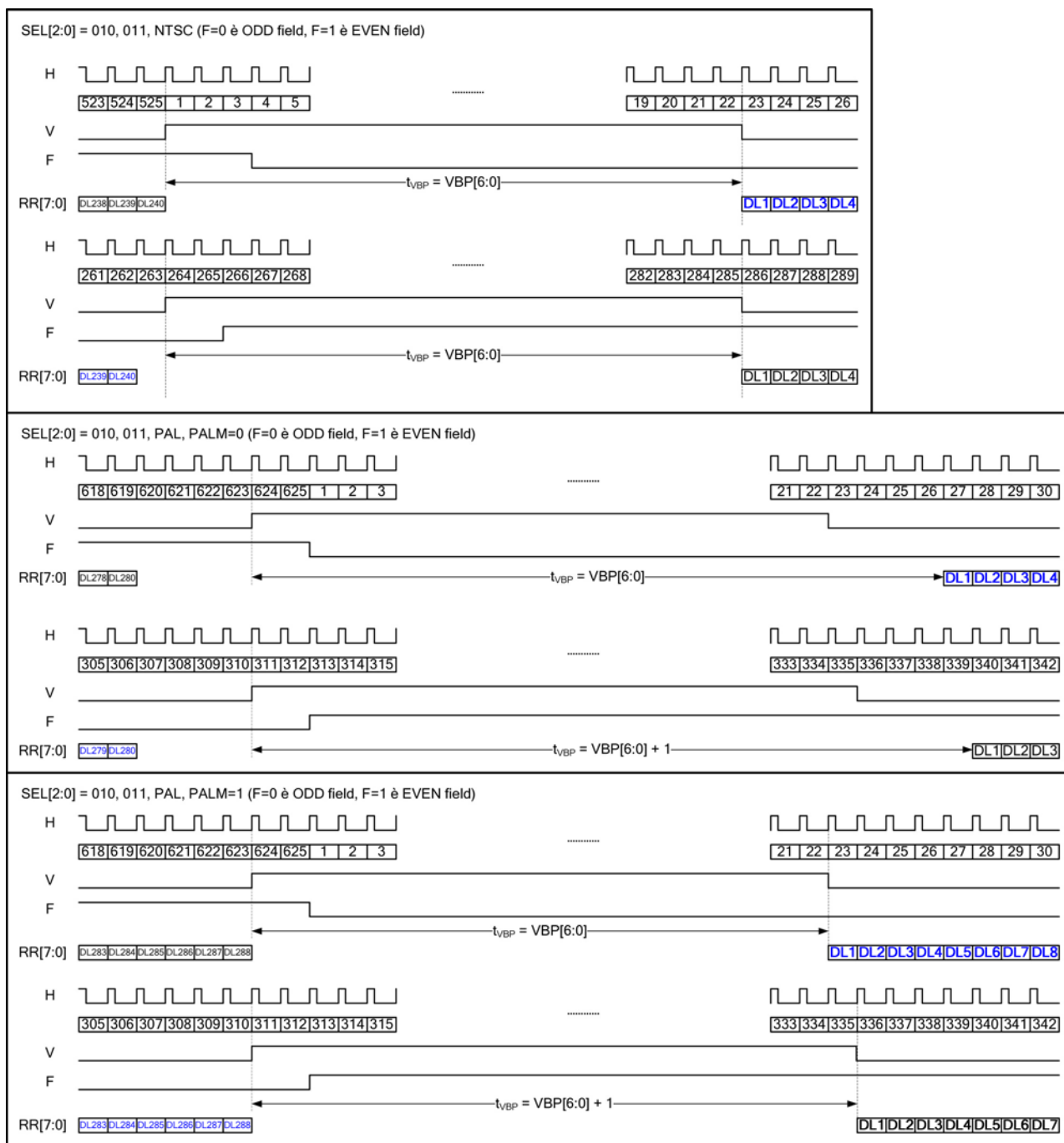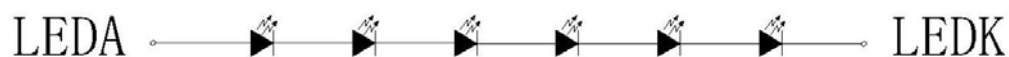

Figur 7.4 CCIR656 Horizontal Timing

Figure 7.5 CCIR656 Vertical Timing

## 8. Backlight Characteristics

BL Circuit Diagram:

LEDA ⎯⎯⎯ ⎯⎯⎯ ⎯⎯⎯ ⎯⎯⎯ ⎯⎯⎯ ⎯⎯⎯ ⎯⎯⎯ LEDK

Item	Symbol	MIN	TYP	MAX	UNIT	Test Condition
Supply Voltage	Vf	18	19.2	20	V	If=20mA
Supply Current	If	--	20	--	mA	--
Luminous Intensity for LCM	--	380	450	--	Cd/m^2	If=20mA
Uniformity for LCM	--	80	--	--	％	If=20mA
Life Time	--	50000	--	--	Hr	If=20mA
Backlight Color	White					

9. Optical Characteristics

(Note1，Note2) (Using Normal Polarizer +CPT Backlight, reference only)

ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
Brightness		BL	$\theta = \varphi = 0°$	380	450	--	cd/m²	Note2
Contrast Ratio		CR	$\theta = \varphi = 0°$	150	250	--	--	Note3
Response Time		Tr+ Tf	$\theta = \varphi = 0°$	--	50	70	ms	Note4
Viewing Angle	Upper	θ	CR$\geqq$10	--	45	--	--	Note 5
	Down			--	45	--	--	
	Right	φ		--	35	--	--	
	Left			--	15	--	--	
Color Filter Chromaticity	White	X	$\theta = \varphi = 0°$	0.264	0.294	0.324	--	Note 6
		y		0.304	0.334	0.364	--	
	Red	X	$\theta = \varphi = 0°$	0.603	0.633	0.663	--	
		y		0.296	0.326	0.356	--	
	Green	X	$\theta = \varphi = 0°$	0.267	0.297	0.327	--	
		y		0.547	0.577	0.607	--	
	Blue	X	$\theta = \varphi = 0°$	0.103	0.133	0.163	--	
		y		0.099	0.129	0.159	--	

Note1: Definition of Operation Voltage (Vop)

$Cr = Loff / Lon$
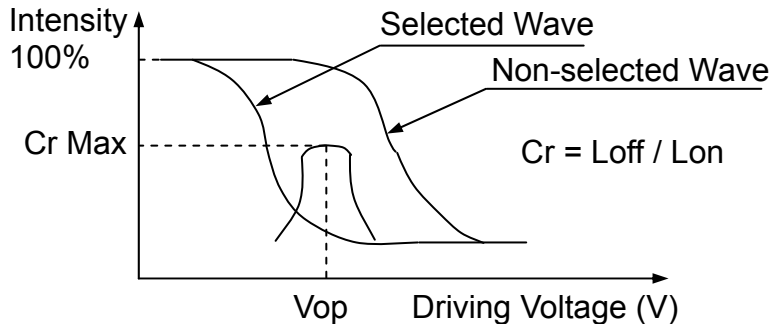
Note2: Definition of Luminance Uniformity：$L = L(MIN) / L(MAX) \times 100\%$
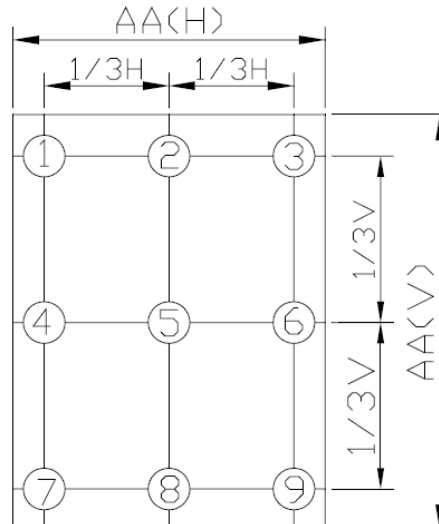
Note 3. Definition of Contrast Ratio：
    CR = White Luminance (ON) / Black Luminance (OFF)

Note 4. Definition of response time：The response time is defined as the time interval between the 10% and 90% amplitudes.
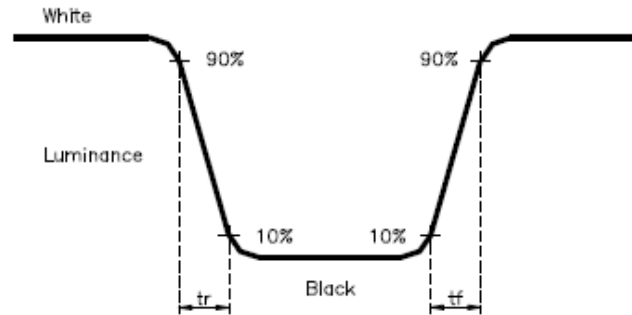
Note 5. Definition of view angle(θ，ψ)：
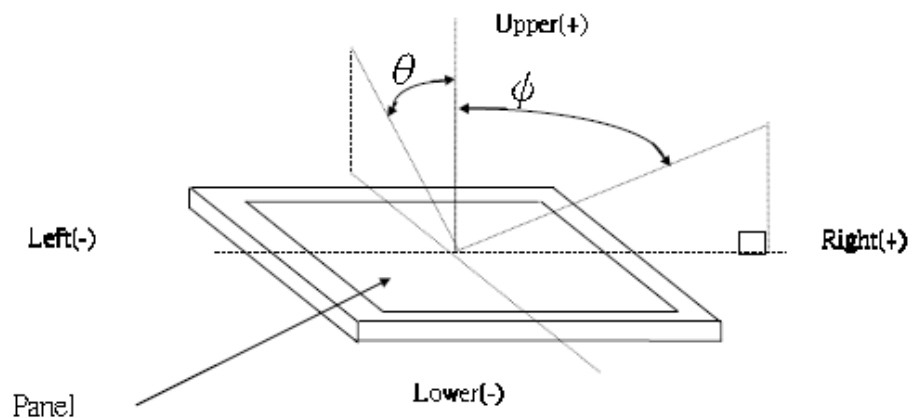
Note 6. Light source: C light.

10. Touch Panel Characteristics
10.1 General Standard Specifications

Item	Specification
Input Method	Finger or Stylus pen
ITO Glass	T=0.70mm
ITO Film	T=0.188mm, Clear Hard Coating & Double-layer
Operating Temperature Range	-20°C ~+70°C 20%~90%RH (Except for dew gathering)
Storage Temperature Range	-30°C ~+80°C 20%~90%RH (Except for dew gathering)
Surface Hardness	≥3H
Hitting Durability	1,000,000 times(pressure 150g , frequency 2 time/s)
Pen Sliding Durability	100,000 times(pressure 150g , speed 60mm/s)
Light Transparency	80% min.
Activation Force	30~70g individual point on with polyacetal stylus pen (R0.8mm)

10.2 Electrical Characteristic Specification

Item	Specification
Operating Voltage	DC 5V
Circuit Resistance	X- axis :200~900Ω
	Y- axis : 200~900Ω
Insulation Resistance	10MΩ（25V DC）
Chatting	< 10 ms

11. Reliability Test Conditions and Methods

NO.	TEST ITEMS	TEST CONDITION	INSPECTION AFTER TEST
	High Temperature Storage	80°C±2°C×200Hours	Inspection after 2~4hours storage at room temperature, the samples should be free from defects: 1, Air bubble in the LCD. 2, Seal leak. 3, Non-display. 4, Missing segments. 5, Glass crack. 6, Current IDD is twice higher than initial value. 7, The surface shall be free from damage. 8, The electric characteristic requirements shall be satisfied.
	Low Temperature Storage	-30°C±2°C×200Hours	
	High Temperature Operating	70°C±2°C×120Hours	
	Low Temperature Operating	-20°C±2°C×120Hours	
	Temperature Cycle(Storage)	-20°C ⟷ 25°C ⟷ 70°C (30min) (5min) (30min) 1cycle Total 10cycle	
	Damp Proof Test (Storage)	50°C±5°C×90%RH×120Hours	
	Vibration Test	Frequency:10Hz~55Hz~10Hz Amplitude:1.5M X,Y,Z direction for total 3hours (Packing Condition)	
	Drooping Test	Drop to the ground from 1M height one time every side of carton. (Packing Condition)	
	ESD Test	Voltage:±8KV,R:330Ω,C:150PF,Air Mode,10times	

REMARK:
1, The Test samples should be applied to only one test item.
2, Sample side for each test item is 5~10pcs.
3,For Damp Proof Test, Pure water(Resistance＞10MΩ)should be used.
4,In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.
5, EL evaluation should be accepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.
6, Failure Judgment Criterion: Basic Specification Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

12. Inspection Standard
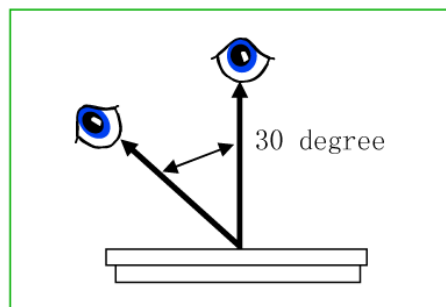
This standard apply to C-STN/TFT module

1. **Spot check plan：**

   According to spot check level Ⅱ,MIL-STD-105D Level Ⅱ,the rank of accept or reject is below:

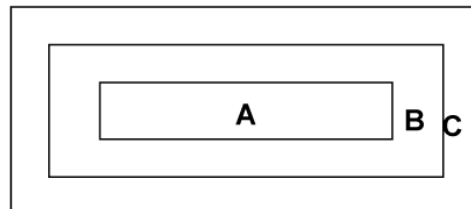   3A、2A：major non-conformance：AQL 0.25 minor non-conformance：AQL 0.4

   1A：major non-conformance： AQL 0.65   minor non-conformance： AQL 1.

2. **Inspection condition：**

30 degree

   Under daylight lamp 20～40W，product distance inspector 'eye 30cm,incline degree 30°。

3. **LCD area define：**

A   B C

   Area A：display area

   Area B：VA area

   Area C：out of VA area, not in sight after assembly
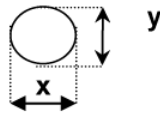
Remark: non-conformance at area C, but is OK that isn't influence reliability of product & assembly by customer.
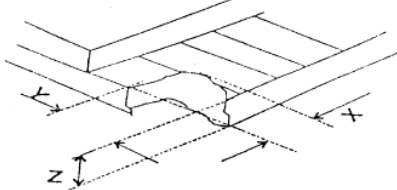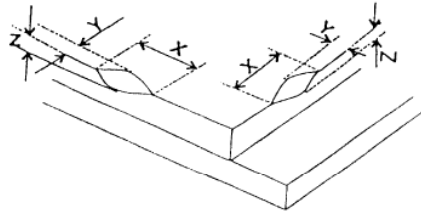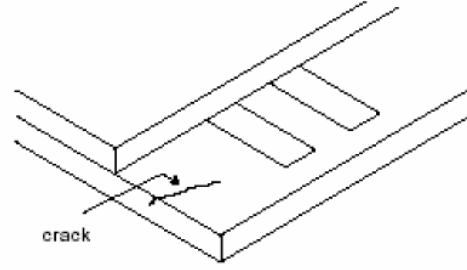
4. Inspection standard

4.1 Major non-conformance

NO.	Item	Inspection standard	Rate
4.1.1	**Function non-conformance**	1) No display, display abnormally 2) Miss line, short 3) B/L no function or function abnormally 4) TP no function	**major**
4.1.2	**miss**	No matter miss what component	
4.1.3	**Out of size**	Module dimension out of spec	

4.2 Appearance non-conformance

NO.	Item	Inspection standard	Rate
4.2.1	**Black or white spot （power on）**	dot non-conformance define Φ $$\Phi = \frac{+y)}{2} \quad x \quad ($$ **A grade** <table><tr><td rowspan="2">area size（mm）</td><td colspan="3">Most approve q'ty</td></tr><tr><td>A</td><td>B</td><td>C</td></tr><tr><td>Φ≤0.10</td><td colspan="2">ignore</td><td rowspan="5">ignore</td></tr><tr><td>0.10＜Φ≤0.15</td><td colspan="2">4</td></tr><tr><td>0.15＜Φ≤0.20</td><td colspan="2">2</td></tr><tr><td>0.20＜Φ≤0.25</td><td colspan="2">1</td></tr><tr><td>0.25＜Φ</td><td colspan="2">0</td></tr></table> Most approve 4 damages, dot to dot ≥10mm	**Minor**
4.2.2	**Black or white line （power on）**	**A grade** <table><tr><td colspan="2">Size(mm)</td><td colspan="3">Most approve q'ty</td></tr><tr><td>L(length）</td><td>W(width)</td><td>A</td><td>B</td><td>C</td></tr><tr><td>ignore</td><td>W≤0.03</td><td colspan="2">ignore</td><td rowspan="4">ignore</td></tr><tr><td>L≤5.0</td><td>0.03＜W≤0.05</td><td colspan="2">3</td></tr><tr><td>L≤3.0</td><td>0.05＜W≤0.07</td><td colspan="2">2</td></tr><tr><td></td><td>0.07＜W</td><td colspan="2">Treat with dot non-conformance</td></tr></table> Most approve 3 damages, line to line ≥10mm	**Minor**

4.2.3	Polarizer position	1) Polarizer attach meet drawing, disallow out of LCD. 2) Polarizer must cover display area（special require unless）	Minor
4.2.4	LCD non-conform ance	(i) crash at side（remark: S=ITO length） | X	Y	Z | |---|---|---| | ≤3.0 | ≤S | ignore | Crash disallow extend to ITO or seal. (ii) commonly surface scathe | X | Y | Z | |---|---|---| | ≤2.0 | \<frame edge | ignore | (iii) crack Disallow extend crack crack	Minor
4.2.5	Contrast voltage warp	VOP/Vlcd voltage of confirmed sample±0.15V	Minor
4.2.6	color	Color & luminance of module scope reference spec	Minor
4.2.7	Cross talk	Reference confirmed limit sample	Minor

13. Handling Precautions

13.1 Mounting method

The LCD panel of AMSON TFT module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

13.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent
[Recommended below] and wipe lightly
- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:
- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns
Do not use the following solvent on the pad or prevent it from being contaminated:
- Soldering flux
- Chlorine (CI) , Sulfur (S)

If goods were sent without being sili8con coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

13.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to VDDIO or GND, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

13.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

13.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the maximum operating temperature, 50%Rh or less is required.

13.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
  [It is recommended to store them as they have been contained in the inner container at the time of delivery from us.

13.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

14. Precaution for Use

14.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

14.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification.
- When a new problem is arisen this is not specified in this specification.
- When an inspection specifications change or operating condition change in customer is reported to AMSON TFT and some problem is arisen in this specification due to the change.
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

15. Packing Method
TBD