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Specification for Approval

Customer:	
Model Name:	

Supplier Approval			Customer approval
R&D Designed	R&D Approved	QC Approved	
Peter	Peng Jun		

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Revision Record

REV NO.	REV DATE	CONTENTS	Note
Α	2018-09-10	NEW ISSUE	

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1. Scope

This specification defines general provisions as well as inspection standards for TFT module supplied by AMSON electronics.

If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution

2. General Information

LCM

ITEM	STANDARD VALUES	UNITS
LCD type	7.0"TFT	
Dot arrangement	800×3(RGB)×480	dots
Color filter array	RGB vertical stripe	
Display mode	TN / Transmissive / Normally white	-
Gray Scale Inversion Direction	6 o'clock	
Eyes Viewing Direction	12 o'clock	
Module size	165.0(W)×100.00(H)×7.55(T)	mm
Active area	154.08(W)×85.92(H)	mm
Dot pitch	0.1926(W)×0.1790(H)	mm
Interface	24-bit Parallel RGB Interface	
Operating temperature	-20 ~ +70	°C
Storage temperature	-30 ~ +80	°C
Back Light	27 White LEDS	
Weight	TBD	g

CTP

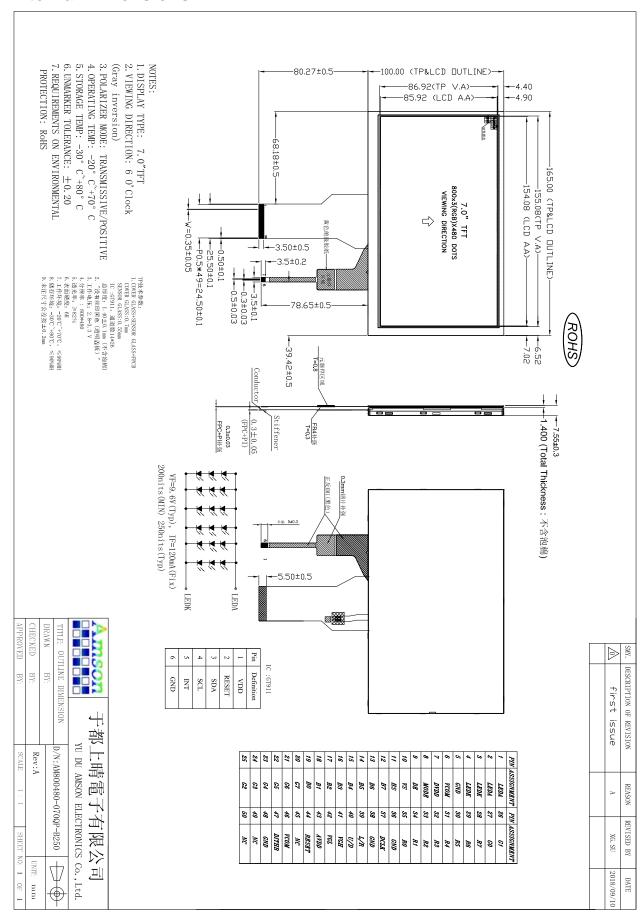
ITEM	STANDARD VALUES	UNITS
CTP type	Glass + Glass +FPC	
CTP Driver IC	GT911	
Surface hardness	6H	
Transmittance	≥82%	
CTP size	165.0(W)×100.0H)×1.4(T)	mm
CTP Viewing area	155.08(W)×86.92 (H)	mm
Operating temperature	-20 ~ +70	°C
Storage temperature	-30 ~ +80	°C
CTP Interface	I ² C	



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3. External Dimensions





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4. Interface Description TFT Interface

II I IIILE						
PIN	PIN NAME	DESCRIPTION				
1	LEDA	I ED backlight (Anode)				
2	LEDA	LED backlight (Anode).				
3	LEDK	LED backlight (Cathodo)				
4	LEDK	LED backlight (Cathode).				
5	GND	Power ground				
6	VCOM	Common Voltage.				
7	DVDD	Digital Power.				
8	MODE	DE/SYNC mode select. Normally pull high. H: DE mode. L: HSD/VSD mode.				
9	DE	Data Enable signal.				
10	VS	Vertical sync input. Negative polarity.				
11	HS	Horizontal sync input. Negative polarity.				
12~19	B7~B0	Blue Data Input				
20~27	G7~G0	Green Data Input				
28~35	R7~R0	Red Data Input				
36	GND	Power ground.				
37	DCLK	Clock input.				
38	GND	Power ground.				
39	L/R	Left or Right Display Control.				
40	U/D	Up / Down Display Control.				
41	VGH	Positive Power for TFT.				
42	VGL	Negative Power for TFT.				
43	AVDD	Analog Power.				
44	RESET	Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high.(R=10K Ω , C=1 μ F)				
45	NC.	Not connect.				
46	VCOM	Common Voltage.				
47	DITHB	Dithering function enable control. (Normally pull high) DITHB="L", to enable internal dithering function. DITHB="H", to disable internal dithering function.				
48	GND	Power ground.				
49	NC.	Not connect.				
50	NC.	Not connect.				



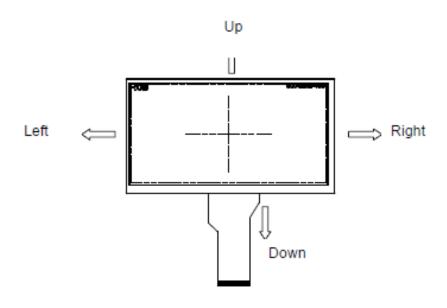
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[Note1] L/R: left or right setting U/D: up or down setting

L/R	U/D	Data shifting
DVDD	GND	Left \rightarrow Right, Up \rightarrow Down(default)
GND	GND	$Right \to Left, \ Up \to Down$
DVDD	DVDD	$Left \to Right, \; Down \to Up$
GND	DVDD	$Right \to Left, \; Down \to Up$

Definition of scanning direction:



CTP

PIN NO.		PIN NAME				
1	3.3V	CTP Digital Power.				
2	RESET	CTP reset pin. Active low to enter reset state.				
3	SDA	CTP I ² C_data				
4	SCL	CTP I ² C_clock.				
5	INTN	CTP interruption signal.				
6	GND	CTP Power ground				



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5. Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit
Digital Supply Voltage	DVDD	-0.3	5.0	V
Analog Supply Voltage	AVDD	6.5	13.5	V
Gate On Voltage	VGH -0.3 40.0		40.0	V
Gate Off Voltage	VGL	-20.0	0.3	V
Gate On- Gate Off Voltage	VGH-VGL	-	40.0	V
Operating Temperature	Тор	-20	70	°C
Storage Temperature	Тѕт	-30	85	°C
Storage Humidity	HD	20	90	%RH

6. DC Characteristics

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Digital Supply Voltage	DVDD	3.0	3.3	3.6	٧	-
Analog Supply Voltage	AVDD	10.2	10.4	10.6	V	-
Gate On Voltage	VGH	15.3	16.0	16.7	٧	-
Gate Off Voltage	VGL	-7.7	-7.0	-6.3	V	-
Common Voltage	VCOM	3.8	4.0	4.2	V	-
Logic logut Voltage	VIH	0.7DVDD	-	DVDD	V	-
Logic Input Voltage	VIL	GND	-	0.3DVDD	V	-

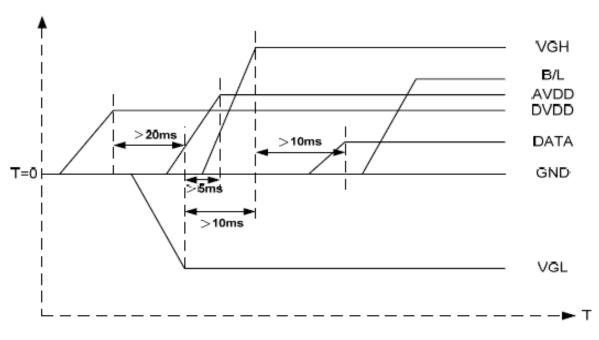
NOTE: VCOM 电压根据客户主板实际效果而定

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7. Timing Characteristics

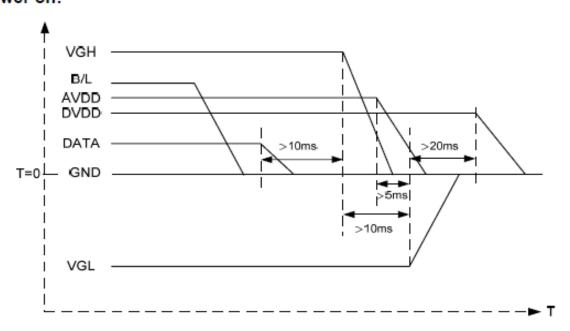
7.1 Power sequence

a. Power on:



 $DV_{DD} \rightarrow VGL \rightarrow VGH \rightarrow Data \rightarrow B/L$

b. Power off:



 $B/L \rightarrow Data \rightarrow VGH \rightarrow VGL \rightarrow DV_{DD}$

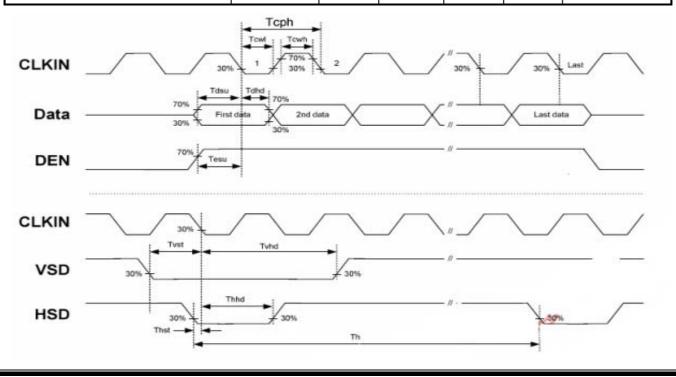
Note: Data include R0~R7, B0~B7, GO~G7, U/D, L/R, DCLK, HS,VS,DE.

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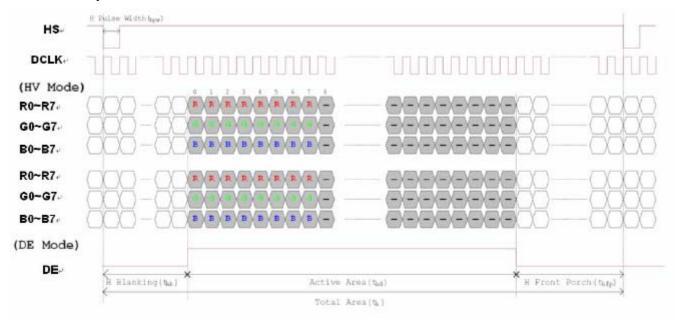
7.2 Timing characteristics 7.2.1 AC Electrical Characteristics

Itom	Cumbal		Values		Unit	Remark
Item	Symbol	Min.	Тур.	Max.	Onit	
HS setup time	Thst	8	-	-	ns	
HS hold time	Thhd	8	-	-	ns	
VS setup time	Tvst	8	-	-	ns	
VS hold time	Tvhd	8	ŀ	-	ns	
Data setup time	Tdsu	8	-	-	ns	
Data hole time	T _{dhd}	8	-	-	ns	
DE setup time	Tesu	8	-	-	ns	
DE hole time	Tehd	8	-	-	ns	
DV _{DD} Power On Slew rate	Tpor	-	-	20	ms	From 0 to 90% DV _{DD}
RESET pulse width	T _{Rst}	1	-	-	ms	
DCLK cycle time	Tooh	20	-	-	ns	
DCLK pulse duty	Towh	40	50	60	%	

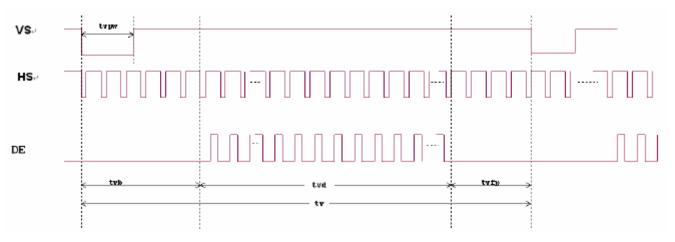


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7.2.2 Data Input Format



Horizontal input timing diagram



Vertical input timing diagram

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7.2.3 Timing

Item	Symbol		Values		Unit	Remark
item	Symbol	Min.	Min. Typ. Max.		Oiiit	Kelliaik
Horizontal Display Area	thd	-	800	-	DCLK	
DCLK Frequency	fclk	26.4	33.3	46.8	MHz	
One Horizontal Line	th	862	1056	1200	DCLK	
HS pulse width	ţhpw	1	-	40	DCLK	
HS Blanking	thb	46	46	46	DCLK	
HS Front Porch	thfp	16	210	354	DCLK	

Itama	Cumbal		Values	Values		Remark
Item	Symbol M	Min.	Тур.	Max.	Unit	Remark
Vertical Display Area	tvd	-	480	-	TH	
VS period time	tv	510	525	650	TH	
VS pulse width	tvpw	1	-	20	TH	
VS Blanking	tvb	23	23	23	TH	
VS Front Porch	tvfp	7	22	147	TH	

7.3 CTP Timing Characteristics 7.3.1 IIC communication timing

Test condition: VDD=3.3V,IOVCC=3.3V,SCL=400kHz, pull high resistance 2.0k Ω

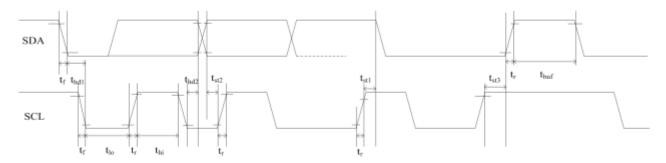
Parameter	Unit	Min	Max
SCL frequency	KHz	-	400
Bus free time between a STOP and START condition	us	1.3	
Hold time (repeated) START condition	us	0.6	
Data setup time	ns	100	
Setup time for a repeated START condition	us	0.6	
Setup Time for STOP condition	us	0.6	

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7.3.2 IIC Slave Address

GT911 provides a standard I²C interface for SCL and SDA to communicate with the host. GT911 always serves as slave device in the system with all communication being initialized by the host. It is strongly recommended that transmission rate be kept at or below 400Kbps. The I²C timing is shown below:



Test condition 1: 1.8V host interface voltage, 400Kbps transmission rate, 2K pull-up resistor

Parameter	Symbol	Min.	Max.	Unit
SCL low period	tio	1.3	-	us
SCL high period	thi	0.6	-	us
SCL setup time for Start condition	t _{st1}	0.6	-	us
SCL setup time for Stop condition	t _{st3}	0.6	-	us
SCL hold time for Start condition	t _{hd1}	0.6	-	us
SDA setup time	tst2	0.1	-	us
SDA hold time	t _{hd2}	0	-	us

Test condition 2: 3.3V host interface voltage, 400Kbps transmission rate, 2K pull-up resistor

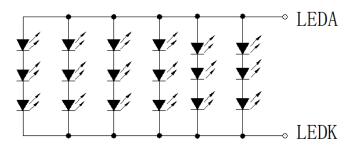
Parameter	Symbol	Min.	Max.	Unit
SCL low period	tio	1.3	-	us
SCL high period	thi	0.6	-	us
SCL setup time for Start condition	t _{st1}	0.6	-	us
SCL setup time for Stop condition	t _{st3}	0.6	-	us
SCL hold time for Start condition	t _{hd1}	0.6	-	us
SDA setup time	t _{st2}	0.1	-	us
SDA hold time	t _{hd2}	0	-	us

GT911 supports two I^2C slave addresses: 0xBA/0xBB and 0x28/0x29. The host can select the address by changing the status of Reset and INT pins during the power-on initialization phase. See the diagram below for configuration methods and timings:

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8. Backlight Characteristic



VF=9.6V(Typ), IF=120mA(Fix)

Item	Symbol	MIN	TYP	MAX	UNIT	Test Condition
Supply Voltage	Vf	8.7	9.6	10.5	V	lf=120mA
Supply Current	If	-	120	-	mA	-
Luminous Intensity for LCM	-	180	250	-	cd/m ²	If=120mA
Uniformity for LCM	-	80	-	-	%	lf=120mA
Life Time	-	-	50000	-	Hr	If=120mA
Backlight Color	White					



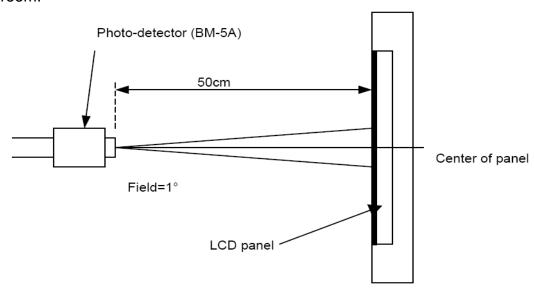
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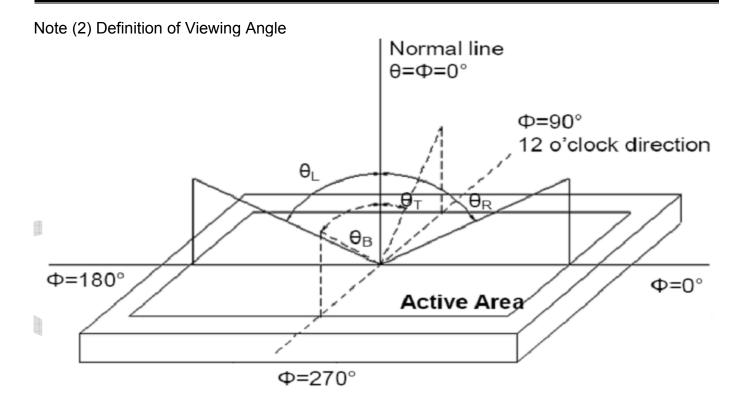
9. Optical Characteristics

Item	Conditions		Min.	Тур.	Max.	Unit	Note	
	Horizontal	θL	60	70	-			
Viewing Angle	ПОПДОПІАІ	θR	60	70	-	dograo	(4) (2) (6)	
(CR>10)	Vartical	θт	40	50	-	degree	(1),(2),(6)	
	Vertical	θв	60	70	-			
Contrast Ratio	Center		400	500	-	-	(1),(3),(6)	
Dooponoo Timo	Rising	ising		10	20	mo	(1) (4) (6)	
Response Time	Falling		-	15	30	ms	(1),(4),(6)	
	Red x			TBD		-		
	Red y Green x			TBD		-		
				TBD		-		
CF Color	Green y		Тур.	TBD	Тур.	-	(4) (6)	
Chromaticity (CIE1931)	Blue x		-0.05	TBD	+0.05	-	(1), (6)	
	Blue y			TBD		-		
	White x			TBD		-		
	White y			TBD		-		

Note (1) Measurement Setup: The LCD module should be stabilized at given temp. 25°C for 15 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 15 minutes in a windless room.



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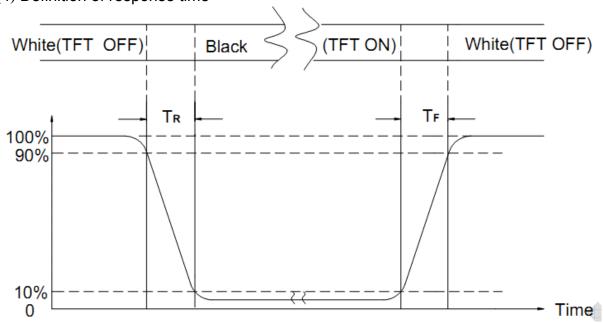


Note (3) Definition of Contrast Ratio (CR)

The contrast ratio can be calculated by the following expression Contrast Ratio (CR) = L63 / L0

L63: Luminance of gray level 63, L0: Luminance of gray level 0

Note (4) Definition of response time



Note (5) Definition of Transmittance (Module is without signal input)

Transmittance = Center Luminance of LCD / Center Luminance of Back Light x 100%

Note (6) Definition of color chromaticity (CIE1931)

Color coordinates measured at the center point of LCD



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10. Reliability Test Conditions and Methods

NO.	TEST ITEMS	ditions and Methods TEST CON	NDITION				
1)	High Temperature Storage	Keep in 80°C ±5°C 96 hrs Surrounding temperature, then storage at normal condition 4hrs.					
2	Low Temperature Storage	Keep in -30°C ±5°C 96 hrs Surrounding temperature, then ste	orage at normal condition 4hrs.				
3	High Temperature / High Humidity Storage Test	Keep in 50 ℃ / 90% R.H duration Surrounding temperature, then stee (Excluding the polarizer)					
4	Temperature Cycling Storage Test	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$					
5	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance : 15° C \sim 35 $^{\circ}$ C 2. Humidity relative : $30\%\sim60\%$					
		 Energy Storage Capacitance(Cs + Cd): 150pF±10% Discharge Resistance(Rd): 330Ω±10% Discharge, mode of operation: Single Discharge (time between successive discharges at lea 1 sec) (Tolerance if the output voltage indication: ±5% 					
6	Vibration Test (Packaged)	 Sine wave 10~55 Hz frequency (1 min/sweep) The amplitude of vibration :1.5 mm Each direction (X, Y, Z) duration for 2 Hrs 					
		Packing Weight (Kg) 0 ~ 45.4	Drop Height (cm) 122				
7	Drop Test (Packaged)	45.4 ~ 90.8 90.8 ~ 454 Over 454	76 61 46				
		Drop Direction: **1 corner / 3 edges / 6 sides each 1time					



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11. Inspection Standard

11.1. QUALITY:

THE QUALITY OF GOODS SUPPLIED TO PURCHASER SHALL COME UP TO THE FOLLOWING STANDARD.

11.1.1. INSPECTIONTOOLS AND INSTRUMENTS

Vernier calipers, film scales, multimeter, magnifying eyepiece, ND5%, luminance meter and so on.

11.1.2. THE METHOD OF PRESERVING GOODS

AFTER DELIVERY OF GOODS FROM AMSONTO PURCHASER. PURCHASER SHALL CONTROL THE LCM AT -10 TO 40 ,AND IT MIGHT BE DESIRABLE TO KEEP AT THE NORMAL ROOM TEMPERATURE AND HUMIDITY UNTIL INCOMING INSPECTION OR THROWING INTO PROCESS LINE.

11.1.3. INCOMING INSPECTION

(A) THE METHOD OF INSPECTION

IF PURCHASER MAKE AN INCOMING INSPECTION, A SAMPLING PLAN SHALL BE APPLIED ON THE CONDITION THAT QUALITY OF ONE DELIVERY SHALL BE REGARDED AS ONE LOT.

(B) THE STANDARD OF QUALITY

ISO-2859-1 (SAME AS MIL-STD-105E), LEVEL: II

CLASS	AQL(%)
CRITICAL	0.4 %
MAJOR	0.65 %
MINOR	1.5 %

EVERY ITEM SHALL BE INSPECTED ACCORDING TO THE CLASS.

(C) MEASURE

IF AS THE RESULT OF ABOVE RECEIVING INSPECTION, A LOT OUT IS DISCOVERED. PURCHASER SHALL BE INFORM SELLER OF IT WITHIN SEVEN DAYS. BUT FIRST SHIPMENT WITHIN FOURTEEN DAYS.

11.1.4. WARRANTY POLICY

AMSON WILL PROVIDE ONE-YEAR WARRANTY FOR THE PRODUCTS ONLY IF UNDER SPECIFICATION OPERATING CONDITIONS. AMSON WILL REPLACE NEW PRODUCTS FOR THESE DEFECT PRODUCTS WHICH UNDER WARRANTY PERIOD AND BELONG TO THE RESPONSIBILITY OF AMSON.

11.2. CHECKING CONDITION

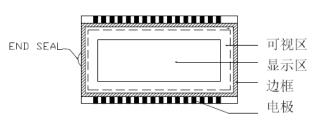
- **11.2.1.**CHECKING DIRECTION SHALL BE IN THE 45 DEGREE AREA TO FACE THE SAMPLE.
- **11.2.2.**CHECKER SHALL SEE OVER 300±25 mm. WITH BARE EYES FAR FROM SAMPLE **11.2.3.**Ambient Illumination:

0 ~30 Lux for functional inspection

500 ~ 1200 Lux for external appearance inspection.

11.2.4. TEST AREA:

11.2.5. Inspection should be carried out with rope electrostatic ring and static finger cover (both hands except small fingers must be worn)



11.2.6. The inspector may make a visual inspection or a comparative examination with a film



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ruler and a magnifying eyepiece. Individual defects shall be determined according to the limited samples.

11.2.7. Functional testing uses electrical testing fixtures or test fixtures required by customers.

11.2.8. the ion fan should be used when testing.

11.2.9. the principle of judgment

11.3.1 If the defect outside the visual area does not affect the assembly and display, it will be judged as a good product.

11.3.2 Poor definition

Pixel:

A combination of three sub-pixels (Red + Green + Blue).



Dot:

Any of the sub-pixels (Red or Green or Blue).







Bright and dark dots:

A point pixel (sub-pixel: R, G, B pixels) is lit or turned off during the display function test. **Highlights**:

Usually considered to be shown on a black screen.

Dark spots:

They are generally considered to be shown on R, G, B solid colors or white images. **Neighborhood**:

Two or three adjacent point pixels (dot: sub-pixel) connected together (R, G or G, B or B, R or RGB).



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11.3. INSPECTION PLAN:

11.5. INST EC	TION PLAN:		
CLASS	ITEM	JUDGEMENT	CLASS
PACKING &	1. OUTSIDE AND INSIDE PACKAGE	"MODEL NO." , "LOT NO." AND "QUANTITY" SHOULD INDICATE ON THE PACKAGE.	Minor
INDICATE	2. MODEL MIXED AND QUANTITY	OTHER MODEL MIXEDREJECTED QUANTITY SHORT OR OVERREJECTED	Critical
	3. PRODUCT INDICATION	"MODEL NO." SHOULD INDICATE ON THE PRODUCT	Major
ASSEMBLY	4. DIMENSION, LCD GLASS SCRATCH AND SCRIBE DEFECT.	ACCORDING TO SPECIFICATION OR DRAWING.	Major
	5. VIEWING AREA	POLARIZER EDGE OR LCD'S SEALING LINE IS VISABLE IN THE VIEWING AREAREJECTED	Minor
	6. BLEMISH - BLACK SPOT - WHITE SPOT IN THE LCD AND LCD GLASS CRACKS	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
APPEARANCE	7. BLEMISH - BLACK SPOT WHITE SPOT AND SCRATCH ON THE POLARIZER	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
	8. BUBBLE IN POLARIZER	ACCORDING TO STANDARD OF VISUAL INSPECTION(INSIDE VIEWING AREA)	Minor
	9. LCD'S RAINBOW COLOR	STRONG DEVIATION COLOR (OR NEWTON RING) OF LCDREJECTED. OR ACCORDING TO LIMITED SAMPLE (IF NEEDED, AND INSIDE VIEWING AREA)	Minor
	10. ELECTRICAL AND OPTICAL CHARACTERISTICS (CONTRAST, VOP, CHROMATICITY ETC)	ACCORDING TO SPECIFICATION OR DRAWING . (INSIDE VIEWING AREA)	Critical
ELECTRICAL	11.MISSING LINE	MISSING DOT: LINE : CHARACTERREJECTED	Critical
	12.SHORT CIRCUIT WRONG PATTERN DISPLAY	NO DISPLAY - WRONG PATTERN DISPLAY - CURRENT CONSUMPTION OUT OF SPECIFICATION REJECTED	Critical
	13. DOT DEFECT (FOR COLOR AND TFT)		Minor



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11.4	<u>. STANI</u>	DARD OF VISUAL INSPECT	ION
NO.	CLASS	ITEM	JUDGEMENT
		BLACK AND WHITE SPOT FOREIGN MATERIEL	(A) ROUND TYPE: unit : mm. DIAMETER (mm.) ACCEPTABLE Q'TY $\Phi \leq 0.2$ DISREGARD
			$0.2 < \Phi \leq 0.4$ 3 (Distance>5mm) $0.4 < \Phi$ 0 NOTE: Φ =(LENGTH+WIDTH)/2
11.4.1	MINOR	DUST IN THE CELL BLEMISH SCRATCH	(B) LINEAR TYPE: unit : mm. LENGTH WIDTH ACCEPTABLE Q*TY W ≤0.05 DISREGARD
			L ≤ 5.0 0.05 < W ≤ 0.08 3 (Distance>5mm) 0.08 < W FOLLOW ROUND TYPE
11.4.2	MINOR	BUBBLE IN POLARIZER DENT ON POLARIZER	$\begin{array}{c cccc} & & & & & & & \\ \hline DIAMETER & & & & & & & \\ \hline & \Phi & \leq 0.3 & & & & & \\ \hline 0.3 < \Phi & \leq 0.6 & & & & & \\ \hline 0.6 < \Phi & & & & & \\ \hline \textbf{x} & & & & & \\ \hline \end{array}$
11.4.3	MINOR	Dot Defect	Items ACC. Q'TY Bright dot N≤5 Dark dot N≤5 Pixel Define: Pixel Pixel Pixel Pixel Pixel Pixel Pixel Pixel Out → Dot → Dot → Dot → Dot Note 1: The definition of dot: The size of a defective dot over 1/2 of whole dot is regarded as one defective dot. Note 2: Bright dot: Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern. Note 3: Dark dot: Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green blue pattern.



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NO.	CLASS	ITEM	JUDGEMEN	Т
11.4.4	MINOR	LCD GLASS CHIPPING	S	Y > S Reject
11.4.5	MINOR	LCD GLASS CHIPPING	SX	X or Y > S Reject
11.4.6	MAJOR	LCD GLASS GLASS CRACK	Y Y	Y > (1/2) T Reject
11.4.7	MAJOR	LCD GLASS SCRIBE DEFECT	A + B	 a> L/3 , A>1.5mm. Reject B: ACCORDING TO DIMENSION
11.4.8	MINOR	LCD GLASS CHIPPING (ON THE TERMINAL AREA)	T	Φ = (x+y)/2 > 2.5 mm Reject
11.4.9	MINOR	LCD GLASS CHIPPING (ON THE TERMINAL SURFACE)	TZX	Y > (1/3) T Reject
11.4.10	MINOR	LCD GLASS CHIPPING	T Z	Y > T Reject



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11.5 INSPECTION STANDARD OF TOUCH PANEL

NO.	CLASS		ITEMS	JUDGEMENT	
11.5.1	MAJOR		Touch Panel Crack		Reject
11.5.2	MINOR	Touch Pan		$X \le 2mm$, $Y \le 2mm$, $Z < 1/2T$	Accept
		Chipping	Edge	x ≤3mm, Y≤3mm, Z<1/2T	Accept
11.5.3	MINOR		Scratch st and Foreign materiel (Linear Type)	W≤0.05, L≤20mm	Accept
		Dust a		0.05mm <w≦0.08mm; l="" ≤10.0mm<br="">Distance between seratch>5.0mm</w≦0.08mm;>	Accept 3 ea Max.
				W>0.08mm	Reject
11.5.4	MINOR		Scratch and Foreign materiel pe: Φ=(Length+Width)/2)	x Φ≤0.3 mm	Accept
				0.3 mm < Φ ≦ 0.6 mm Distance between spots > 5.0mm	Accept 5 ea Max.
				Φ>0.55mm	Reject
11.5.5	MINOR		Touch Panel Dent / Fish Eyes	Φ≤0.55mm	Accept
				0.35mm< Φ ≦1.0mm Distance > 5.0mm	Accept 3 ea Max.
				Φ>2.0mm	Reject
11.5.6	MINOR		Touch Panel Air Bubble	Φ ≤0.2mm	Accept
				0.3 mm < Φ ≦0.6mm Distance between bubbles > 5.0mm	Accept 3 ea Max.
				Φ>0.5mm	Reject
11.5.7	MINOR	Touch Panel		W≦0.05mm, L≦5mm Distance between scratch>5.0mm	Accept 3 ea Max.
		Prin	ng area Scratch	W>0.05mm or L>5mm (W>0.05 Follow 11.5.4 Round type)	Reject
11.5.8	MINOR	Touch Panel White Haze Mark / Dust		Can not be removed	Reject



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12. Handling Precautions

12.1 Mounting method

The LCD panel of AMSON TFT module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

12.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent

[Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (CI), Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

12.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to power or ground, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

12.4 packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

12.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.
 - Usage under the maximum operating temperature, 50%Rh or less is required.



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12.6 storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
 [It is recommended to store them as they have been contained in the inner container at the time of delivery from us

12.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

13. Precaution for Use

13.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

13.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification.
- When a new problem is arisen which is not specified in this specifications?
- When an inspection specifications change or operating condition change in customer is reported to AMSON TFT and some problem is arisen in this specification due to the change.
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

14. Packing Method

TBD