

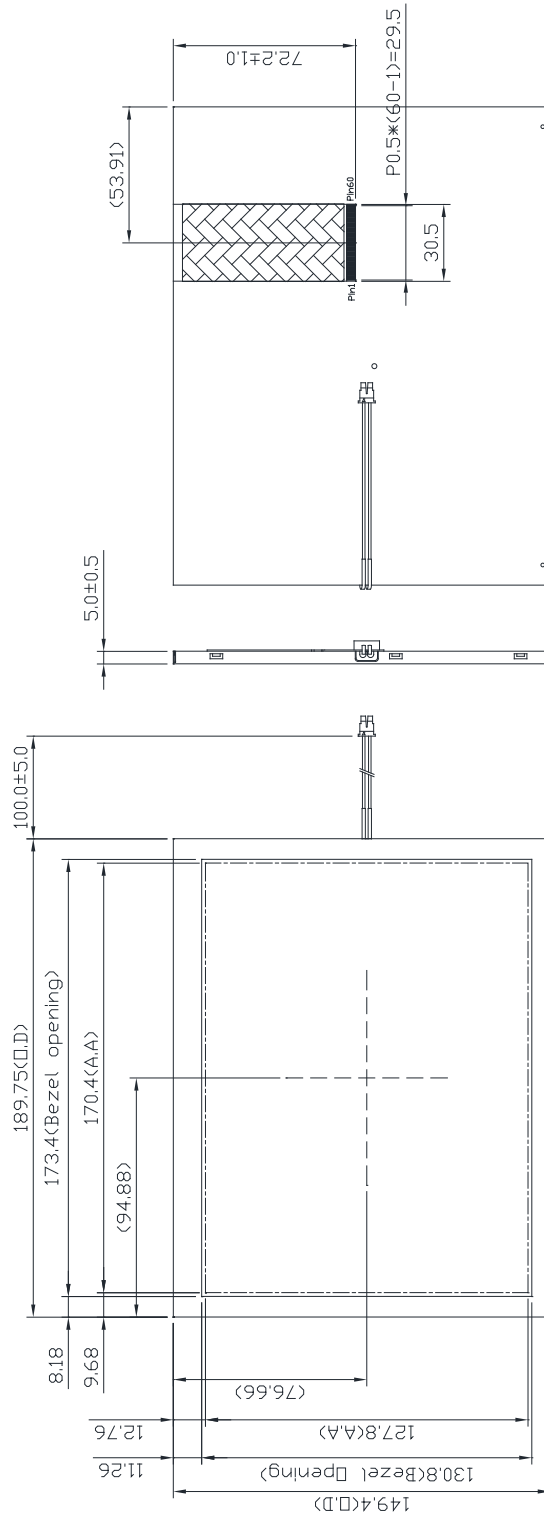
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2.MECHANICAL SPECIFICATIONS

(1)	Number Of Dots	800R.G.B X 600
(2)	Module Size(mm)	189.75 (W) X 149.4 (H) X 5.0 (D)
(3)	Active Area(mm)	170.4(W) X 127.8(H)
(4)	Pixel Pitch(mm)	0.213(W) X 0.213(H)
(5)	LCD / Polarizer Model	TFT , Transmissive , Normally White , Anti-Glare ,Hard coating
(6)	LED Backlight Color	White
(7)	Viewing Direction	6 O' clock Horizontal : Right side 75°(typ.), Left side 75°(typ.) Vertical : Up side 70°(typ.), Down side 60°(typ.)
(8)	Gray Scale Inversion Direction	12 O' clock
(9)	Color Configuration	R.G.B Vertical Stripe
(10)	Interface	LVDS
(11)	Module Weight(g)	250g±5%

3. OUTLINE DIMENSIONS



4. INTERFACE PIN CONNECTION

4.1 TFT LCM Module

FPC Down Connector, (FH28-60S-0.5SH (HIROSE or equivalent), 60pin, pitch = 0.5mm)

Pin NO.	Symbol	I/O	Description	Remark
1	AGND	P	Analog Ground	-
2	AVDD	P	Analog Power	-
3	VCC	P	Digital Power	-
4	R0	I	Data Input (LSB)	
5	R1	I	Data Input	-
6	R2	I	Data Input	-
7	R3	I	Data Input	-
8	R4	I	Data Input	-
9	R5	I	Data Input	
10	R6	I	Data Input	-
11	R7	I	Data Input (MSB)	-
12	G0	I	Data Input (LSB)	-
13	G1	I	Data Input	-
14	G2	I	Data Input	-
15	G3	I	Data Input	-
16	G4	I	Data Input	-
17	G5	I	Data Input	-
18	G6	I	Data Input	-
19	G7	I	Data Input (MSB)	-
20	B0	I	Data Input (LSB)	-
21	B1	I	Data Input	-
22	B2	I	Data Input	-
23	B3	I	Data Input	-
24	B4	I	Data Input	-
25	B5	I	Data Input	-
26	B6	I	Data Input	-
27	B7	I	Data Input (MSB)	-
28	DCLK	I	Clock input	-
29	DE	I	Data Enable signal	-
30	HSD	I	Horizontal sync input. Negative polarity	-

Pin NO.	Symbol	I/O	Description	Remark
31	VSD	I	Vertical sync input. Negative polarity	-
32	MODE3	I	DE/SYNC mode select. Normally pull high H : DE mode L : HSD/VSD mode	-
33	RSTB	I	Global reset pin. Active low to enter reset state. suggest to connecting with an RC reset circuit for stability .normally pull high.	-
34	STBYB	I	Standby mode, normally pull high STBYB="1",normal operation STBYB="0",timming control , source driver will turn off, all output are high-Z	-
35	SHLR	I	Source right or left sequence control .SHLR="L", shift left: last data=S1<-S2...S1200=first data ; SHLR="H", shift right :first data=S1->S2...S1200=last data	-
36	VCC	P	Digital Power	-
37	UPDN	I	gate up or down scan control. UPDN="L" , DOWN shift : G1->G2...->G600 ; UPDN="H", up shift: G1<-G2...<-G600	-
38	GND	P	Digital Ground	-
39	AGND	P	Analog Ground	-
40	AVDD	P	Analog Power	-
41	VCOM	I	For external VCOM DC input (Adjustable)	
42	DITH	I	Dithering setting DITH="H" 6bit resolution (last 2 bits of input data truncated) (default setting)	
43、44	NC	-	Not connect	
45	V10	P	Gamma correction voltage reference	
46	V9	P	Gamma correction voltage reference	
47	V8	P	Gamma correction voltage reference	
48	V7	P	Gamma correction voltage reference	
49	V6	P	Gamma correction voltage reference	
50	V5	P	Gamma correction voltage reference	
51	V4	P	Gamma correction voltage reference	
52	V3	P	Gamma correction voltage reference	
53	V2	P	Gamma correction voltage reference	
54	V1	P	Gamma correction voltage reference	

